


Aumictech

Test & measurement • bought, sold, repaired, calibrated

Data Sheet

August 13, 2026

ANRITSU

ANRITSU MP1763B Error Detector 5-12 Ghz opt 01

Anritsu MP1763B Pulse Pattern Generator 50 MHz to 12.5 GHz The Anritsu MP1763B is a high-speed pulse pattern generator covering 50 MHz to 12.5 GHz for R&D of high-speed logic, ICs, optical modules, and digital systems. It is typically paired with the MP1764A error detector for BERTS applications. Clock and data amplitudes are variable with adjustable offset, a variable clock delay supports phase-margin tests, and PRBS or programmable patterns (including STM-64 frame-length class patterns) are available. Option 01 adds a built-in 12.5 GHz clock synthesizer; GPIB and a 3.5-inch floppy support automation and recall. The Anritsu MP1763B is a high-speed pulse pattern generator covering 50 MHz to 12.5 GHz for R&D of high-speed logic, ICs, optical modules, and digital systems. It is typically paired with the MP1764A error detector for BERTS applications. Clock and data amplitudes are variable with adjustable offset, a variable clock delay supports phase-margin tests, and PRBS or programmable patterns (including STM-64 frame-length class patterns) are available. Option 01 adds a built-in 12.5 GHz clock synthesizer; GPIB and a 3.5-inch floppy support automation and recall.



MODEL

Anritsu MP1763B

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

MP1763B

LISTING

<https://aumictechlabs.com/products/mp1763b-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Anritsu MP1763B Error Detector with Option 01 is a specialized instrument for bit error rate (BER) testing across the 5 to 12 GHz frequency spectrum. It quantifies signal integrity in high-

speed digital communication systems by detecting and counting bit errors in real time. This device serves telecommunications, satellite communications, and radar system development environments where verification of signal quality and reliability is critical.

Technical Specifications

Frequency Range: 5 GHz to 12 GHz

Error Detection Capabilities: Bit error rate (BER) measurement with real-time calculation and display

Support for various standard and user-defined test patterns

Multiple error detection modes for high-speed digital interfaces

Performance Characteristics:

- Optimized sensitivity for low-level error signal detection in high-frequency bands
- Wide operational bandwidth across the 5–12 GHz range
- High measurement accuracy and repeatability

Interfaces:

- RF input: High-frequency coaxial connector rated for 5–12 GHz operation
- Data input for receiving test data streams
- Reference clock input for external synchronization
- Control interface for remote operation and automation
- Data and error output interfaces for measured results

Physical Form: Standard rack-mount or benchtop configuration

Key Features

- Real-time BER calculation and measurement display
- Flexible test pattern capability (standard and custom patterns)
- Multiple error detection schemes for diverse digital interface standards
- High-frequency RF connectivity optimized for 5–12 GHz signals
- External clock synchronization for precision timing
- Automated measurement and data reporting

Typical Applications

- Microwave and millimeter-wave transceiver validation
- Satellite communication link integrity assessment
- Radar system signal quality verification
- High-speed digital interface characterization
- RF component and subsystem testing

Compatibility & Integration

The MP1763B integrates with standard laboratory and production test environments through multiple control and data interfaces, supporting both manual operation and automated test sequences.

Features & description

From manufacturer datasheet

Features

- Frequency range 50 MHz to 12.5 GHz • Built in clock source with Option 01 • Variable clock and data amplitude 0.25 to 2 Vp-p class • Offset adjust within 2 V class • Variable clock delay for phase margin
 - PRBS and programmable patterns to 8 Mb class • Complementary outputs • GPIB and floppy disk for presets
-

Applications

- High-speed IC and ASIC BER stimulus to 12.5 Gb/s • Optical module and transmission equipment evaluation • Phase margin and amplitude margin testing • STM-64 class pattern generation • Paired BERTS with MP1764A error detector • Automated GPIB production screening

MP1763B Characteristics / Specifications

PARAMETER	VALUE
Model	MP1763B
Manufacturer	Anritsu
Instrument Type	Pulse Pattern Generator
Alias	MP1763B; Anritsu MP1763B
Frequency range	50 MHz to 12.5 GHz
Maximum bit rate	12.5 Gb/s
Data output channels	2
Clock output channels	3
Output connector	SMA
Clock and data amplitude range	0.25 to 2 Vp-p
Offset adjust range	within 2 V
Rise fall time class	30 ps or less (20 to 80 percent) family
Programmable pattern length max	8 Mb
PRBS patterns	M series pseudorandom
STM64 frame pattern class	six frames of STM-64 length support
Complementary outputs	yes
Floppy disk drive	3.5 inch built in
Remote control	GPIB
Related error detector	MP1764A
Related later PPG	MP1763C 50 Mbit/s to 12.5 Gbit/s
Primary use	12.5 Gb/s pulse pattern generation
Form factor	benchtop PPG
Variable delay	internal clock delay for timing characterization
Pattern types	PRBS and programmable cell data
Impedance class	50 ohm outputs

PARAMETER	VALUE
Mark ratio	selectable with PRBS modes
Error free stimulus pair	MP1763B plus MP1764A BERTS
Product family	Anritsu MP176x PPG/ED
Operating mode continuous	yes
Burst capability class	supported in family BERTS workflows Notes Specs from Anritsu MP1763B product listings and MP1763C/64 family datasheet context (50 MHz–12.5 GHz; Opt 01 synthesizer; amplitude/offset). Confirm Option 01 presence, exact rise/fall, and ED pairing on the unit. Prefer MP1763C datasheet numbers when B-revision faceplate differs.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	MP1763B
Manufacturer	Anritsu
Instrument Type	Pulse Pattern Generator
Alias	MP1763B; Anritsu MP1763B
Frequency range	50 MHz to 12.5 GHz
Maximum bit rate	12.5 Gb/s
Data output channels	2
Clock output channels	3
Output connector	SMA
Clock and data amplitude range	0.25 to 2 Vp-p
Offset adjust range	within 2 V
Rise fall time class	30 ps or less (20 to 80 percent) family
Programmable pattern length max	8 Mb
PRBS patterns	M series pseudorandom
STM64 frame pattern class	six frames of STM-64 length support
Complementary outputs	yes
Floppy disk drive	3.5 inch built in
Remote control	GPIB
Option 01: built in 12.5 GHz synthesizer clock source	
Specifications	
PARAMETER	VALUE
Related error detector	MP1764A
Related later PPG	MP1763C 50 Mbit/s to 12.5 Gbit/s
Primary use	12.5 Gb/s pulse pattern generation
Form factor	benchtop PPG

PARAMETER	VALUE
Variable delay	internal clock delay for timing characterization
Pattern types	PRBS and programmable cell data
Impedance class	50 ohm outputs
Mark ratio	selectable with PRBS modes
Error free stimulus pair	MP1763B plus MP1764A BERTS
Product family	Anritsu MP176x PPG/ED
Operating mode continuous	yes
Burst capability class	supported in family BERTS workflows

Notes

Specs from Anritsu MP1763B product listings and MP1763C/64 family datasheet context (50 MHz–12.5 GHz; Opt 01 synthesizer; amplitude/offset). Confirm Option 01 presence, exact rise/fall, and ED pairing on the unit. Prefer MP1763C datasheet numbers when B-revision faceplate differs.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 13, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.