

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

Data Sheet

August 15, 2026

ACTIVE SILICON

Active Silicon AS-PHX-D24CL-PE1 Camera Link Frame Grabber

The Active Silicon AS-PHX-D24CL-PE1 is a Camera Link frame grabber designed for high-performance image acquisition. It supports a wide range of Camera Link cameras, providing a reliable interface for demanding applications. This frame grabber ensures high-speed data transfer and efficient image processing.



MODEL

Active Silicon AS-PHX-D24CL-PE1

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

AS-PHX-D24CL-PE1

LISTING

<https://aumictechlabs.com/products/as-phx-d24cl-pe1-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Active Silicon AS-PHX-D24CL-PE1 is a PCI Express Camera Link frame grabber for high-speed image acquisition in industrial and scientific imaging systems. Built on PCIe 1.1 x1 architecture with burst rates exceeding 190 Mbytes/sec, this board delivers direct hardware control of image data streaming with zero CPU overhead via scatter-gather DMA and Posted Writes. The board conforms to Camera Link v1.2 specification and accommodates Base Camera Link formats including 8 to 16-bit single-tap, 8-bit RGB, and dual-tap 12-bit camera sources at pixel clock rates up to 85 MHz. Technical Specifications Interface: PCI Express 1.1, single lane (x1) PCIe Burst Rate: Exceeds 190 Mbytes/sec (subject to host performance) Address Space: 16 Mbytes for address space; 256 bytes for I/O space Camera Link Specification: v1.2 Maximum

Pixel Clock: 85 MHz Counter Timers: Four 32-bit units I/O Opto-Isolated Outputs: Sink up to 20 mA from 24 V supply I/O Opto-Isolated Inputs: Sense 3.3 V to 24 V logic high Differential Input Ports: Two 2-bit EIA-644 (LVDS) ports TTL I/O Ports: Two 8-bit ports

Key Features

- Supports all Base Camera Link tap configurations including line interlaced, line offset, pixel interlaced, and pixel offset formats
- Power over Camera Link (PoCL) with SafePower for camera powering via Camera Link cable
- Hardware scatter-gather DMA engine for efficient data transfer
- FPGA-based processing with Region of Interest and sub-sampling controls
- Look-Up Table functionality for gamma correction, dynamic range cropping, and binary thresholding
- UART with EIA-644 signaling accessible as standard Windows COM port
- Configurable event-driven interrupts for frame/line timing and FIFO status
- DataMapper functionality for acquired data mapping and packing

Compatibility & Integration

Supports both Power over Camera Link-enabled and conventional non-PoCL cameras. Optional AS-PHX-ADP-50MD-IO interface adapter provides 50-way mini D connector access to I/O signals. PHX SDK available for development and integration.

ASPHXD24CLPE1 Characteristics / Specifications

PARAMETER	VALUE
Model	AS-PHX-D24CL-PE1
Instrument Type	Camera Link frame grabber
Family	Active Silicon Phoenix
Interface	Camera Link
Host Bus	PCI Express PE1 class
Channel Class	D24CL 24-bit / dual-channel class
Manufacturer	Active Silicon
Literature	Active Silicon Phoenix AS-PHX-D24CL-PE1 listings
System Role	PCIe image acquisition
Use Case	machine vision
Sibling	Phoenix-D36-PE1 LVDS digital frame grabber
Form Factor	PCI Express add-in
Documentation Type	AS-PHX-D24CL-PE1 listing
Brand Lineage	Active Silicon Phoenix
Application Class	Camera Link grabber
Related	Phoenix frame-grabber family
Connector Class	Camera Link plus PCIe
Host Class	PCIe host
Year Class	AS-PHX-D24CL-PE1
Ate Class	vision acquisition
Module Class	frame grabber
Pixel Class	Camera Link pixel pipeline
Buffer Class	on-board frame buffer (data sheet)
Software Class	Phoenix SDK class

ASPHXD24CLPE1 Specifications

PARAMETER	VALUE
Phoenix Camera Link PCIe frame grabber	D24CL 24-bit / dual-channel class
PE1 PCI Express x1 class	Sibling Phoenix-D36-PE1 LVDS
Technical Specifications	Model: AS-PHX-D24CL-PE1

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	AS-PHX-D24CL-PE1
Instrument Type	Camera Link frame grabber
Family	Active Silicon Phoenix
Interface	Camera Link
Host Bus	PCI Express PE1 class
Channel Class	D24CL 24-bit / dual-channel class
Manufacturer	Active Silicon
Literature	Active Silicon Phoenix AS-PHX-D24CL-PE1 listings
System Role	PCIe image acquisition
Use Case	machine vision
Sibling	Phoenix-D36-PE1 LVDS digital frame grabber
Form Factor	PCI Express add-in
Documentation Type	AS-PHX-D24CL-PE1 listing
Brand Lineage	Active Silicon Phoenix
Application Class	Camera Link grabber
Related	Phoenix frame-grabber family
Connector Class	Camera Link plus PCIe
Host Class	PCIe host
Year Class	AS-PHX-D24CL-PE1
Ate Class	vision acquisition
Module Class	frame grabber
Pixel Class	Camera Link pixel pipeline
Buffer Class	on-board frame buffer (data sheet)
Software Class	Phoenix SDK class

Notes

From Active Silicon Phoenix AS-PHX-D24CL-PE1 listings. Pixel clock, Camera Link mode, and buffer size are on the Phoenix D24CL data sheet; do not invent them.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.