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Data Sheet

August 15, 2026

ADLINK

Adlink PXI-3950-0040 3U PXI System Controller

The Adlink PXI-3950-0040 is a 3U PXI system controller that integrates CPU, memory, and storage directly within the PXI chassis, eliminating external PC dependencies for measurement and automation applications. Built around an Intel Core2 Duo T7500 processor running at 2.2 GHz, this embedded controller delivers deterministic performance in hybrid PXI-based test systems. The unit ships with 4 GB of 667 MHz DDR2 SDRAM, expandable via one SO-DIMM socket, and includes a 320 GB SATA hard drive at 7200 RPM, with optional CompactFlash support for alternative storage configurations. Compatibility & Integration Designed for PXI chassis environments with compatible rear transition modules. Supports LXI instrument communication via dedicated Ethernet port and legacy GPIB control via integrated IEEE488 controller. Aumictech offers NIST-traceable calibration and repair for the Adlink PXI-3950-0040.



MODEL

Adlink PXI-3950-0040

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

PXI-3950-0040

LISTING

<https://aumictechlabs.com/products/pxi-3950-0040-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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The unit ships with 4 GB of 667 MHz DDR2 SDRAM, expandable via one SO-DIMM socket, and includes a 320 GB SATA hard drive at 7200 RPM, with optional CompactFlash support for alternative storage configurations.

Technical Specifications

Processor & Memory Intel Core2 Duo T7500 @ 2.2 GHz with 800 MHz Front Side Bus Intel GME965 Graphic Memory Controller Hub chipset 4 GB 667 MHz DDR2 SDRAM (dual-channel capable) One DDR2 SO-DIMM socket for memory expansion 320 GB SATA hard drive, 7200 RPM Type II CompactFlash socket for storage alternatives Programmable watchdog timer for system reliability

Display & Graphics Intel GMA X3100 graphics accelerator DVI-I connector supporting digital output up to 1600 × 1200 @ 60 Hz or analog CRT up to 1280 × 1024 Single 18-bit LVDS channel to rear transition module

Connectivity Dual Gigabit Ethernet ports (RJ-45) Four USB 2.0 ports on faceplate On-board IEEE488 GPIB controller with Micro-D 25-pin connector Two RS-232/422/485 configurable 16C550 UART compatible COM ports High-definition audio input/output jacks SMB trigger I/O connector for PXI trigger bus routing

Physical & Environmental Dimensions: 60.5 mm × 128.7 mm × 213.2 mm Weight: 0.9 kg Occupies 1 system slot plus 2 controller expansion slots Operating temperature: 0°C to 50°C Storage temperature: -20°C to 80°C Relative humidity: 5% to 95% non-condensing Shock rating: 30 G half-sine, 11 ms pulse Operating vibration: 5 to 500 Hz, 0.21 Grms (3 axes) Non-operating vibration: 5 to 500 Hz, 2.46 Grms (3 axes) FCC Class A and EN 61326-1 compliant

Key Features PXI specification Rev. 2.2 compliant Windows 7 ×32 operating system support Self-contained embedded platform eliminates external PC requirement Dual Gigabit Ethernet for LAN and LXI instrument control Flexible serial port configuration (RS-232/422/485 by jumper)

Compatibility & Integration Designed for PXI chassis environments with compatible rear transition modules. Supports LXI instrument communication via dedicated Ethernet port and legacy GPIB control via integrated IEEE488 controller.

PXI39500040 Characteristics / Specifications

PARAMETER	VALUE
Model	PXI-3950-0040
Instrument Type	3U PXI system controller
Form Factor	3U PXI
Manufacturer	ADLINK
Series Family	PXI-3950
Configuration Code	0040
Slot Role	PXI system controller slot
Chassis Compatibility	3U PXI chassis
Cpu Class	embedded PC class controller
Memory Class	SODIMM system memory class
Storage Class	onboard or modular storage class
Ethernet Class	Gigabit Ethernet class
Usb Class	multiple USB ports class
Display Class	VGA or DVI class graphics
Operating System Class	Windows embedded class support
Operating Temperature Class	0 C to 50 C class
Storage Temperature Class	minus 20 C to 70 C class
Humidity Class	noncondensing
Pxi Bandwidth Class	PXI system bandwidth per family
Triggering	PXI trigger bus controller role
Star Trigger Class	supported in compatible chassis
Related Base Model	PXI-3950
Watchdog Class	hardware watchdog class
Bios Class	embedded BIOS configuration
Bandwidth	Class: PXI system bandwidth per family

PARAMETER	VALUE
Storage temperature	Class: minus 20 C to 70 C class
Operating temperature	Class: 0 C to 50 C class
Humidity	Class: noncondensing

PXI39500040 Specifications

PARAMETER	VALUE
Applications	PXI automated test system control, multi-slot PXI chassis masters, and compact 3U instrumentation controllers.
Key Features	3U PXI system controller
Technical Specifications	Model: PXI-3950-0040

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Storage temperature	Class: minus 20 C to 70 C class
Operating temperature	Class: 0 C to 50 C class
Operating Temperature	Class: 0 C to 50 C class
Humidity	Class: noncondensing
Operating Temperature Class	0 C to 50 C class
Storage Temperature Class	minus 20 C to 70 C class
Humidity Class	noncondensing
Pxi Bandwidth Class	PXI system bandwidth per family
Triggering	PXI trigger bus controller role
Star Trigger Class	supported in compatible chassis
Related Base Model	PXI-3950
Watchdog Class	hardware watchdog class
Bios Class	embedded BIOS configuration Notes From ADLINK PXI-3950-0040 catalog listing as a 3U PXI system controller configuration. Exact CPU speed memory and storage follow the 0040 dash-code datasheet row.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes

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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.