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Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 1651A

Agilent Keysight 1651A Logic Analyzer 32 Channel The HP 1651A is a 32-channel member of the 1650-series logic analyzers. OEM 1650/1651 literature lists 32 channels, 25 MHz state, and 100 MHz timing (1650A is the 80-channel sibling). Microprocessor and digital timing/state debug on HP 1650-series benchtop analyzers. Instrument Type: Standalone logic analyzer The HP 1651A is a 32-channel member of the 1650-series logic analyzers. OEM 1650/1651 literature lists 32 channels, 25 MHz state, and 100 MHz timing (1650A is the 80-channel sibling). Microprocessor and digital timing/state debug on HP 1650-series benchtop analyzers. Instrument Type: Standalone logic analyzer Display: CRT state/timing listings and waveforms



MODEL

**Agilent / Keysight
1651A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1651A

LISTING

[https://aumictechlabs.c
om/products/1651a-2/](https://aumictechlabs.com/products/1651a-2/)

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30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Agilent/Keysight 1651A is a 32-channel logic analyzer engineered for state and timing analysis of digital circuits and microprocessor-based systems. The instrument delivers flexible configuration options - single state analyzer, single timing analyzer, or concurrent state and timing operation - making it adaptable to diverse debugging workflows. With integrated pattern generation, oscilloscope capability, and comprehensive triggering, the 1651A isolates complex digital faults across 8-, 16-, and 32-bit processors. Technical Specifications Analysis Channels & Memory 32 analysis channels with 1k deep memory State analysis: 25 MHz maximum clock rate Timing analysis: 100 MHz maximum clock rate Glitch detection on all channels; minimum

detectable glitch width 5 ns at threshold Transitional timing on all channels for capture of infrequent events Triggering & Display Complex state triggering: sequence levels, pattern recognizers, range recognizers, storage qualification State display modes: Compare, Chart, Waveform Pattern Generation & Oscilloscope Up to 204 channels of pattern generation at 50 Mbit/s Up to 8 channels digitizing oscilloscope capability at 400 Msample/s Microprocessor SupportCompatible with most popular 8-, 16-, and 32-bit microprocessors. Preprocessor interfaces available for standard CPUs; user-definable interface (HP 10320C) for custom architectures.Key Features Interactive user interface with Auto-Scale for rapid timing analyzer and oscilloscope setup Rotary knob control for quick value entry and marker positioning Built-in disc drive for setup storage and measurement archival RS-232C and HP-IB interfaces for programming and data transfer External trigger BNC connector Compact 22 lb footprint with built-in handle; optional soft carrying caseTypical Applications Microprocessor and digital system debugging State sequence and timing correlation analysis Glitch detection and transient event capture Real-time pattern generation and stimulusCompatibility & Integration The 1651A integrates into the HP 16500A Logic Analysis System (five card slots with Intermodule Bus for time-correlated multi-card measurements). Companion software converts acquired state data to microprocessor mnemonics. HP 10390A System Performance Analysis software provides State Histogram, State Overview, and Time Interval measurements.

1651A Characteristics / Specifications

PARAMETER	VALUE
Model	1651A
Instrument Type	Standalone logic analyzer
Series Family	HP 1650/1651/1652/1653
Logic Channels	32
State Speed	25 MHz
Timing Speed	100 MHz
Form Factor	benchtop logic analyzer
Display	CRT state/timing listings and waveforms
Storage	3.5 inch floppy class of 1650-series
Remote Class	HP-IB on optioned 1650-series units
Trigger	pattern, edge, and sequence triggering class
Probing	1650-series pod and grabber leads
Application	8/16-bit microprocessor debug
User Interface	dedicated keypad and CRT menus
Documentation	HP 1650/1651/1653 operating and service literature
Operating Environment	laboratory ambient per 165x manual
Power	AC mains operated
Clocking	state clocks and qualifiers class of 1650-series
Glitch	glitch capture class on timing analyzer
Compare	trace compare class
Print	HP-IB printer support class
Oscilloscope	not included (see 1653B for 32-channel plus scope)
Sibling 1650A	80 channels, 25 MHz state, 100 MHz timing

1651A Specifications

PARAMETER	VALUE
Agilent Keysight 1651A Logic Analyzer 32 Channel	Overview
Applications	Microprocessor and digital timing/state debug on HP 1650-series benchtop analyzers.
Key Features	32 channels
Technical Specifications	Model: 1651A
Notes	Channel count, 25 MHz state, and 100 MHz timing from HP 1650-series product literature for 1651A. Oscilloscope rows apply only when listed.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.