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Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 1660A

Agilent Keysight 1660A Logic Analyzer 136 Channel 100 MHz State 500 MHz Timing The HP 1660A is a 1660-series standalone logic analyzer. OEM 1660-series technical data (5963-2172) and the 1660-series user's reference list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 136 state/timing channels. Digital hardware debug, microprocessor bus trace, and timing/glitch analysis. 100 MHz state / 500 MHz half-channel timing The HP 1660A is a 1660-series standalone logic analyzer. OEM 1660-series technical data (5963-2172) and the 1660-series user's reference list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 136 state/timing channels. Digital hardware debug, microprocessor bus trace, and timing/glitch analysis. 100 MHz state / 500 MHz half-channel timing Graphical trigger macros and inverse assemblers



MODEL

**Agilent / Keysight
1660A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1660A

LISTING

[https://aumictechlabs.c
om/products/1660a-2/](https://aumictechlabs.com/products/1660a-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Agilent/Keysight 1660A is a logic analyzer for digital system debugging and validation across embedded systems, ASIC development, and digital design. It captures and analyzes complex digital signals using state and timing analysis modes. Technical Specifications Channel Configuration Available in 34, 68, 102, and 136 logic analyzer channels. Half-channel modes support expanded memory configurations. State Analysis Sampling rate: 100 MHz Memory: 4K samples per channel (8K in half-channel modes) State clocks/qualifiers: 2, 4, or 6 (varies by

model) Timing Analysis Conventional timing: 250 MHz (all channels) / 500 MHz (half channels)
Transitional timing: 125 MHz (all channels) / 250 MHz (half channels) Glitch timing: 125 MHz (half channels)
Timing resolution: 2 ns Display & Interface Gray-scale CRT graphical display (later models feature VGA color flat-panel). Advanced triggering with graphical trigger macros. The 1660AS series integrates a 2-channel oscilloscope with 1 GSa/s sampling rate and 8K samples per channel for capturing non-repeating events. Storage Selected models include 2 GB hard drive and 1.44 MB floppy drive.

Key Features Simultaneous state and timing analysis Graphical trigger macros for simplified configuration Front-panel keypad, mouse (standard on most models), optional keyboard Half-channel modes enable higher timing rates on reduced channel count Glitch detection at timing frequencies

Typical Applications Embedded system debugging ASIC development and validation Digital design verification High-speed signal capture and analysis

Compatibility & Integration Fully programmable via HP-IB and RS-232 interfaces supporting IEEE 488.2. Centronix parallel printer port standard on 1664A model; RS-232 and HP-IB optional on that variant.

1660A Characteristics / Specifications

PARAMETER	VALUE
Model	1660A
Instrument Type	Standalone logic analyzer
Series Family	HP 1660 A/C/E series
State And Timing Channels	136
State Speed	100 MHz, all channels
State Speed Alternate Listing	50 MHz also listed on the 1660-series characteristics table
Conventional Timing All Channels	250 MHz
Conventional Timing Half Channels	500 MHz
Transitional Timing All Channels	125 MHz
Transitional Timing Half Channels	250 MHz
Glitch Timing	125 MHz half channels
State Clocks Qualifiers	6
Memory Depth Full Channel	4K samples per channel
Memory Depth Half Channel	8K samples per channel
Channel Count Full Half	136/68
State Sequence Levels	12 sequence levels with two-way branching (1660A user reference)
State Pattern Terms	10 pattern resource terms, 2 range terms, 2 timers
Timing Sequence Levels	10 sequence levels with two-way branching
Timing Edge Terms	2 edge terms plus 2 timers (timing analyzer)
Remote Interface	HP-IB and RS-232C standard (except 1664A notes)
Disk	3.5 inch high-density floppy, LIF and DOS
Weight Family	26 lb (11.8 kg)
Width	17.3 in (440 mm) family outline
Height	8.1 in (205 mm) family outline
Depth	14.5 in (367 mm) family outline

PARAMETER	VALUE
Display	gray-scale CRT

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

technical data (5963-2172) and the 1660-series user's reference list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 136 state/timing channels.

Applications
Digital hardware debug, microprocessor bus trace, and timing/glitch analysis.

Key Features
136 state/timing channels
100 MHz state / 500 MHz half-channel timing
4K memory (8K half-channel)
Graphical trigger macros and inverse assemblers
HP-IB and RS-232 programmability

Technical Specifications

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Notes
Channel, timing, state, memory, and (where listed) oscilloscope rows from HP 1660-Series Technical Data 5963-2172 and HP 1660-Series User's Reference 01660-90904. CS/ES add color display relative to A/AS; confirm E-series memory options on the 1660E label if present.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.