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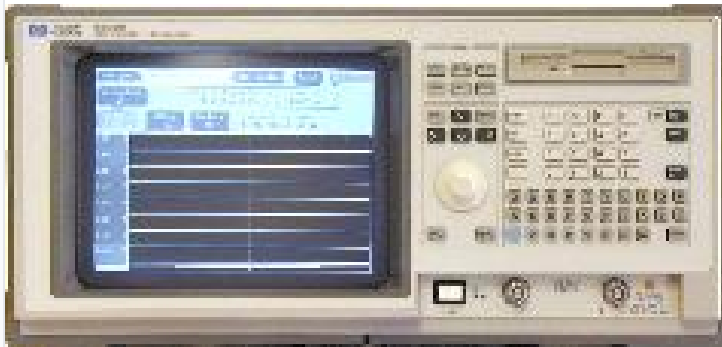
Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 1660CS

Agilent Keysight 1660CS Logic Analyzer 136 Channel Color with Oscilloscope The HP 1660CS is a 1660-series standalone logic analyzer. OEM 1660-series technical data (5963-2172) and the 1660-series user's reference list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 136 state/timing channels plusabuilt-in 2-channel 1 GSa/s 250 MHz oscilloscope. Digital hardware debug, microprocessor bus trace, and timing/glitch analysis with simultaneous analog capture on the built-in scope. 100 MHz state / 500 MHz half-channel timing The HP 1660CS is a 1660-series standalone logic analyzer. OEM 1660-series technical data (5963-2172) and the 1660-series user's reference list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 136 state/timing channels plusabuilt-in 2-channel 1 GSa/s 250 MHz oscilloscope. Digital hardware debug, microprocessor bus trace, and timing/glitch analysis with simultaneous analog capture on the built-in scope. 100 MHz state / 500 MHz half-channel timing Graphical trigger macros and inverse assemblers



MODEL

**Agilent / Keysight
1660CS**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1660CS

LISTING

<https://aumictechlabs.com/products/1660cs-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight 1660CS is a portable benchtop logic analyzer that combines state and timing analysis with an integrated 2-channel oscilloscope for comprehensive digital system debugging. Engineers use it to identify and resolve hardware and software issues in embedded systems through simultaneous capture and display of state, timing, and oscilloscope data - pinpointing

design errors that single-domain tools miss.

Technical Specifications

Logic Analysis 136 state and timing channels State analysis to 100 MHz; conventional timing to 250 MHz (all channels) or 500 MHz (half channels) Transitional timing to 125 MHz (all channels) or 250 MHz (half channels) Memory: 4 KB per channel (normal) or 8 KB per channel (half channels) Timing resolution down to 2 ns Glitch detection minimum width: 3.5 ns 2 state clocks/qualifiers 12-level trigger sequencing for state analysis; 10 levels for timing Integrated Oscilloscope 2 channels, 250 MHz bandwidth 1 GSa/s maximum sample rate per channel 8k samples memory per channel 1.4 ns rise time; 8-bit vertical resolution Triggering & Analysis Edge, voltage level, and pattern duration triggering with automatic pulse parameters System Performance Analysis: state histograms, state overview, time interval analysis Time-correlated measurements across all three domains Processor-specific mnemonic display for state data Record length to 8000 pt/sec

Key Features

Symbolic Download Utility (E2450A) extracts symbolic information from object modules for source-line referencing Source code correlation via B3740A software analyzer over LAN for C++ and assembly debugging 540 MB hard disk drive with 3.5-inch high-density floppy (DOS and LIF formats)

Typical Applications Embedded system development, signal integrity validation, hardware-software integration verification, and complex timing closure analysis.

Compatibility & Integration HP-IB, RS-232C, and Centronics printer interfaces standard Optional Ethernet LAN port (standard on CP model; Option 015 for C/CS models) Optional ThinLAN and EtherWIST LAN ports for remote connectivity

1660CS Characteristics / Specifications

PARAMETER	VALUE
Model	1660CS
Instrument Type	Standalone logic analyzer
Series Family	HP 1660 A/C/E series
State And Timing Channels	136
State Speed	100 MHz, all channels
State Speed Alternate Listing	50 MHz also listed on the 1660-series characteristics table
Conventional Timing All Channels	250 MHz
Conventional Timing Half Channels	500 MHz
Transitional Timing All Channels	125 MHz
Transitional Timing Half Channels	250 MHz
Glitch Timing	125 MHz half channels
State Clocks Qualifiers	6
Memory Depth Full Channel	4K samples per channel
Memory Depth Half Channel	8K samples per channel
Channel Count Full Half	136/68
State Sequence Levels	12 sequence levels with two-way branching (1660A user reference)
State Pattern Terms	10 pattern resource terms, 2 range terms, 2 timers
Timing Sequence Levels	10 sequence levels with two-way branching
Timing Edge Terms	2 edge terms plus 2 timers (timing analyzer)
Remote Interface	HP-IB and RS-232C standard (except 1664A notes)
Disk	3.5 inch high-density floppy, LIF and DOS
Weight Family	26 lb (11.8 kg)
Width	17.3 in (440 mm) family outline
Height	8.1 in (205 mm) family outline
Depth	14.5 in (367 mm) family outline

PARAMETER	VALUE
Oscilloscope Channels	2
Oscilloscope Sample Rate	1 GSa/s per channel (1660AS/CS table)
Oscilloscope Bandwidth	dc to 250 MHz (dc coupled)
Oscilloscope Rise Time	1.4 ns
Oscilloscope Vertical Resolution	8 bits
Oscilloscope Memory	8k samples per channel
Display	color CRT
Bandwidth	dc to 250 MHz (dc coupled)
Rise Time	1.4 ns

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

technical data (5963-2172) and the 1660-series user's reference list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 136 state/timing channels plusabuilt-in 2-channel 1 GSa/s 250 MHz oscilloscope.

Applications

Digital hardware debug, microprocessor bus trace, and timing/glitch analysis with simultaneous analog capture on the built-in scope.

Key Features

136 state/timing channels

100 MHz state / 500 MHz half-channel timing

4K memory (8K half-channel)

Graphical trigger macros and inverse assemblers

HP-IB and RS-232 programmability

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Display	color CRT

Notes

Channel, timing, state, memory, and (where listed) oscilloscope rows from HP 1660-Series Technical Data 5963-2172 and HP 1660-Series User's Reference 01660-90904. CS/ES add color display relative to A/AS; confirm E-series memory options on the 1660E label if present.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 14, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.