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Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 1662CS

Agilent Keysight 1662CS Logic Analyzer 68 Channel The HP 1662CS is a 1660-series standalone logic analyzer with 68 state/timing channels. OEM 1660-series technical data list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 4 state clocks/qualifiers plus a built-in 2-channel oscilloscope. Digital hardware debug, microprocessor bus trace, and timing/glitch analysis with simultaneous analog capture on the built-in scope. 100 MHz state / 500 MHz half-channel timing The HP 1662CS is a 1660-series standalone logic analyzer with 68 state/timing channels. OEM 1660-series technical data list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 4 state clocks/qualifiers plus a built-in 2-channel oscilloscope. Digital hardware debug, microprocessor bus trace, and timing/glitch analysis with simultaneous analog capture on the built-in scope. 100 MHz state / 500 MHz half-channel timing Graphical trigger macros and inverse assemblers

**MODEL****Agilent / Keysight
1662CS****CALIBRATION****Available on
request****CONDITION****Used****STATUS****In stock****RFQ / SKU****1662CS****LISTING**<https://aumictechlabs.com/products/1662cs-2/>

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NAICS 423690 • 811210 • 541380

This unit

The Agilent/Keysight 1662CS is a benchtop logic analyzer that combines state and timing analysis with an integrated 2-channel oscilloscope for debugging and validation of digital systems. Engineers can perform simultaneous logic and analog measurements, capture state transitions and timing violations, and correlate captured data with processor mnemonics and C++ source code for accelerated fault isolation. Technical Specifications Logic Analysis: 68 state and timing

channels State analysis speed: 100 MHz with 2 state clocks/qualifiers Timing analysis speed: 500 MHz Memory depth: 4k states per channel; 8k states in half-channel mode Trigger sequencing: 12 levels (state), 10 levels (timing) Integrated Oscilloscope: 2 channels Sampling rate: 1 GSa/s Bandwidth: 250 MHz Vertical resolution: 8 bits Triggering: Logic analyzer-controlled Memory: 8000 samples per channel Storage & Connectivity: Internal 540 MB hard disk drive and 3.5-inch floppy disk drive Ethernet LAN for remote PC/workstation access HP-IB, RS-232-C, and Centronics printer interfaces Probing: Lightweight miniprobes with high-impedance options Over 200 probe solutions for diverse signal connections Interposers available for minimal loading during signal integrity and protocol measurements

Key Features Processor-specific mnemonics and inverse assembler support for annotated state listings Source line referencing via HP B3740A software analyzer - correlates captured state data directly with C++ source code Analog parametric measurements alongside digital capture Gray-scale graphical display interface Portable benchtop form factor

Typical Applications Digital system debugging, microprocessor validation, timing analysis, protocol verification, and hardware-software integration testing.

Compatibility & Integration Works with inverse assemblers for processor-specific analysis. Supports HP B3740A analyzer software for source code correlation. LAN connectivity enables data sharing and remote diagnostics from networked engineering workstations.

1662CS Characteristics / Specifications

PARAMETER	VALUE
Model	1662CS
Instrument Type	Standalone logic analyzer
Series Family	HP 1660 A/C/E series
State And Timing Channels	68
Channel Count Full Half	68/34
State Speed	100 MHz, all channels
State Speed Alternate Listing	50 MHz also listed on the 1660-series characteristics table
Conventional Timing All Channels	250 MHz
Conventional Timing Half Channels	500 MHz
Transitional Timing All Channels	125 MHz
Transitional Timing Half Channels	250 MHz
Glitch Timing	125 MHz half channels
State Clocks Qualifiers	4
Memory Depth Full Channel	4K samples per channel
Memory Depth Half Channel	8K samples per channel
State Sequence Levels	12 sequence levels with two-way branching
State Pattern Terms	10 pattern resource terms, 2 range terms, 2 timers
Timing Sequence Levels	10 sequence levels with two-way branching
Remote Interface	HP-IB and RS-232C standard
Disk	3.5 inch high-density floppy, LIF and DOS
Weight Family	26 lb (11.8 kg) on 1660A-class outline; 28.6 lb (13 kg) on 1660E technical data
Width	17.3 in (440 mm) family outline
Height	8.1 in (205 mm) family outline
Depth	14.5 in (367 mm) family outline
Display	color CRT

PARAMETER	VALUE
Power	115 Vac or 230 Vac, minus 22 percent to +10 percent, 48-66 Hz, 320 VA max on 1660E technical data
Oscilloscope Channels	2
Oscilloscope Sample Rate Cs	1 GSa/s per channel on 1660CS technical data 5963-2172
Oscilloscope Bandwidth Cs	dc to 250 MHz (dc coupled) on 1660CS technical data
Oscilloscope Sample Rate Es	2 GSa/s per channel on 1660ES technical data
Oscilloscope Bandwidth Es	dc to 500 MHz on 1660ES technical data
Oscilloscope Rise Time Es	700 ps on 1660ES technical data
Oscilloscope Vertical Resolution	8 bits
Oscilloscope Memory Es	32k samples per channel on 1660ES technical data
Bandwidth	Cs: dc to 250 MHz (dc coupled) on 1660CS technical data
Rise Time	Es: 700 ps on 1660ES technical data

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

technical data list 100 MHz state, 500 MHz half-channel timing, 4K/8K memory, and 4 state clocks/qualifiers plusabuilt-in 2-channel oscilloscope.

Applications

Digital hardware debug, microprocessor bus trace, and timing/glitch analysis with simultaneous analog capture on the built-in scope.

Key Features

68 state/timing channels

100 MHz state / 500 MHz half-channel timing

4K memory (8K half-channel)

Graphical trigger macros and inverse assemblers

HP-IB and RS-232 programmability

Technical Specifications

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Oscilloscope Vertical Resolution	8 bits
Oscilloscope Memory Es	32k samples per channel on 1660ES technical data

Notes

Channel, timing, state, memory, clock, and (where listed) oscilloscope rows from HP 1660-Series Technical Data 5963-2172, HP 1660CS-series user's guide channel table, and HP 1660E/ES technical data 1660E-series sheet. CS/ES add color display andabuilt-in scope; ES scope is 2 GSa/s / 500 MHz on the 1660E-series sheet while CS technical data lists 1 GSa/s / 250 MHz.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.