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Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 16712A

Agilent 16712A State and Timing Module 102 Channel The Agilent 16712A is a 102-channel state/timing module for 16700-series logic analysis systems. OEM 5966-3367E lists 100 MHz maximum state clock, 250/500 MHz conventional timing (full/half channel), 125 MHz transitional timing, and 128K/256K memory (full/half channel). General-purpose and 8/16-bit processor debug in 16700 mainframes needing 102 channels and 128K-class depth. 10 pattern / 2 range / 2 edge-glitch trigger resources The Agilent 16712A is a 102-channel state/timing module for 16700-series logic analysis systems. OEM 5966-3367E lists 100 MHz maximum state clock, 250/500 MHz conventional timing (full/half channel), 125 MHz transitional timing, and 128K/256K memory (full/half channel). General-purpose and 8/16-bit processor debug in 16700 mainframes needing 102 channels and 128K-class depth. 10 pattern / 2 range / 2 edge-glitch trigger resources Instrument Type: State and timing logic analyzer module



MODEL

**Agilent / Keysight
16712A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

16712A

LISTING

<https://aumictechlabs.com/products/16712a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight 16712A is a timing and state analysis module for the 16700 Series logic analysis systems. It delivers simultaneous state and timing analysis through a single probe, eliminating the need for separate probing solutions and enabling fully time-correlated measurements across multiple signal domains. The module captures up to 204 channels simultaneously with 128K samples per channel memory depth, supporting both state and timing

functions on different channels within the same measurement window.

Technical Specifications

State Analysis Clock Rate: 100 MHz Timing Analysis Clock Rate: 500 MHz Memory Depth: 128K samples per channel Maximum Simultaneous Channels: 204 channels Channel Functionality: State or timing analysis per channel

Key Features Time-correlated state and timing analysis on different channels within a single capture Unified probe interface supporting both state and timing measurements Configurable per-channel analysis mode - mix state and timing analysis in one acquisition 128K sample memory enables extended capture windows at operating clock rates

Typical Applications Digital design debugging and validation Cross-domain signal correlation and timing analysis Protocol and bus-level signal verification Multi-clock domain verification

Compatibility & Integration Designed for integration into Keysight 16700 Series mainframes including models 16700A, 16700B, 16701A, 16701B, 16702A, and 16702B. All modules integrate tightly to provide time-correlated measurements across the logic analysis system. The module form factor is a plug-in card (approximately 14 lbs) that installs directly into a compatible mainframe.

16712A Characteristics / Specifications

PARAMETER	VALUE
Model	16712A
Instrument Type	State and timing logic analyzer module
Host Mainframe	Agilent 16700-series logic analysis system
Channels Per Module	102
Maximum State Clock	100 MHz
Conventional Timing Full Half	250 MHz / 500 MHz
Transitional Timing	125 MHz
Memory Depth Full Half	128K / 256K (half-channel timing doubles depth)
Sibling 16710A Memory	8K / 16K
Sibling 16711A Memory	32K / 64K
Maximum Channels Single Time Base	204
Maximum Channels In System	1020
Trigger Patterns	10
Trigger Ranges	2
Trigger Edge Glitch	2
Trigger Timers	2
Trigger Sequence State	12 levels
Trigger Sequence Timing	10 levels
Maximum Trigger Sequence Speed	125 MHz
State Clocks Qualifiers	6
Setup Hold Window	4.0 ns window adjustable from 4.0/0 ns to 0/4.0 ns in 500 ps increments per 34 channels
Threshold Range	TTL, ECL, user-definable plus or minus 6.0 V in 50 mV increments
Related 16716A	68 channels, 167 MHz state, 2 GHz Timing Zoom, 512K/1M
Related 16718A	68 channels, 333 MHz state, 8M/16M

16712A Specifications

PARAMETER	VALUE
Agilent 16712A State and Timing Module 102 Channel	Overview
Applications	General-purpose and 8/16-bit processor debug in 16700 mainframes needing 102 channels and 128K-class depth.
Key Features	102 channels per module
Technical Specifications	Model: 16712A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Instrument Type	State and timing logic analyzer module
Host Mainframe	Agilent 16700-series logic analysis system
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Trigger Sequence Timing	10 levels
Maximum Trigger Sequence Speed	125 MHz
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Setup Hold Window	4.0 ns window adjustable from 4.0/0 ns to 0/4.0 ns in 500 ps increments per 34 channels
Threshold Range	TTL, ECL, user-definable plus or minus 6.0 V in 50 mV increments
Related 16716A	68 channels, 167 MHz state, 2 GHz Timing Zoom, 512K/1M
Related 16718A	68 channels, 333 MHz state, 8M/16M
Notes	
From Agilent State and Timing Modules for Logic Analysis Systems product overview 5966-3367E,	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.