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Data Sheet

August 14, 2026

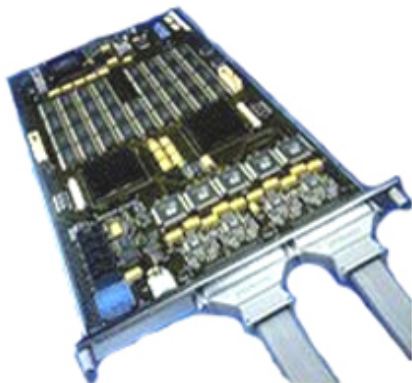
AGILENT / KEYSIGHT

Agilent/Keysight 16716A

Agilent 16716A State and Timing Module 68 Channel The Agilent 16716A is a 68-channel 16700-series module with 167 MHz state, 2 GHz Timing Zoom, 333/667 MHz conventional timing, and 512K/1M memory. OEM 5966-3367E lists VisiTrigger resources (16 patterns, 15 ranges) and a 2.5 ns setup/hold window. 32/64-bit processor debug, timing-margin analysis, and simultaneous 2 GHz Timing Zoom with high-speed state through the same probe.

Instrument Type: State and timing logic analyzer module The Agilent 16716A is a 68-channel 16700-series module with 167 MHz state, 2 GHz Timing Zoom, 333/667 MHz conventional timing, and 512K/1M memory. OEM 5966-3367E lists VisiTrigger resources (16 patterns, 15 ranges) and a 2.5 ns setup/hold window. 32/64-bit processor debug, timing-margin analysis, and simultaneous 2 GHz Timing Zoom with high-speed state through the same probe.

Instrument Type: State and timing logic analyzer module Host Mainframe: Agilent 16700-series logic analysis system



MODEL

**Agilent / Keysight
16716A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

16716A

LISTING

<https://aumictechlabs.com/products/16716a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight 16716A is a logic analyzer module that expands the measurement capabilities of compatible mainframe systems. It delivers dual-speed analysis for comprehensive digital signal characterization, capturing 68 channels of data with deep memory resources to isolate and debug transient events in complex digital designs. Technical Specifications State

Analysis: 167 MHz maximum clock speed Timing Analysis: 2 GHz maximum speed Memory Depth: 512K samples per channel Channel Count: 68 channels per module

Key Features Simultaneous state and timing measurements enable analysis of signal relationships across multiple clock domains Deep sample memory supports extended capture windows for systems with infrequent trigger events High channel density accommodates monitoring of wide data buses and control signal sets without additional modules Module architecture integrates directly into Keysight 16700A series mainframes

Typical Applications Digital system validation and characterization Microprocessor and microcontroller debug Bus protocol analysis and timing verification ASIC and FPGA design verification

Compatibility & Integration The 16716A slots into compatible Agilent/Keysight logic analyzer mainframes, including the 16700A series. Probing integrates with standard Keysight logic analyzer probe solutions configured for the target measurement environment. Mainframe documentation specifies exact compatibility matrices, physical connector requirements, and supported probe types for your specific configuration.

16716A Characteristics / Specifications

PARAMETER	VALUE
Model	16716A
Instrument Type	State and timing logic analyzer module
Host Mainframe	Agilent 16700-series logic analysis system
Channels Per Module	68
Maximum State Clock	167 MHz
Timing Zoom	2 GHz Timing Zoom
Conventional Timing Full Half	333 MHz / 667 MHz
Memory Depth Full Half	512K / 1M (half-channel timing doubles depth)
Maximum Channels Single Time Base	340
Maximum Channels In System	680
Trigger Patterns	16
Trigger Ranges	15
Trigger Edge Glitch	2
Trigger Timers	2 per module minus 1
Global Counters	2
Flags	8
Maximum Trigger Sequence Levels	16
Maximum Trigger Sequence Speed	167 MHz
Trigger Branching	4-way arbitrary IF/THEN/ELSE
State Clocks Qualifiers	4
Setup Hold Window	2.5 ns window adjustable from 4.5/-2.0 ns to -2.0/4.5 ns in 100 ps increments per channel
Threshold Range	TTL, ECL, user-definable plus or minus 6.0 V in 10 mV increments
Timing Zoom Memory Family	16K depth, 250 MHz to 2 GHz variable sample rate on the 16716/17/18/19 Timing Zoom description
Related 16718A Memory	8M / 16M on 16718A

16716A Specifications

PARAMETER	VALUE
Agilent 16716A State and Timing Module 68 Channel	Overview
Applications	32/64-bit processor debug, timing-margin analysis, and simultaneous 2 GHz Timing Zoom with high-speed state through the same probe.
Key Features	68 channels per module
Technical Specifications	Model: 16716A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes

From Agilent State and Timing Modules product overview 5966-3367E, 16716A column. Timing Zoom depth/rate is the published 16716/17/18/19 family Timing Zoom paragraph.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.