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Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 1671D

Inverse Assemblers: microprocessor inverse assemblers class Application: 32-bit processor and bus analysis User Interface: windowed state/timing displays Documentation: HP 1670D-series technical data covering 1671D Application: 32-bit processor and bus analysis User Interface: windowed state/timing displays Documentation: HP 1670D-series technical data covering 1671D Pods: 1670-series probe pods and lead sets



MODEL

**Agilent / Keysight
1671D**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1671D

LISTING

<https://aumictechlabs.com/products/1671d-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight 1671D is a benchtop logic analyzer built for debugging and characterizing digital circuits and embedded microprocessor systems. This 102-channel instrument captures signal activity across state and timing domains, isolate timing violations, protocol errors, and hardware faults in complex digital designs. Technical Specifications Channels: 102 State Analysis: 100 MHz on all channels Timing Analysis: 125 MHz on all channels; 250 MHz on 1/2 channels Memory Depth: 64K samples (all channels); 128K samples (1/2 channels, timing mode only) Minimum Detectable Glitch Width: 3.5 ns Key Features The 1671D operates in both state and timing acquisition modes, allowing you to monitor signal relationships at the clock edge or measure asynchronous glitch activity with nanosecond resolution. The 3.5 ns glitch detection capability resolves transient anomalies that escape slower instruments. Dual memory configurations optimize capture depth based on channel count and analysis mode. Interfaces & Storage Internal

hard drive provides persistent mass storage for capture data and instrument configurations. A 3.5-inch high-density floppy drive supports DOS and LIF file formats for portable data exchange. LAN connectivity via 10Base-T and 10Base-2 connectors enables remote control and network-based data transfer.

Typical Applications Logic analysis of microprocessor debug signals, verification of bus protocols, timing correlation across multiple clock domains, and transient fault isolation in embedded systems.

1671D Characteristics / Specifications

PARAMETER	VALUE
Instrument Type	Standalone logic analyzer
Series Family	HP 1670D / 1671D / 1672D / 1673D
Logic Channels	102
Sibling Channel Counts	136 (1670D), 68 (1672D), 34 (1673D)
Acquisition Memory Class	64K of the 1670D series
State Speed Class	100 MHz class of 1670D series
Timing Speed Class	250 MHz class of 1670D series
Display	color CRT/flat-panel class of 1670D
Remote Class	HP-IB and RS-232, LAN optional
Successor	1671G 150 MHz state, 500 MHz timing, 64K/256K/2M
Form Factor	benchtop standalone logic analyzer
Floppy	DOS floppy storage class
Trigger	graphical trigger macros class of 1670 series
Inverse Assemblers	microprocessor inverse assemblers class
Application	32-bit processor and bus analysis
User Interface	windowed state/timing displays
Documentation	HP 1670D-series technical data covering 1671D
Pods	1670-series probe pods and lead sets
Operating Environment	laboratory ambient per 1670D manual
Power	AC mains operated
Verification	confirm 1671D vs 1671G on the model label before applying 150 MHz/2M rows

1671D Specifications

PARAMETER	VALUE
Applications	State and timing debug of 32-bit embedded systems needing 102 channels and 64K depth.
Key Features	102 channels (1671D vs 136-channel 1670D)
Inverse assemblers and graphical triggers	Predecessor to 1671G 150 MHz / 500 MHz / deeper memory
Technical Specifications	Instrument Type: Standalone logic analyzer

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Instrument Type	Standalone logic analyzer
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Logic Channels	102
Sibling Channel Counts	136 (1670D), 68 (1672D), 34 (1673D)
Acquisition Memory Class	64K of the 1670D series
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Timing Speed Class	250 MHz class of 1670D series
Display	color CRT/flat-panel class of 1670D
Remote Class	HP-IB and RS-232, LAN optioned
Successor	1671G 150 MHz state, 500 MHz timing, 64K/256K/2M
Form Factor	benchtop standalone logic analyzer
Floppy	DOS floppy storage class
Trigger	graphical trigger macros class of 1670 series
Inverse Assemblers	microprocessor inverse assemblers class
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Notes On Speed: 150 MHz/500 MHz/2M numbers are 1671G, not 1671D

Channel Math: 102 = 96 data + clocks class of 1661/1671 family

Notes

102-channel identity and 1670D-series 64K / 100 MHz-class state / 250 MHz-class timing from HP 1670D-series literature. Do not apply 1671G 150 MHz, 500 MHz, or 2M memory to 1671D.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.