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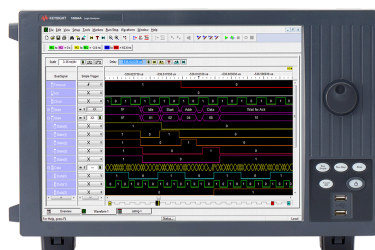
Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 16863A

Agilent Keysight 16863A Portable Logic Analyzer 102 Channel The Keysight 16863A is the 102-channel member of the 16860A Series portable logic analyzers. OEM 16860A Series data sheet lists 102 channels (96 data plus 6 clock), 2.5 GHz / 5.0 GHz timing, 350/700 MHz state, 12.5 GHz timing zoom, and 2 Mb to 128 Mb memory options. Portable high-speed digital debug, DDR/LPDDR capture to 1400 MT/s class, and multi-bus eye scan at 102 channels. 102 channels without quarter-channel 10 GHz option (that option is 16862A/16864A only) The Keysight 16863A is the 102-channel member of the 16860A Series portable logic analyzers. OEM 16860A Series data sheet lists 102 channels (96 data plus 6 clock), 2.5 GHz / 5.0 GHz timing, 350/700 MHz state, 12.5 GHz timing zoom, and 2 Mb to 128 Mb memory options. Portable high-speed digital debug, DDR/LPDDR capture to 1400 MT/s class, and multi-bus eye scan at 102 channels. 102 channels without quarter-channel 10 GHz option (that option is 16862A/16864A only) 350 MHz state standard, 700 MHz Option 700



MODEL

**Agilent / Keysight
16863A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

16863A

LISTING

<https://aumictechlabs.com/products/16863a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keysight 16863A is a 102-channel portable logic analyzer for high-speed digital signal capture and analysis. It combines state and timing analysis capabilities with deep memory and advanced trigger sequencing to isolate and characterize signal integrity issues in complex digital systems. Technical Specifications Channels & Display 102 channels 15-inch color touchscreen

display (1024 × 768 resolution) 1.4 GHz trigger sequencer State Analysis (Synchronous Sampling) Maximum data rate: 1.4 Gb/s Clock rate: Up to 700 MHz Timing Analysis (Asynchronous Sampling) Maximum timing speed: 5 GHz (conventional) / 12.5 GHz (timing zoom) Timing resolution: 80 ps at 12.5 GHz Timing capture: 2.5 GHz/5.0 GHz (full-/half-channel) Memory Standard: 2 Mb Deep memory options: 4 Mb to 256 Mb (software upgradeable) Maximum depth: 128 Mpts Timing zoom memory: 256K Signal Support Single-ended and differential signals DDR, DDR2, DDR3, DDR4, LPDDR, LPDDR2, LPDDR3, LPDDR4 protocolsKey Features Simultaneous eye diagrams across all 102 channels Automated threshold and sample position setup for high-speed bus measurement Removable SSD drive for rapid data transfer and large-file storage USB 3.0 and Gigabit LAN connectivity PCI and PCI Express x1 expansion slots Windows 10 preinstalledInterfaces 2 × USB 3.0 ports (rear) 6 × USB 2.0 ports (2 front, 4 rear) 10/100/1000 Base-T LAN Trigger In/Out BNC connectors (5.5 V maximum)Physical & Environmental Weight: 12.5 kg Operating temperature: 5 °C to 40 °C Humidity: Up to 90% non-condensing at 40 °C Altitude: Up to 4000 m

16863A Characteristics / Specifications

PARAMETER	VALUE
Model	16863A
Instrument Type	Portable logic analyzer
Series Family	Keysight 16860A Series
Logic Analyzer Channels	102 (96 data and 6 clock)
Max Timing Sample Rate Full Half	2.5 GHz full channel / 5.0 GHz half channel
Quarter Channel Timing	not available on 16863A (Option T10 is 16862A and 16864A only)
Max State Clock Standard	350 MHz
Max State Clock Option 7	700 MHz
Max State Data Rate Standard	700 Mb/s
Max State Data Rate Option 7	1400 Mb/s
Timing Zoom	12.5 GHz (80 ps sample resolution) at 256 K samples
Memory Depth Standard	2 Mb
Memory Options	4 Mb, 8 Mb, 16 Mb, 32 Mb, 64 Mb, 128 Mb (16863A-004 through 16863A-128)
Single Clock Qualifiers	4
Minimum Setup Hold Single Clock	80 ps setup, 80 ps hold (160 ps data valid window)
Sample Position Resolution Single Clock	20 ps typical
Max Trigger Sequencer Speed Timing	2.5 GHz
Timing Interval Accuracy Within Pod	plus or minus (1 sample period plus 130 ps plus 0.01 percent of time interval reading)
Display Size Resolution	38.1 cm (15 in) touch, 1024 × 768
System Memory Ssd	8 GB RAM, 256 GB removable SSD, Windows 7 embedded 64 bit
Ac Power	100 to 120 V plus or minus 10 percent 50/60/400 Hz; 100 to 240 V plus or minus 10 percent 50/60 Hz; 325 W max
Dimensions	291.57 mmHx 450.65 mmWx 456.54 mmDWeight Net Shipping: 12.5 kg (27.6 lb) net; 23.3 kg (51 lb) shipping
Operating Temperature	5 C to 40 C

PARAMETER	VALUE
U4201A Cable Count	maximum 3 U4201A cables
Sibling Models	16861A 34 ch, 16862A 68 ch, 16864A 136 ch
Data Rate	Standard: 700 Mb/s

16863A Specifications

PARAMETER	VALUE
Applications	Portable high-speed digital debug, DDR/LPDDR capture to 1400 MT/s class, and multi-bus eye scan at 102 channels.
Key Features	102 channels without quarter-channel 10 GHz option (that option is 16862A/16864A only)
Technical Specifications	Model: 16863A

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	5 C to 40 C
Operating Temperature	5 C to 40 C
U4201A Cable Count	maximum 3 U4201A cables
Sibling Models	16861A 34 ch, 16862A 68 ch, 16864A 136 ch Notes From Keysight 16860A Series Portable Logic Analyzers data sheet (16863A column: 102 channels, timing/state/memory, power 325 W, 12.5 kg, dimensions, Option 700). Quarter-channel 10 GHz timing is not offered on 16863A.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Model	16863A
Instrument Type	Portable logic analyzer
Series Family	Keysight 16860A Series
Logic Analyzer Channels	102 (96 data and 6 clock)
Max Timing Sample Rate Full Half	2.5 GHz full channel / 5.0 GHz half channel
Quarter Channel Timing	not available on 16863A (Option T10 is 16862A and 16864A only)
Max State Clock Standard	350 MHz
Max State Clock Option 700	700 MHz
Max State Data Rate Standard	700 Mb/s
Max State Data Rate Option 700	1400 Mb/s
Timing Zoom	12.5 GHz (80 ps sample resolution) at 256 K samples
Memory Depth Standard	2 Mb
Memory Options	4 Mb, 8 Mb, 16 Mb, 32 Mb, 64 Mb, 128 Mb (16863A-004 through 16863A-128)
Single Clock Qualifiers	4
Minimum Setup Hold Single Clock	80 ps setup, 80 ps hold (160 ps data valid window)
Sample Position Resolution Single Clock	20 ps typical
Max Trigger Sequencer Speed Timing	2.5 GHz
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Sibling Models

16861A 34 ch, 16862A 68 ch, 16864A 136 ch

Notes

From Keysight 16860A Series Portable Logic Analyzers data sheet (16863A column: 102 channels, timing/state/memory, power 325 W, 12.5 kg, dimensions, Option 700). Quarter-channel 10 GHz timing is not offered on 16863A.

Ordering Information

OPTION	DESCRIPTION
Option 700	15 inch 1024 × 768 touch display

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.