

**Aumictech**

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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight 4925B

DDS, modem, and serial DTE/DCE BERT from 75 b/s to 72 kb/s. Synchronous to 72 kb/s with internal clock Async Data Rates: 75 b/s to 19.2 kb/s framed or unframed Sync Data Rates: up to 72 kb/s when 4925B provides the clock Synchronous to 72 kb/s with internal clock Async Data Rates: 75 b/s to 19.2 kb/s framed or unframed Sync Data Rates: up to 72 kb/s when 4925B provides the clock Measurements: bit error, block error, errored seconds, percent error-free seconds, timing delay, parity errors



MODEL

**Agilent / Keysight
4925B**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

4925B

LISTING

<https://aumictechlabs.com/products/4925b-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight 4925B is a portable Bit Error Rate Test Set engineered for data communications system validation across RS-232-C/V.24 and V.35 interfaces. It executes bit and block error tests, errored seconds analysis, error-free second percentages, timing delay measurement, and parity error detection in both asynchronous and synchronous configurations. Asynchronous testing spans 75 b/s to 19.2 kb/s in framed or unframed modes. Synchronous operation supports data rates to 72 kb/s with internal clock generation, enabling Digital Data System (DDS) testing. The instrument transmits FOX messages encoded in 5-bit Baudot, 6-bit EBCD, 7-bit ASCII, or 8-bit EBCDIC for terminal and printer validation. Technical Specifications Asynchronous data rates: 75 b/s to 19.2 kb/s (framed/unframed) Synchronous data rates: Up to 72 kb/s with integrated clock Error measurements: Bit error rate, block error rate, errored seconds,

percent error-free seconds, timing delay, parity errors Diagnostic capabilities: Dropout detection, clock slip detection, RTS-CTS delay measurement Protocol support: FOX message transmission (Baudot, EBCD, ASCII, EBCDIC) Power: Six 9-volt alkaline batteries; >50 hours operation on RS-232-C/V.24 V.35 operation requires HP 18185A or HP 18194A AC moduleKey Features Integrated breakout box for RS-232-C/V.24 signal line manipulation and monitoring Cross-patching capability between DCE and DTE sides Multiple diagnostic startup tests: end-to-end, loopback, BELL 208B modem Modem fault isolation, particularly automatic equalization errorsTypical Applications DDS network characterization, modem performance validation, point-to-point serial link testing, terminal and printer connectivity verification.Compatibility & Integration RS-232-C/V.24 and V.35 interfaces. AC power modules (HP 18185A/18194A) required for sustained V.35 operations.

4925B Characteristics / Specifications

PARAMETER	VALUE
Model	4925B
Instrument Type	Bit error rate test set
Async Data Rates	75 b/s to 19.2 kb/s framed or unframed
Sync Data Rates	up to 72 kb/s when 4925B provides the clock
Interfaces	RS-232-C/V.24 and V.35
Measurements	bit error, block error, errored seconds, percent error-free seconds, timing delay, parity errors
Fox Codes	5-bit Baudot, 6-bit EBCD, 7-bit ASCII, 8-bit EBCDIC
Startup Tests	end-to-end, loopback, and BELL 208B modem
Annunciation	dropouts, clock slips, and RTS-CTS delay time
Breakout	manipulate and monitor RS-232-C/V.24 lines; crosspatch DCE to DTE
Battery	six 9-volt alkaline transistor batteries
Battery Life	greater than 50 hours of RS-232-C/V.24 operation
V35 Power	AC module 18185A or 18194A recommended for V.35
Related Case	18192A carrying case
Related Rs232 Cable	18183A RS-232-C/V.24
Related V35 Cable	18184A V.35
Form Factor	portable BERT
Manufacturer	HP Agilent Keysight
Use Case	DDS and modem BERT
Literature	HP 4925B Bit Error Rate Test Set listings
Family	4925 BERT
Documentation Type	OEM BERT catalog
Brand Lineage	Hewlett Packard Agilent Keysight
System Role	serial BERT

4925B Specifications

PARAMETER	VALUE
HP Agilent 4925B Bit Error Rate Test Set	Overview
Applications	DDS, modem, and serial DTE/DCE BERT from 75 b/s to 72 kb/s.
Key Features	75 b/s to 19.2 kb/s asynchronous
Synchronous to 72 kb/s with internal clock	RS-232-C/V.24 and V.35
Technical Specifications	Model: 4925B

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
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Sync Data Rates	up to 72 kb/s when 4925B provides the clock
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Form Factor	portable BERT
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Family	4925 BERT
Documentation Type	OEM BERT catalog
Brand Lineage	Hewlett Packard Agilent Keysight
System Role	serial BERT
Notes	
From HP 4925B BERT listings (75 b/s-19.2 kb/s async, 72 kb/s sync, RS-232/V.35, FOX codes, >50 hour battery).	

Pattern set, error-inject, and V.35 clocking details are on the 4925B specification page.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.