

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight A400GE-QDD

The Keysight A400GE-QDD is a compact test system for validating Bit Error Rate (BER) and Forward Error Correction (FEC) performance in 400 Gigabit Ethernet electronics. Designed for engineers qualifying 400GE chips, optical transceivers, and port electronics, it delivers real-time BER and FEC statistics across all lanes simultaneously. The system measures performance in minutes rather than hours, enabling rapid identification of bursty and thermal errors through detailed KP4 FEC analysis. Aumictech offers NIST-traceable calibration and repair for the Agilent / Keysight A400GE-QDD. All calibrations are performed to NIST-traceable standards and ship with a certificate. Turnaround is typically 1–3 business days after receipt. This Agilent / Keysight A400GE-QDD is listed at . Calibration is available separately from . Contact us for volume pricing or if you need multiple units.



MODEL

**Agilent / Keysight
A400GE-QDD**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

A400GE-QDD

LISTING

<https://aumictechlabs.com/products/a400ge-qdd-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keysight A400GE-QDD is a compact test system for validating Bit Error Rate (BER) and Forward Error Correction (FEC) performance in 400 Gigabit Ethernet electronics. Designed for engineers qualifying 400GE chips, optical transceivers, and port electronics, it delivers real-time BER and FEC statistics across all lanes simultaneously. The system measures performance in minutes rather than hours, enabling rapid identification of bursty and thermal errors through

detailed KP4 FEC analysis. Technical Specifications 400GE Interface Support: 8×56 Gb/s electrical interface with PAM4 encoding; IEEE 802.3bs and IEEE 802.3cd compliant Speed Configurations: 1×400GE, 2×200GE, 4×100GE, 8×50GE Latency Measurement Resolution: 0.625 ns at 400GE; 1.25 ns at 200GE Lane Signaling: 56 Gb/s electrical per port Key Features Layer 1 BERT and KP4 FEC testing with real-time system-wide BER and FEC statistics across all lanes FEC codeword bit-error density distribution analysis for symbol error performance detail Detection of bursty and thermal errors over time through KP4 FEC symbol bit error distribution KiOS software - browser-based single-page application supported by Google Chrome for fast setup and PASS/FAIL reporting with user-defined tolerances Compatibility & Integration Optical Transceivers: QSFP-DD MSA compliant; supports 400GBASE-DR4, 400GBASE-FR4, 400GBASE-SR8, and QSFP56 Copper Media: 400GBASE-CR8 passive copper Direct Attached Cables up to 3 meters Optional Accessories: Host and module compliance boards, cables, and adapter bundles available for simplified integration with BERT analyzers and development boards

A400GEQDD Characteristics / Specifications

PARAMETER	VALUE
Model	A400GE-QDD
Instrument Type	400GE Layer 1 BERT and KP4 FEC test system
Lane Signaling	56 Gb electrical lane signaling per port
Ports	2-port 400GE Layer-1 BERT QSFP-DD (941-0080)
Software	KiOS Layer 1 BERT 400GE test software Ui: single-page application in Google Chrome
Fec Option	RS-544 (KP4) FEC symbol error correction
Fec Analysis	FEC codeword bit-error density distribution (FEC tail)
Upgrade Port	field-upgrade 2-port to 4-port
Upgrade Fec	field-upgrade KP4 FEC onto 2-port or 4-port
Form Factor	compact benchtop, rackmount as desired
Interface	QSFP-DD
Manufacturer	Keysight Ixia Agilent
Use Case	400GE BER and FEC
Literature	Keysight A400GE-QDD 400GE Layer 1 BERT and KP4 FEC Multiport Test System data sheet
Family	A400GE
Related 800ge	G800GE / AresONE 800GE QSFP-DD chassis
Documentation Type	OEM A400GE-QDD data sheet
Brand Lineage	Ixia Keysight Agilent
System Role	400GE L1 BERT
Reports	PASS/FAIL with user tolerance and limits

A400GEQDD Specifications

PARAMETER	VALUE
Applications	400GE electrical-lane BERT and KP4 FEC qualification of modules and PHY.
Key Features	400GE Layer 1 BERT
Technical Specifications	Model: A400GE-QDD

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	A400GE-QDD
Instrument Type	400GE Layer 1 BERT and KP4 FEC test system
Lane Signaling	56 Gb electrical lane signaling per port
Ports 2	2-port 400GE Layer-1 BERT QSFP-DD (941-0080)
Ports 4	4-port 400GE Layer-1 BERT QSFP-DD (941-0081)
Software	KiOS Layer 1 BERT 400GE test software
Ui	single-page application in Google Chrome
Fec Option	RS-544 (KP4) FEC symbol error correction
Fec Analysis	FEC codeword bit-error density distribution (FEC tail)
Upgrade Port	field-upgrade 2-port to 4-port
Upgrade Fec	field-upgrade KP4 FEC onto 2-port or 4-port
Form Factor	compact benchtop, rackmount as desired
Interface	QSFP-DD
Manufacturer	Keysight Ixia Agilent
Use Case	400GE BER and FEC
Literature	Keysight A400GE-QDD 400GE Layer 1 BERT and KP4 FEC Multiport Test System data sheet
Family	A400GE
Related 800ge	G800GE / AresONE 800GE QSFP-DD chassis
Documentation Type	OEM A400GE-QDD data sheet
Brand Lineage	Ixia Keysight Agilent
System Role	400GE L1 BERT
Reports	PASS/FAIL with user tolerance and limits
Notes	
From Keysight A400GE-QDD data sheet (56 Gb lanes, 941-0080/941-0081, KiOS, optional RS-544 KP4 FEC, field upgrades). Pattern set, pre-emphasis, and QSFP-DD power class are in the A400GE-QDD hardware guide.	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.