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Data Sheet

August 14, 2026

AGILENT / KEYSIGHT

Agilent/Keysight E1450A

Agilent E1450A 160 MHz 12 Channel Timing Module The Agilent/Keysight E1450A isaC-size, 2-slot, register-based VXI 160 MHz timing module with timebase, timing generators, trigger/wait/marker circuits, and sequencer. OEM product materials list 12 pattern clocks (six stimulus, six response) for up to ten E1451A/E1452A pattern I/O modules, 6.25 ns edge placement resolution, and up to 256 timing cycles. Digital functional test timing control, bus-cycle emulation, and margin testing with E1451A/E1452A pattern modules. The Agilent/Keysight E1450A isaC-size, 2-slot, register-based VXI 160 MHz timing module with timebase, timing generators, trigger/wait/marker circuits, and sequencer. OEM product materials list 12 pattern clocks (six stimulus, six response) for up to ten E1451A/E1452A pattern I/O modules, 6.25 ns edge placement resolution, and up to 256 timing cycles. Digital functional test timing control, bus-cycle emulation, and margin testing with E1451A/E1452A pattern modules. 160 MHz timing with 6.25 ns edge resolution



MODEL

**Agilent / Keysight
E1450A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

E1450A

LISTING

<https://aumictechlabs.com/products/e1450a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight E1450A is a C-size, two-slot VXIbus timing module that provides precision timing and pattern generation control for automated digital test systems. It synchronizes complex stimulus and response sequences, delivers programmable marker outputs, and interfaces directly with Pattern I/O modules to orchestrate high-speed DUT testing with cycle-by-cycle parameter

adjustment. Technical Specifications Module Type: Register-based VXIbus timing module, C-size, two-slot form factor Maximum Clock Speed: 160 MHz Edge Placement Resolution: 6.25 ns Timing Cycles: Up to 256 cycles per pattern Pattern Clocks: 12 total - 6 for stimulus, 6 for response Control Signals: Eight general-purpose signals for strobes and synchronization On-the-Fly Parameter Adjustment: Supported across all timing cycles Key Features Accepts triggers from VXIbus backplane, front panel SMB connectors, or DUT Programmable marker outputs via front panel SMB connectors or VXIbus trigger bus lines Implements bus wait states using end-of-ready function with ready input Supports the optional E1453 Timing Pod for extended cable runs up to 2.1 meters E1453 pod buffers 8 control signals and 10 Q-lines; includes trigger and ready line conditioning When E1453 is not deployed, control and Q-lines route through module pod connector; trigger and ready lines use SMB connectors Typical Applications High-speed digital device testing requiring precise edge timing Multi-pin DUT synchronization with independent stimulus and response paths Automated test equipment (ATE) systems using VXIbus architecture Applications requiring on-the-fly timing parameter updates during test execution Compatibility & Integration The E1450A connects to up to 10 E1451A/E1452A Pattern I/O Modules per mainframe, enabling control of 960 pins total with sufficient power. In extended systems using the E1482 VXIbus Extender, one E1450A operates as master for up to 2 slave E1450A modules, supporting up to 30 Pattern I/O Modules across multiple mainframes. External synchronization with DUTs and other instrumentation is fully supported.

E1450A Characteristics / Specifications

PARAMETER	VALUE
Timing Frequency	160 MHz
Pattern Clocks	12 (6 stimulus, 6 response)
Compatible Pattern Modules	up to 10 E1451A/E1452A
Edge Placement Resolution	6.25 ns
Maximum Timing Cycles	256
Combined Cycle Length Max	1024 subcycles
Subcycle Period Min	6.25 ns
Subcycle Period Max	409.6 us
Subcycle Resolution	6.25 ns
Subcycle Accuracy	0.01 percent
Sequencer Vector Memory	65536 vectors (64k)
General Purpose Control Signals	up to 8
Maximum Continuous Output Current Per Line	plus/minus 24 mA
Output Impedance Typical	50 ohm
High Open Circuit Output Voltage Min	4.4 V
Low Open Circuit Output Voltage Max	0.1 V
Module Size	C
Slots Occupied	2
VXI Device Type	register based
Plus 5 V Peak Current	1.6 A
Plus 5 V Dynamic Current	80 mA
Minus 5.2 V Peak Current	8.9 A
Minus 2 V Peak Current	5.9 A
Module Components	timebase, timing generators, triggers, sequencer

E1450A Specifications

PARAMETER	VALUE
Agilent E1450A 160 MHz 12 Channel Timing Module	Overview
Applications	Digital functional test timing control, bus-cycle emulation, and margin testing with E1451A/E1452A pattern modules.
Key Features	160 MHz timing with 6.25 ns edge resolution
Technical Specifications	Timing Frequency: 160 MHz

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Timing Frequency	160 MHz
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General Purpose Control Signals	up to 8
Maximum Continuous Output Current Per Line	plus/minus 24 mA
Output Impedance Typical	50 ohm
High Open Circuit Output Voltage Min	4.4 V
Low Open Circuit Output Voltage Max	0.1 V
Module Size	C
Slots Occupied	2
VXI Device Type	register based
Plus 5 V Peak Current	1.6 A
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Module Components	timebase, timing generators, triggers, sequencer
Notes	
Numeric values from Keysight/Agilent E1450A product specifications summaries citing OEM timing-module docs.	

Confirm installed firmware/options on the faceplate.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.