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Data Sheet
August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight E1610A

HP Agilent E1610A 34 Mb/s E3 Line Interface The Agilent E1610A is a 34 Mb/s E3 line interface VXI module for the Broadband Series Test System. OEM BSTS catalogs list it with E1616A (DS1/DS3), E1695A (DS3), and E4201A (E1) as the PDH ATM physical-layer family. E3 ATM cell mapping and physical-layer testing in E4200/E4210 chassis with optional E4209B protocol processor. Works with E4209B Cell Protocol Processor The Agilent E1610A is a 34 Mb/s E3 line interface VXI module for the Broadband Series Test System. OEM BSTS catalogs list it with E1616A (DS1/DS3), E1695A (DS3), and E4201A (E1) as the PDH ATM physical-layer family. E3 ATM cell mapping and physical-layer testing in E4200/E4210 chassis with optional E4209B protocol processor. Works with E4209B Cell Protocol Processor PDH companion to E4201A E1 and E1616A DS3

MODEL **Agilent / Keysight E1610A**	CALIBRATION **Available on request**
CONDITION **Used**	STATUS **In stock**
RFQ / SKU **E1610A**	LISTING **https://aumictechlabs.com/products/e1610a-2/**

NIST-traceable cal · SAM registered · 30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight E1610A is a single-slot C-size VXI module that generates and analyzes ATM cell streams at 34 Mb/s (E3) line rates. Operating within the Agilent E4200/E4210 Broadband Series Test System, it delivers test capabilities at both physical and ATM cell layers across multiple framing standards.Technical Specifications Module Type: VXI Module, C-size, 1-slot configuration Data Rate: 34 Mb/s (E3) Supported Framing Formats: G.832 (ITU-T Rec. G.832), G.751 PLCP (ETS 300-337, ETS 200-214), G.751 ATM Transmission Convergence (ETS 300-337,

ITU-T Rec. I.432), pure-cell transmission (ITU-T Rec. I.432) Operating Modes: Terminal (full signal generation and analysis), Repeater (physical layer loopback), Local Loopback (transmit-to-receiver electrical loop) Capture Memory: 8 MBKey Features Traffic Generation Generates normal and abnormal test traffic with on-demand erroneous pattern insertion for protocol robustness validation Protocol data unit builder with sequencing and library functions for complex traffic creation One foreground channel plus up to 100 background channels for loading simulation Measurement Capabilities Bit rate error testing using PRBS patterns within cells PRBS error detection and analysis with trigger capability Cell delay, interarrival time, and loss measurements via cell timestamp insertion Traffic Capture and Playback Continuous capture with memory buffer lapping or event-triggered modes Automated decoding with English-language display output Filtered and triggered capture supportTypical Applications R&D engineering, product development, quality assurance, performance testing, type approval, and conformance testing for high-speed ATM transmission and protocol validation.Compatibility & Integration Designed as a modular component within the Agilent E4200/E4210 Broadband Series Test System architecture.

E1610A Characteristics / Specifications

PARAMETER	VALUE
Model	E1610A
Instrument Type	34 Mb/s E3 ATM line interface
Line Rate	34.368 Mb/s E3
Hierarchy	CEPT E3
Card Format	C-size VXI
Partner Processor	E4209A/B Cell Protocol Processor
Partner Chassis	E4200A/B or E4210A/B
Related E1 Lif	E4201A 2.048 Mb/s E1
Related Ds3 Lif	E1695A 45 Mb/s DS3
Related Dual Ds1 Ds	E1616A
Manufacturer	HP Agilent Keysight
Use Case	E3 ATM physical and cell layer test
Standards Family	ITU-T G.703 E3
Remote Control	BSTS UNIX controller
System Role	E3 ATM LIF
Form Factor	C-size VXI module
Bus	VXI
Connector Class	75 ohm coaxial E3 class
Slot Family	BSTS VXI
Mapping Class	E3 ATM mapping per BSTS PDH LIFs
Related J2 Electrical	E1613A 6.3 Mb/s
Related J2 Optical	E1614A 6.3 Mb/s optical
Rate E1 Sibling	2.048 Mb/s
Rate Ds3 Sibling	44.736 Mb/s

E1610A Specifications

PARAMETER	VALUE
HP Agilent E1610A 34 Mb/s E3 Line Interface	Overview
Applications	E3 ATM cell mapping and physical-layer testing in E4200/E4210 chassis with optional E4209B protocol processor.
Key Features	Dedicated 34.368 Mb/s E3 ATM LIF
Works with E4209B Cell Protocol Processor	PDH companion to E4201A E1 and E1616A DS3
Technical Specifications	Model: E1610A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	E1610A
Instrument Type	34 Mb/s E3 ATM line interface
Line Rate	34.368 Mb/s E3
Hierarchy	CEPT E3
Card Format	C-size VXI
Partner Processor	E4209A/B Cell Protocol Processor
Partner Chassis	E4200A/B or E4210A/B
Related E1 Lif	E4201A 2.048 Mb/s E1
Related Ds3 Lif	E1695A 45 Mb/s DS3
Related Dual Ds1 Ds3	E1616A
Manufacturer	HP Agilent Keysight
Use Case	E3 ATM physical and cell layer test
Standards Family	ITU-T G.703 E3
Remote Control	BSTS UNIX controller
System Role	E3 ATM LIF
Form Factor	C-size VXI module
Bus	VXI
Connector Class	75 ohm coaxial E3 class
Slot Family	BSTS VXI
Mapping Class	E3 ATM mapping per BSTS PDH LIFs
Related J2 Electrical	E1613A 6.3 Mb/s
Related J2 Optical	E1614A 6.3 Mb/s optical
Rate E1 Sibling	2.048 Mb/s
Rate Ds3 Sibling	44.736 Mb/s

Notes

From Agilent BSTS catalog identity for E1610A 34 Mb/s (E3) Line Interface. Pulse amplitude and impedance details belong on the E1610A datasheet.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

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