


Aumictech

Test & measurement · bought, sold, repaired, calibrated

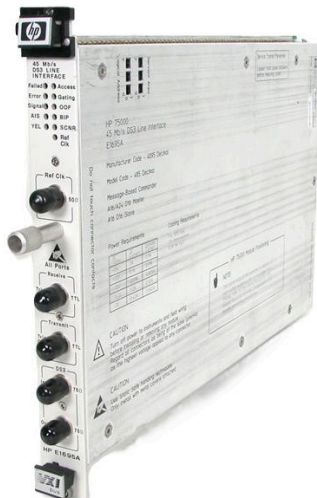
Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight E1695A

HP Agilent E1695A 45 Mb/s DS3 Line Interface The Agilent E1695A is a 45 Mb/s DS3 line interface VXI module for the Broadband Series Test System. OEM BSTS catalogs list it beside E1616A (DS1/DS3) and E1610A (E3) as the dedicated DS3 physical-layer ATM access module. DS3 ATM cell mapping, physical-layer alarms, and cell-layer tests in E4200/E4210 chassis. Works with E4209B Cell Protocol Processor The Agilent E1695A is a 45 Mb/s DS3 line interface VXI module for the Broadband Series Test System. OEM BSTS catalogs list it beside E1616A (DS1/DS3) and E1610A (E3) as the dedicated DS3 physical-layer ATM access module. DS3 ATM cell mapping, physical-layer alarms, and cell-layer tests in E4200/E4210 chassis. Works with E4209B Cell Protocol Processor Instrument Type: 45 Mb/s DS3 ATM line interface



MODEL

**Agilent / Keysight
E1695A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

E1695A

LISTING

<https://aumictechlabs.com/products/e1695a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight E1695A is a connectivity and signal access accessory engineered to extend the capability of Keysight logic analyzer platforms for digital systems testing and debugging. It facilitates reliable signal acquisition and accurate data capture essential for electronic troubleshooting and complex digital analysis workflows. The E1695A integrates within Keysight's modular logic analysis ecosystem, supporting high-speed timing capture and deep memory operations across multiple measurement channels. Technical Specifications The E1695A can interface with high-impedance probing solutions and flying-lead probe architectures used in

modern logic analysis systems. Its construction meets the electrical and mechanical demands of high-speed digital signal acquisition with minimal impact on circuits under test.

Key Features

- Accessory for Keysight 16900 Series modular logic analyzer mainframes
- Supports multiple probing configurations including high-impedance interposer probes and flying-lead probes
- Enables flexible signal access and connectivity across digital test points
- Maintains signal integrity during acquisition and analysis phases

Typical Applications

- Digital circuit debugging and protocol analysis
- Timing validation and signal synchronization verification
- Complex multi-signal capture in embedded system development
- Signal integrity assessment in high-speed digital designs
- Post-acquisition waveform dissection and behavioral analysis

Compatibility & Integration

The E1695A is purpose-built for integration with Keysight's 16900 Series logic analyzers, which provide modular mainframes, configurable measurement channels, and advanced triggering and analysis tools. It works in conjunction with Keysight's probe portfolio to optimize signal fidelity and measurement accuracy. Complete operating instructions and safety guidance are provided in the included User's Manual.

E1695A Characteristics / Specifications

PARAMETER	VALUE
Model	E1695A
Instrument Type	45 Mb/s DS3 ATM line interface
Line Rate	44.736 Mb/s DS3
Hierarchy	Bell DS3 / T3
Card Format	C-size VXI
Partner Processor	E4209A/B Cell Protocol Processor
Partner Chassis	E4200A/B or E4210A/B
Related Dual Rate Lif	E1616A 1.5/45 Mb/s DS1/DS3
Related E3 Lif	E1610A 34 Mb/s E3
Related E1 Lif	E4201A 2.048 Mb/s E1
Manufacturer	HP Agilent Keysight
Use Case	DS3 ATM physical and cell layer test
Coding Class	B3ZS DS3 as on E1616A DS3 port
Connector Class	BNC 75 ohm DS3 class BSTS LIFs
Standards Family	ANSI T1.102/T1.107 DS3
Remote Control	BSTS UNIX controller
System Role	DS3 ATM LIF
Form Factor	C-size VXI module
Bus	VXI
Ds3 Payload Atm Class	40.704 Mb/s as on E1616A DS3 bandwidth
Slot Family	BSTS VXI
Mapping Class	DS3 direct or PLCP class BSTS DS3 LIFs
Alarm Class	AIS IDLE Yellow class DS3 LIFs
Bandwidth	Slot Family: BSTS VXI

E1695A Specifications

PARAMETER	VALUE
HP Agilent E1695A 45 Mb/s DS3 Line Interface	Overview
Applications	DS3 ATM cell mapping, physical-layer alarms, and cell-layer tests in E4200/E4210 chassis.
Key Features	Dedicated 44.736 Mb/s DS3 ATM LIF
Works with E4209B Cell Protocol Processor	Companion to E1616A dual DS1/DS3 LIF
Technical Specifications	Model: E1695A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	E1695A
Instrument Type	45 Mb/s DS3 ATM line interface
Line Rate	44.736 Mb/s DS3
Hierarchy	Bell DS3 / T3
Card Format	C-size VXI
Partner Processor	E4209A/B Cell Protocol Processor
Partner Chassis	E4200A/B or E4210A/B
Related Dual Rate Lif	E1616A 1.5/45 Mb/s DS1/DS3
Related E3 Lif	E1610A 34 Mb/s E3
Related E1 Lif	E4201A 2.048 Mb/s E1
Manufacturer	HP Agilent Keysight
Use Case	DS3 ATM physical and cell layer test
Coding Class	B3ZS DS3 as on E1616A DS3 port
Connector Class	BNC 75 ohm DS3 class BSTS LIFs
Standards Family	ANSI T1.102/T1.107 DS3
Remote Control	BSTS UNIX controller
System Role	DS3 ATM LIF
Form Factor	C-size VXI module
Bus	VXI
Ds3 Payload Atm Class	40.704 Mb/s as on E1616A DS3 bandwidth
Slot Family	BSTS VXI
Mapping Class	DS3 direct or PLCP class BSTS DS3 LIFs
Alarm Class	AIS IDLE Yellow class DS3 LIFs
Notes	
From Agilent BSTS catalog identity for E1695A 45 Mb/s (DS3) Line Interface. Port-level voltages follow the E1616A DS3 section when the dedicated E1695A sheet is not at hand; confirm on the E1695A datasheet.	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.