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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight E4209B

HP Agilent E4209B Cell Protocol Processor The Agilent E4209B Cell Protocol Processor is the ATM/AAL protocol engine for the Broadband Series Test System. OEM 5965-7053E lists real-time monitoring and simulation of ATM, F4/F5 OAM, and AAL1, hardware segmentation and reassembly, and generation of up to eight user-defined ATM streams at line rates from 1.5 Mb/s to 622 Mb/s with matching line interfaces. ATM signalling, AAL, SMDS/DXI, traffic policing, and conformance testing in E4200/E4210 BSTS chassis with UNIX controller. The Agilent E4209B Cell Protocol Processor is the ATM/AAL protocol engine for the Broadband Series Test System. OEM 5965-7053E lists real-time monitoring and simulation of ATM, F4/F5 OAM, and AAL1, hardware segmentation and reassembly, and generation of up to eight user-defined ATM streams at line rates from 1.5 Mb/s to 622 Mb/s with matching line interfaces. ATM signalling, AAL, SMDS/DXI, traffic policing, and conformance testing in E4200/E4210 BSTS chassis with UNIX controller. Eight independent distributed ATM traffic streams



MODEL

**Agilent / Keysight
E4209B**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

E4209B

LISTING

<https://aumictechlabs.com/products/e4209b-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight E4209B is a Broadband Cell Protocol Processor (CPP) module designed for real-time and post-processing protocol testing within the Agilent/Keysight Broadband Series Test System (BSTS). This 1-slot, C-size VXI module enables ATM network testing with user-defined

traffic generation capabilities up to 149.76 Mb/s across the 155 Mb/s BSTS cell bus. The module supports configurable line interfaces spanning data rates from 1.5 to 622 Mb/s, making it suitable for multi-rate broadband protocol validation.

Technical Specifications

Product Type: Broadband Cell Protocol Processor (CPP) Module

Form Factor: 1-Slot, C-size VXI Module

Protocol Support: Asynchronous Transfer Mode (ATM)

User-Defined Traffic Generation: Up to 149.76 Mb/s

Cell Bus Interface: 155 Mb/s BSTS backplane

Configurable Line Interface Data Rates: 1.5 to 622 Mb/s

Key Features

- Real-time and post-processing ATM protocol testing capabilities
- VXIbus integration for seamless system connectivity
- Multi-rate line interface support across broadband speed ranges
- Flexible traffic generation for protocol validation

Typical Applications

- ATM network protocol validation and functional testing
- Real-time traffic generation and monitoring
- Post-processing analysis of protocol behavior
- Multi-rate broadband system characterization

Compatibility & Integration

The E4209B integrates into the Broadband Series Test System (BSTS) via VXIbus backplane connectivity. The module operates within either the E4210 Form-13 chassis (11 user module slots) or the E4400 Form-7 chassis (5 user module slots), providing flexible system configuration options for various test environments.

E4209B Characteristics / Specifications

PARAMETER	VALUE
Model	E4209B
Instrument Type	ATM cell protocol processor
Rated Line Rate Class	0 to 155 Mb/s CPP (line interfaces to 622 Mb/s)
Card Format	1 slot C-size VXI
Distributed Streams	8
Single Shot Unique Cells	up to 2047
Combined Unique Cells All Streams	2047
Capture Capacity	131072 cells per 8 MB capture RAM
Timestamp Resolution	0.1 us
Pattern Matchers	8 programmable 53 byte cells
Bit Match States	0, 1, or wildcard
Timer Range	1 to 256 ms plus or minus 1 ms
Cell Rate Control	0 to 100 percent of line rate
Atm Header Fields	GFC VPI VCI PT CLP user defined
Hec Modes	user defined or auto calculate
Interface Modes	UNI or NNI selectable
Aal1 Fields	CSI sequence number protection
Oam Flows	F4 VP and F5 VC segment and end to end
Oam Functions	AIS FERF continuity check forward/backward monitor
Decode References	ITU-T I.361 I.363 I.610
Partner Line Interfaces	E1616A E1610A E1618A E1697A
Partner Chassis	E4200A/B Form-7 or E4210A/B Form-13
Programming Environment	C language UNIX
Manufacturer	HP Agilent Keysight

E4209B Specifications

PARAMETER	VALUE
HP Agilent E4209B Cell Protocol Processor	Overview
Applications	ATM signalling, AAL, SMDS/DXI, traffic policing, and conformance testing in E4200/E4210 BSTS chassis with UNIX controller.
Hardware SAR at full line speed	Capture of 131072 cells per 8 MB RAM with 0.1 us timestamps
Technical Specifications	Model: E4209B
Notes	Numeric and protocol rows from Agilent E4209B Cell Protocol Processor technical specifications 5965-7053E / 5966-0036E Rev C.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes

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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.