

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight E4832A

Keysight E4832A ParBERT 675 Mb/s Data Module The Keysight E4832A isaParBERT 81250 675 Mb/s generator/analyzer data module with four front-end slots. It accepts E4838A generator and E4835A analyzer front-ends, provides up to 2 Mbit memory per channel, and operates from 333.334 kHz to 675 MHz with E4805B E4808A or E4809A clocks. TTL PECL LVDS class parallel BER, multi-channel digital characterization, and lower-rate Mux/Demux stimulus/analysis on ParBERT 81250. Four front-end slots for up to four generator channels The Keysight E4832A isaParBERT 81250 675 Mb/s generator/analyzer data module with four front-end slots. It accepts E4838A generator and E4835A analyzer front-ends, provides up to 2 Mbit memory per channel, and operates from 333.334 kHz to 675 MHz with E4805B E4808A or E4809A clocks. TTL PECL LVDS class parallel BER, multi-channel digital characterization, and lower-rate Mux/Demux stimulus/analysis on ParBERT 81250. Four front-end slots for up to four generator channels Analyzer front-end pairs occupy two slots each



MODEL

**Agilent / Keysight
E4832A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

E4832A

LISTING

<https://aumictechlabs.com/products/e4832a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight E4832A is a single-slot VXI module that functions as a parallel bit error ratio tester within the ParBERT 81250 platform. It supports configurable test setups for parallel interface validation across Gigabit Ethernet, Digital Video, and OC-192 applications,

accommodating up to four E4838A Generator cards and two E4835A Analyzer cards. Technical Specifications Data Rate & Memory Operating range: 333 Kb/s to 675 Mb/s Memory depth: 2 Mbit per channel Pattern storage: Up to 2 Mb memory-based patterns Generator Module Output configuration: Differential Amplitude range: 0.1 to 3.5 V peak-to-peak Transition time (10%-90%): 0.5 to 4.5 ns Data mode jitter: < 100 ps peak-to-peak (80 ps typical) Clock mode jitter: 8 ps rms (typical) Analyzer Module Input types: Differential or single-ended Input impedance: 50 ohm Sensitivity: 50 mV differential Input voltage ranges: -2 V to +3 V and 0 V to 5 V (selectable) Key Features Pseudo Random Word Sequence (PRWS) generation Standard PRBS generation to $2^{31}-1$ User-defined and mixed-data pattern analysis Bit error ratio measurement from parallel ports Typical Applications Gigabit Ethernet interface validation Digital Video signal testing OC-192 parallel interface characterization Compatibility & Integration The E4832A integrates with Keysight E4838A generator and E4835A analyzer front-end cards. The ParBERT 81250 platform supports scaled configurations up to 45 Gb/s analysis speeds using different module combinations.

E4832A Characteristics / Specifications

PARAMETER	VALUE
Model	E4832A
Instrument Type	ParBERT 81250 675 Mb/s data module
Card Format	VXI C-size 1 slot
Front End Slots	4
Max Data Rate	675 Mb/s
Frequency Range Generator	333.334 kHz to 675 MHz
Memory Depth Per Channel	up to 2 Mbit
PRBS PRWS Polynomials	$2^n - 1$ n equals 7 9 10 11 15 23 31
Mark Density Settings	1/8 1/4 1/2 3/4 7/8 for n equals 7 9 10 11 15
Delay Range	0 to 3.0 μ s
Sampling Delay Resolution	2 ps
Timing Accuracy	plus or minus 50 ps plus or minus 50 ppm at 15 to 35 C
Skew Typical	50 ps after deskew
Pulse Width Range	750 ps to period minus 750 ps
Pulse Width Resolution	2 ps
Pulse Width Accuracy	plus or minus 200 ps plus or minus 0.1 percent
Duty Cycle Range	1 percent to 99 percent subject to width limits
Analyzer Sampling Rate	333.334 kb/s to 675 Mb/s
Fine Delay Range Analyzer	plus or minus 1 period
Auto Delay Alignment Range	plus or minus 50 ns
Max Memory At 16 Bit Segment Resolution	2097152 bits
Compatible Clock Modules	E4805B E4808A E4809A
Channels Per Frame Class	up to 44 at 675 Mb/s
Addressable Technologies	LVDS PECL TTL 3.3 V CMOS
Module Mass Net Typical	2 kg including front-ends

PARAMETER	VALUE
Data Rate	675 Mb/s

E4832A Specifications

PARAMETER	VALUE
Keysight E4832A ParBERT 675 Mb/s Data Module	Overview
Applications	TTL PECL LVDS class parallel BER, multi-channel digital characterization, and lower-rate Mux/Demux stimulus/analysis on ParBERT 81250.
Two Mbit memory depth per channel	PRBS/PRWS polynomials through $2^{31}-1$
Technical Specifications	Model: E4832A
Notes	Numeric rows from Agilent ParBERT 81250 datasheet E4832A section. Front-end amplitude and transition specs reside on E4838A/E4835A sheets.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	E4832A
Instrument Type	ParBERT 81250 675 Mb/s data module
Card Format	VXI C-size 1 slot
Front End Slots	4
Max Data Rate	675 Mb/s
Frequency Range Generator	333.334 kHz to 675 MHz
Memory Depth Per Channel	up to 2 Mbit
PRBS PRWS Polynomials	2^n-1 n equals 7 9 10 11 15 23 31
Mark Density Settings	1/8 1/4 1/2 3/4 7/8 fornequals 7 9 10 11 15
Delay Range	0 to 3.0 us
Sampling Delay Resolution	2 ps
Timing Accuracy	plus or minus 50 ps plus or minus 50 ppm at 15 to 35 C
Skew Typical	50 ps after deskew
Pulse Width Range	750 ps to period minus 750 ps
Pulse Width Resolution	2 ps
Pulse Width Accuracy	plus or minus 200 ps plus or minus 0.1 percent
Duty Cycle Range	1 percent to 99 percent subject to width limits
Analyzer Sampling Rate	333.334 kb/s to 675 Mb/s
Fine Delay Range Analyzer	plus or minus 1 period
Auto Delay Alignment Range	plus or minus 50 ns
Max Memory At 16 Bit Segment Resolution	2097152 bits
Compatible Clock Modules	E4805B E4808A E4809A
Channels Per Frame Class	up to 44 at 675 Mb/s
Addressable Technologies	LVDS PECL TTL 3.3 V CMOS
Module Mass Net Typical	2 kg including front-ends

Notes

Numeric rows from Agilent ParBERT 81250 datasheet E4832A section. Front-end amplitude and transition specs reside on E4838A/E4835A sheets.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.