

**Aumictech**

Test &amp; measurement • bought, sold, repaired, calibrated

**Data Sheet**

August 15, 2026

AGILENT / KEYSIGHT

## Agilent/Keysight G800GE

Keysight Ixia G800GE AresONE 800GE QSFP-DD Test Chassis G800GE is the Keysight/Ixia AresONE 800GE QSFP-DD fixed-chassis class. OEM AresONE 800GE QSFP-DD references list native QSFP-DD800 physical interfaces with L1-3 support and default PAM4 speeds 2×400GE, 4×200GE, 8×100GE, and 1×800GE per port. Example full-performance 4-port model is 800GE-4P-QDD (944-1192); reduced-performance counterpart is 800GER-4P-QDD (944-1193).

Compatible transceivers include QSFP-DD 941-0084 and QSFP-DD-COAX 941-0085.

Electrical-lane specs reference IEEE 802.3ck 100 Gb/s signaling. G800GE is the Keysight/Ixia AresONE 800GE QSFP-DD fixed-chassis class. OEM AresONE 800GE QSFP-DD references list native QSFP-DD800 physical interfaces with L1-3 support and default PAM4 speeds 2×400GE, 4×200GE, 8×100GE, and 1×800GE per port. Example full-performance 4-port model is 800GE-4P-QDD (944-1192); reduced-performance counterpart is 800GER-4P-QDD (944-1193).

Compatible transceivers include QSFP-DD 941-0084 and QSFP-DD-COAX 941-0085.

Electrical-lane specs reference IEEE 802.3ck 100 Gb/s signaling. 800GE/400GE/200GE/100GE L1-L3 traffic test on QSFP-DD800 ports.



MODEL

**Agilent / Keysight  
G800GE**

CALIBRATION

**Available on  
request**

CONDITION

**Used**

STATUS

**In stock**

RFQ / SKU

**G800GE**

LISTING

<https://aumictechlabs.com/products/g800ge-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

**This unit**

The Keysight G800GE is a test system for validating 800 Gigabit Ethernet devices, optical transceivers, and networking equipment ports. It delivers comprehensive Layer 1 BERT and Layer 2 packet blast testing with deep focus on Forward Error Correction performance analysis. The system accelerates development cycles by identify and resolve issues in minutes rather than hours.

**Technical Specifications**

**Data Rates & Lane Configuration** 1×800GE, 2×400GE, 4×200GE, 8×100GE over 106.25 Gb/s host electrical lanes 1×400GE, 2×200GE, 4×100GE, 8×50GE over 53.125 Gb/s host electrical lanes PAM4 signaling per port; IEEE 802.3ck compliant electrical interface

**BERT Capabilities** Full line rate bit error rate testing with per-lane and per-port statistics Enhanced BERT option for BERT-inferred FEC analysis PRBS-13Q and PRBS-31Q pattern generation

**FEC Testing & Analysis** RS-544 (KP4) FEC symbol error correction qualification FEC codeword bit-error density distribution analysis FEC margin and Frame Loss Ratio measurements FEC symbol error injection at 400GE and 200GE speeds Correctable and uncorrectable FEC statistics per-port FEC Trigger and Capture option for codeword bit-error location analysis

**Layer 2 Testing** Line rate packet blast with simple packet processing System-level view of packet statistics across all lanes and ports

**Key Features** Electrical lane signaling at 106.25 Gb/s and 53.125 Gb/s per lane Exceptional signal quality, frequency accuracy, and output power OSFP800 and QSFP-DD800 port support Electrical coaxial interface for direct connection to Module Compliance Boards and device evaluation boards Hardware clock IN/OUT for external clock synchronization CMIS v5.0, v4.0, and v3.0 management register support for optical transceivers

**Typical Applications** 800GE device and transceiver validation Forward Error Correction performance qualification Telecommunications, aerospace, and defense equipment testing

**Compatibility & Integration** Supports OSFP800, QSFP-DD800, and electrical coaxial interfaces. Integrates with external clock sources and CMIS-compliant optical transceiver management systems.

## G800GE Characteristics / Specifications

| PARAMETER              | VALUE                                                                                           |
|------------------------|-------------------------------------------------------------------------------------------------|
| Model                  | G800GE                                                                                          |
| Instrument Type        | AresONE 800GE QSFP-DD fixed chassis                                                             |
| Interface              | native QSFP-DD800 physical interfaces                                                           |
| Layers                 | L1-3 support                                                                                    |
| Default Speeds         | 2×400GE, 4×200GE, 8×100GE, and 1×800GE per port (PAM4)                                          |
| Example 4 Port Full    | 800GE-4P-QDD (944-1192)                                                                         |
| Example 4 Port Reduced | 800GER-4P-QDD (944-1193)                                                                        |
| Related 2 Port Full    | 800GE-2P-QDD (944-1190)                                                                         |
| Related 2 Port Reduced | 800GER-2P-QDD (944-1191)                                                                        |
| Related 8 Port Full    | 800GE-8P-QDD (944-1194)                                                                         |
| Related 8 Port Reduced | 800GER-8P-QDD (944-1195)                                                                        |
| Transceiver Qsfp Dd    | 941-0084                                                                                        |
| Transceiver Coax       | QSFP-DD-COAX 941-0085                                                                           |
| Electrical Standard    | IEEE 802.3ck 100 Gb/s, 200 Gb/s, and 400 Gb/s electrical interfaces based on 100 Gb/s signaling |
| Form Factor            | fixed chassis                                                                                   |
| Manufacturer           | Keysight Ixia Agilent                                                                           |
| Use Case               | 800GE density traffic test                                                                      |
| Literature             | Ixia AresONE 800GE QSFP-DD Fixed Chassis reference                                              |
| Family                 | AresONE 800GE                                                                                   |
| Related 400ge          | AresONE-S 400GE QSFP-DD                                                                         |
| Documentation Type     | OEM AresONE 800GE reference                                                                     |
| Brand Lineage          | Ixia Keysight Agilent                                                                           |
| System Role            | 800GE QSFP-DD generator                                                                         |

# G800GE Specifications

| PARAMETER                           | VALUE                                                           |
|-------------------------------------|-----------------------------------------------------------------|
| Applications                        | 800GE/400GE/200GE/100GE L1-L3 traffic test on QSFP-DD800 ports. |
| Key Features                        | Native QSFP-DD800                                               |
| Full and reduced performance models | IEEE 802.3ck lane class                                         |
| Technical Specifications            | Model: G800GE                                                   |

# Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

performance 4-port model is 800GE-4P-QDD (944-1192); reduced-performance counterpart is 800GER-4P-QDD (944-1193). Compatible transceivers include QSFP-DD 941-0084 and QSFP-DD-COAX 941-0085. Electrical-lane specs reference IEEE 802.3ck 100 Gb/s signaling.

Applications

800GE/400GE/200GE/100GE L1-L3 traffic test on QSFP-DD800 ports.

Key Features

Native QSFP-DD800

1x800GE plus 2x400/4x200/8x100GE

L1-3 support

Full and reduced performance models

IEEE 802.3ck lane class

Technical Specifications

## Technical Specifications

| PARAMETER              | VALUE                                                                                           |
|------------------------|-------------------------------------------------------------------------------------------------|
| Model                  | G800GE                                                                                          |
| Instrument Type        | AresONE 800GE QSFP-DD fixed chassis                                                             |
| Interface              | native QSFP-DD800 physical interfaces                                                           |
| Layers                 | L1-3 support                                                                                    |
| Default Speeds         | 2x400GE, 4x200GE, 8x100GE, and 1x800GE per port (PAM4)                                          |
| Example 4 Port Full    | 800GE-4P-QDD (944-1192)                                                                         |
| Example 4 Port Reduced | 800GER-4P-QDD (944-1193)                                                                        |
| Related 2 Port Full    | 800GE-2P-QDD (944-1190)                                                                         |
| Related 2 Port Reduced | 800GER-2P-QDD (944-1191)                                                                        |
| Related 8 Port Full    | 800GE-8P-QDD (944-1194)                                                                         |
| Related 8 Port Reduced | 800GER-8P-QDD (944-1195)                                                                        |
| Transceiver Qsfp Dd    | 941-0084                                                                                        |
| Transceiver Coax       | QSFP-DD-COAX 941-0085                                                                           |
| Electrical Standard    | IEEE 802.3ck 100 Gb/s, 200 Gb/s, and 400 Gb/s electrical interfaces based on 100 Gb/s signaling |
| Form Factor            | fixed chassis                                                                                   |
| Manufacturer           | Keysight Ixia Agilent                                                                           |
| Use Case               | 800GE density traffic test                                                                      |
| Literature             | Ixia AresONE 800GE QSFP-DD Fixed Chassis reference                                              |
| Family                 | AresONE 800GE                                                                                   |

| PARAMETER          | VALUE                       |
|--------------------|-----------------------------|
| Related 400ge      | AresONE-S 400GE QSFP-DD     |
| Documentation Type | OEM AresONE 800GE reference |
| Brand Lineage      | Ixia Keysight Agilent       |
| System Role        | 800GE QSFP-DD generator     |

Notes

From Ixia/Keysight AresONE 800GE QSFP-DD chassis reference (QSFP-DD800, default PAM4 speeds, 944-1190 through 944-1195, 941-0084/0085 optics). Per-port FEC and packet-rate tables are in the IxOS AresONE 800GE hardware guide.

### Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: [sales@aumictech.com](mailto:sales@aumictech.com) · +1 (484) 841-9341

#### Aumictech

788 S 500 E, Pleasant Grove, UT 84062  
[sales@aumictech.com](mailto:sales@aumictech.com) · +1 (484) 841-9341  
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·  
30-day returns  
NAICS 423690 · 811210 · 541380  
Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.