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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight M8062A

The Keysight M8062A is the 32 Gb/s BERT front end that extends J-BERT M8020A pattern generation and analysis to 32 Gb/s class rates for next-generation serial receiver characterization. Extending M8020A setups to 25/28/32 Gb/s class SERDES, CEI, Ethernet, and high-speed computer I/O receiver tests. Front end expansion for M8020A to 32 Gb/s class Preserves integrated jitter and de-emphasis workflow of J-BERT Native Host Rates Before Expansion: 8.5 and 16 Gb/s class DUT Interface Class: high speed serial differential Serial Rate Class After Expansion: 32 Gb/s class Numeric and capability rows from Keysight M8020A product/data sheet statements for M8062A 32 Gb/s front end. Fine pin-level levels belong on the detailed front-end tables.



MODEL

**Agilent / Keysight
M8062A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

M8062A

LISTING

<https://aumictechlabs.com/products/m8062a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keysight M8062A is a 32 Gb/s BERT front-end module that extends the M8020A Bit Error Ratio Tester for high-speed serial link characterization. This 2-slot AXIe module delivers integrated generator and analyzer capabilities up to 32.4 Gb/s, supporting NRZ line coding with full jitter injection, de-emphasis, ISI generation, and analyzer equalization. Engineers use the M8062A for R&D validation of 100G class links, Thunderbolt 3, SAS 24G, data center interfaces, and long-haul communication standards. Technical Specifications Data Rate: Up to 32.4 Gb/s

Module Type: BERT front-end, 2-slot AXIe form factor Line Coding: NRZ Output Amplitude: 1 mV resolution; voltage window -1 V to +3 V External Termination Voltage: -1 V to +3 V Transition Times: 12 ps typical (20%/80%) Intrinsic Random Jitter: < 200 fs rms typical (20.625–32.0 Gb/s) Total Jitter: 6 ps pp typical (20.6–30.0 Gb/s at BER 10^{-12}) Electrical Idle Transition Time: 4 ns typical (full swing to 0 V) Control Interface: USB via M8020A integrated UI; remote programming via automation commands

Key Features Jitter Injection: Calibrated LFPJ, HF PJ, Clk/2, BUJ, RJ, and SSC injection De-emphasis: 8-tap integrated de-emphasis up to 24 dB; 5 post-cursor and 2 pre-cursor taps with individual weight adjustment. Option M8062A-0G4 required for de-emphasis negotiation. ISI Generator: Electronically adjustable channel emulation with one or two frequency breakpoints and frequency-dependent loss control Interference Injection: Single-ended source supporting common-mode and differential-mode injection Clock Recovery: Built-in recovery functionality Analyzer Equalization: Integrated equalization to recover closed-eye data Error Analysis: Full-rate analysis when paired with M8041A module

Typical Applications 100G serial link validation, Thunderbolt 3 testing, SAS 24G characterization, data center interconnect R&D, and long-haul communication standards.

Compatibility & Integration The M8062A operates as a 2-slot AXIe module in compatible AXIe chassis such as the M9505A and integrates with the Keysight J-BERT M8020A ecosystem.

M8062A Characteristics / Specifications

PARAMETER	VALUE
Model	M8062A
Instrument Type	32 Gb/s BERT front end
Parent System	M8020A J-BERT
Maximum Sample Rate Class	32.4 Gb/s
Supported Data Rate Class	expandable to 32 Gb/s from 16 Gb/s base
Amplitude Class At 32 Gb/s	0.9 Vpp class
Line Coding Support Class	NRZ class stressed serial
Module Type	AXIe front end module
Channel Count Class With Host	1 to 2 class depending on configuration
Clock Data Recovery	uses M8020A CDR architecture
Equalizer Path	analyzer equalizer remains available in system
Jitter Injection Path	inherits M8020A calibrated jitter sources
De Emphasis Path	inherits M8020A 8 tap de emphasis
Form Factor	AXIe compatible with 5 slot M8020A chassis
Control	M8000 / M8020A system software
Primary Role	rate expansion front end
Compatible Host	M8020A high performance BERT
Typical Use	32G SERDES RX characterization
Native Host Rates Before Expansion	8.5 and 16 Gb/s class
Platform Series	M8000
DUT Interface Class	high speed serial differential
Serial Rate Class After Expansion	32 Gb/s class
Data Rate	Class: expandable to 32 Gb/s from 16 Gb/s base

M8062A Specifications

PARAMETER	VALUE
Keysight M8062A 32 Gb/s BERT Front End	Overview
Applications	Extending M8020A setups to 25/28/32 Gb/s class SERDES, CEI, Ethernet, and high-speed computer I/O receiver tests.
Key Features	Front end expansion for M8020A to 32 Gb/s class
Technical Specifications	Model: M8062A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes	
Numeric and capability rows from Keysight M8020A product/data sheet statements for M8062A 32 Gb/s front end. Fine pin-level levels belong on the detailed front-end tables.	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.