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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight M9710A

Keysight M9710A AXIe High Speed Digitizer 4 Channel 10 Bit The Keysight M9710A is a single-slot 10-bit AXIe digitizer capturing DC to 2.5 GHz at 5 GS/s (10 GS/s interleaved). OEM data sheet lists up to 8 GB DDR3 memory and high dynamic range SFDR. Advanced physics, hydrodynamics, multichannel high-speed DAQ. Optimized frequency response flatness for wideband pulses The Keysight M9710A is a single-slot 10-bit AXIe digitizer capturing DC to 2.5 GHz at 5 GS/s (10 GS/s interleaved). OEM data sheet lists up to 8 GB DDR3 memory and high dynamic range SFDR. Advanced physics, hydrodynamics, multichannel high-speed DAQ. Optimized frequency response flatness for wideband pulses Instrument Type: AXIe high-speed digitizer DAQ



MODEL

**Agilent / Keysight
M9710A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

M9710A

LISTING

<https://aumictechlabs.com/products/m9710a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keysight M9710A is an 8-channel AXIe high-speed digitizer delivering 10-bit resolution at up to 5 GS/s per channel, or 10 GS/s across two interleaved channels. Engineered for demanding multichannel data acquisition across DC to 2.5 GHz, it integrates Xilinx Virtex-6 FPGAs for real-time signal processing and custom algorithm implementation. The module provides up to 8 GB of DDR3 acquisition memory (1.6 GSamples per channel) and 50 Ω DC-coupled inputs with selectable 250 mV or 1 V full-scale ranges. Trigger time interpolation achieves 15 ps RMS

precision. Spurious-free dynamic range exceeds 56 dBc typical over large analysis bandwidths, with optimized frequency response flatness and low noise density. Its single-slot AXIe form factor scales from 2-slot to 14-slot chassis, accommodating up to thirteen modules in the largest configuration. Technical Specifications Resolution: 10-bit ADC Sampling Rate: 5 GS/s per channel (standard); 10 GS/s on two interleaved channels Analog Bandwidth: DC to 2.5 GHz Channels: 4 synchronous channels (2 when using interleaving option) Acquisition Memory: Up to 8 GB DDR3 (1.6 GSamples per channel) Full-Scale Range: Selectable 250 mV or 1 V Input Impedance: 50 Ω , DC coupled Input Voltage Offset Range: $\pm$ FSR Trigger Time Interpolator Precision: 15 ps RMS SFDR: >56 dBc typical Key Features Xilinx Virtex-6 FPGA for real-time data processing and algorithm acceleration Low distortion with high dynamic range architecture Low noise density across the measurement bandwidth Optimized frequency response flatness Proprietary signal conditioning and clock distribution for jitter and spurious signal minimization Typical Applications Advanced research experiments and hydrodynamics Multichannel data acquisition in aerospace and defense High-bandwidth signal analysis and characterization Compatibility & Integration AXIe chassis support spans the M9502A (2-slot, up to two modules), M9505A and M9506A (5-slot, up to five modules each), and M9514A (14-slot, up to thirteen modules). Software compatibility includes Windows and Linux with PathWave VSA Software, U1092A Multichannel Acquisition Software, MD2 IVI Driver, MD2 LabVIEW Driver, and MD2 Soft Front Panel.

M9710A Characteristics / Specifications

PARAMETER	VALUE
Model	M9710A
Instrument Type	AXIe high-speed digitizer DAQ
Analog Channels	4 (2 when interleaving with INT)
Adc Resolution	10 bits
Max Sample Rate	10 GS/s with INT
Standard Sample Rate	5 GS/s per channel
Input Frequency Range	DC to 2.5 GHz
Acquisition Memory Maximum	8 GB (1.6 GSamples/ch)
Input Impedance	50 ohm DC coupled
Full Scale Ranges	250 mV or 1 V selectable
Input Voltage Offset Range	plus or minus FSR
Trigger Time Interpolator Precision	15 ps rms
Sfdr Typical	greater than 56 dBc over large analysis bandwidth
Form Factor	1 slot AXIe
Onboard Processing	Xilinx Virtex-6 FPGAs
Pulse Analysis Capability	optimized for few hundred picosecond pulses
Two Modules In M9502A	8 channel 10 bit system
Five Modules In M9505A	20 channel 10 bit system
Thirteen Modules In M9514A	52 channel 10 bit system
Compatible Software	PathWave VSA and U1092A Multichannel Acquisition
Os Support	Windows and Linux
Chassis Example High Performance	M9506A AXIe with optional BC1 LAN SCPI controller
Channel Coherence	four phase-coherent channels per module
Interleave Mode	combine two 5 GS/s channels to 10 GS/s
Bandwidth	Form Factor: 1 slot AXIe

M9710A Specifications

PARAMETER	VALUE
Key Features	Four phase-coherent 10-bit channels
Technical Specifications	Model: M9710A
Notes	Channel, resolution, sample rate, bandwidth, memory, FSR, TTI, and SFDR from Keysight M9710A data sheet 5992-1898.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes

Channel, resolution, sample rate, bandwidth, memory, FSR, TTI, and SFDR from Keysight M9710A data sheet 5992-1898.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

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30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.