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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight N1076B

Keysight N1076B Electrical Clock Recovery up to 64 GBd The Keysight N1076B provides instrument-grade electrical clock recovery up to 64 GBd (Option 264) for NRZ and PAM4 with adjustable loop bandwidth/peaking and aux clock jitter as low as 100 fs rms. High-speed electrical SERDES TX/RX test, standards-compliant jitter and eye measurements without DUT clock. Integrated variable equalizers for closed-eye inputs The Keysight N1076B provides instrument-grade electrical clock recovery up to 64 GBd (Option 264) for NRZ and PAM4 with adjustable loop bandwidth/peaking and aux clock jitter as low as 100 fs rms. High-speed electrical SERDES TX/RX test, standards-compliant jitter and eye measurements without DUT clock. Integrated variable equalizers for closed-eye inputs USB control from DCA mainframe or standalone FlexDCA PC

**MODEL****Agilent / Keysight
N1076B****CALIBRATION****Available on
request****CONDITION****Used****STATUS****In stock****RFQ / SKU****N1076B****LISTING**<https://aumictechlabs.com/products/n1076b-2/>

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NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight N1076B is a precision electrical clock recovery module that extracts timing information from high-speed data streams spanning 125 MBd to 64 GBd. It supports both NRZ and PAM4 signal types with standards-compliant Golden PLL architecture, adjustable peaking, and loop bandwidth up to 20 MHz. The module delivers ultra-low residual random jitter below 100 fs RMS and achieves lock on signals as low as 30 mV pp (rates ≤ 53.125 GBd) or 60 mV pp (rates > 53.125 GBd). Single-ended and differential data inputs connect to oscilloscope clock/trigger

inputs. The optional Jitter Spectrum Analysis (JSA) capability isolates root causes and distributions of low-frequency jitter. For degraded or closed-eye signals, the integrated Variable Equalizer (Option EVA) recovers locking capability on electrical inputs.

Technical Specifications

Data Rate: 125 MBd to 64 GBd continuous Signal Support: NRZ and PAM4 electrical signals Clock Recovery: Standards-compliant Golden PLL with adjustable peaking and loop bandwidth to 20 MHz Minimum Input to Lock (NRZ & PAM4, single-ended, open eye): 30 mV pp (≤ 53.125 GBd); 60 mV pp (> 53.125 GBd); 10 mV pp at 10.3125 GBd and 26.56 GBd (characteristic) Residual Random Jitter: < 100 fs RMS Auxiliary Clock Output: 8 GHz to 16 GHz Input Voltage (max.): ± 500 mV (single-ended); ± 2.2 Vpp (differential) Power Consumption: 290 W maximum; 52 VA typical Operating Altitude: To 4,600 m (15,000 ft) Humidity: 95% RH non-condensing maximum Dimensions: 88.26 mm H $\times$ 212.5 mm W $\times$ 485 mm D (without front connectors) Weight: 6.4 kg typical

Key Features

Single-ended and differential data inputs for flexible signal acquisition Jitter Spectrum Analysis option for root-cause jitter characterization Variable Equalizer (Option EVA) to open closed eyes on degraded signals Recovered clock output to oscilloscope clock or trigger input Auxiliary recovered clock output from 8 GHz to 16 GHz

Compatibility & Integration

Connects via USB to N1000A or 86100D DCA-X mainframe, or PC running N1010A FlexDCA software Requires FlexDCA firmware version A.05.80 or above Supports N1010300A FlexPLL Application or N1081PLCA PCI Express PLL Test Application for standards-compliant measurements Line power: 100/120 VAC or 220/240 VAC, 50/60/400 Hz

N1076B Characteristics / Specifications

PARAMETER	VALUE
Model	N1076B
Instrument Type	DCA-M electrical clock recovery
Max Symbol Rate Option 2	64 GBd
Signal Formats	NRZ and PAM4
Auxiliary Clock Intrinsic Random Jitter	as low as 100 fs rms
Control Interface	USB 2.0 to N1000A 86100D or PC with N1010A FlexDCA
Loop Bandwidth	adjustable for standards compliant golden PLL
Peaking	selectable peaking
Equalizers	integrated variable equalizers on both electrical inputs
Jitter Spectrum Analysis	available with JSA
Precision Timebase Use	aux clock to N1000A PTB input
User Interface	FlexDCA identical to N1000A 86100D
Residual Random Jitter Family Headline	less than 100 fs rms
Primary Application	electrical transmitter and receiver clock recovery
Closed Eye Recovery	equalizers enable recovery from closed eyes
PII Analysis Software	N1010300A FlexPLL Signal Integrity Package
Standards Examples	IEEE 802.3 Fibre Channel OIF-CEI compliant CR setups
Form Factor	DCA-M compact clock recovery instrument
Host Software	N1010A FlexDCA
Mainframe Compatibility	N1000A or 86100D DCA-X
Input Type	electrical
Clock Output	recovered clock plus auxiliary clock output
Phase Locked Loop	configurable golden PLL jitter transfer
Bandwidth	adjustable for standards compliant golden PLL

N1076B Specifications

PARAMETER	VALUE
Keysight N1076B Electrical Clock Recovery up to 64 GBd	Overview
Key Features	NRZ and PAM4 electrical clock recovery
Technical Specifications	Model: N1076B
Notes	Symbol rate, formats, jitter, interfaces, and PLL features from Keysight N1076B/7A/7B/8A DCA-M clock recovery data sheet 5992-1620.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Model	N1076B
Instrument Type	DCA-M electrical clock recovery
Max Symbol Rate Option 264	64 GBd
Signal Formats	NRZ and PAM4
Auxiliary Clock Intrinsic Random Jitter	as low as 100 fs rms
Control Interface	USB 2.0 to N1000A 86100D or PC with N1010A FlexDCA
Loop Bandwidth	adjustable for standards compliant golden PLL
Peaking	selectable peaking
Equalizers	integrated variable equalizers on both electrical inputs
Jitter Spectrum Analysis	available with JSA
Precision Timebase Use	aux clock to N1000A PTB input
User Interface	FlexDCA identical to N1000A 86100D
Residual Random Jitter Family Headline	less than 100 fs rms
Primary Application	electrical transmitter and receiver clock recovery
Closed Eye Recovery	equalizers enable recovery from closed eyes
PLL Analysis Software	N1010300A FlexPLL Signal Integrity Package
Standards Examples	IEEE 802.3 Fibre Channel OIF-CEI compliant CR setups
Form Factor	DCA-M compact clock recovery instrument
Host Software	N1010A FlexDCA
Mainframe Compatibility	N1000A or 86100D DCA-X
Input Type	electrical
Clock Output	recovered clock plus auxiliary clock output
Phase Locked Loop	configurable golden PLL jitter transfer
Notes	
Symbol rate, formats, jitter, interfaces, and PLL features from Keysight N1076B/7A/7B/8A DCA-M clock recovery data sheet 5992-1620.	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.