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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight N4851A

Keysight N4851A MIPI D-PHY Digital Acquisition Probe The Keysight N4851A isaMIPI D-PHY digital acquisition probe for CSI-2 and DSI protocol analysis on 16800/16900-series logic analyzers. It snoops the bus with 2 lanes at up to 800 Mbps high-speed mode. Mobile CSI-2 camera and DSI display link debug, HS/LP mode change tracking, and packet-level protocol decode with error visibility. Works with optional N4861A stimulus probe The Keysight N4851A isaMIPI D-PHY digital acquisition probe for CSI-2 and DSI protocol analysis on 16800/16900-series logic analyzers. It snoops the bus with 2 lanes at up to 800 Mbps high-speed mode. Mobile CSI-2 camera and DSI display link debug, HS/LP mode change tracking, and packet-level protocol decode with error visibility. Works with optional N4861A stimulus probe

Instrument Type: MIPI D-PHY digital acquisition probe



MODEL

**Agilent / Keysight
N4851A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

N4851A

LISTING

<https://aumictechlabs.com/products/n4851a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Agilent/Keysight N4851A is a MIPI D-PHY digital acquisition probe engineered for real-time debugging and testing of MIPI D-PHY interfaces in mobile and embedded systems. It captures CSI-2 and DSI protocol traffic transparently between devices or operates in active test mode with simultaneous stimulus injection and response analysis. The probe integrates with 16800 and 16900 Series logic analyzers to deliver hierarchical trace visibility, protocol decoding, packet inspection, and intelligent triggering on protocol violations and user-defined packet

content. Technical Specifications Maximum Acquisition Speed: 800 Mbps Lane Configuration: 1 or 2 lanes Voltage Support: Low power and high speed voltage levels User-Defined Payload: Up to 2048 bits Logic Analyzer Requirement: 16800 or 16900 Series with 68 channels minimum Probe Interface: 90-pin cable compatible with Agilent 1695x-series logic analyzer cards External Connectors: SMA (m-m) connectors for stimulus integration with N4861A probeKey Features Real-time acquisition of MIPI D-PHY traffic with hierarchical trace display CSI-2 and DSI protocol decoder with integrated packet viewer Bi-directional communication and bus mode change monitoring Protocol-specific triggering on packets, individual bits, and violations Analysis-only mode for transparent device-to-device traffic capture Active test mode enabling customizable stimulus generation and simultaneous device response analysis High impedance probing options including flying leads and soft-touch connectorsTypical Applications Camera interface validation (CSI-2 testing) Display interface characterization (DSI testing) Mobile SoC peripheral debugging Controller and device behavior analysis System integration verificationCompatibility & Integration The N4851A operates as a modular component within Keysight's 16800/16900 Series logic analyzer platform. It pairs with the N4861A MIPI D-PHY Stimulus Probe for integrated stimulus-response testing. Probing solutions support various mechanical interfaces optimized for signal integrity.

N4851A Characteristics / Specifications

PARAMETER	VALUE
Model	N4851A
Instrument Type	MIPI D-PHY digital acquisition probe
Lane Width	2 channels
Maximum Bit Rate High Speed	800 Mbps
Minimum Bit Rate High Speed	80 Mbps
Maximum Bit Rate Low Power	10 Mbps
LPVhigh Range	800 mV to 3.3 V
Voltage Sensitivity Typical	100 mV
Termination Style	bus snoop not an endpoint
Protocol DSI 1.01 Decode	yes
Protocol DSI 1.00 Decode	no
Mode Change Tracking	yes displayed on trace
HS And LP Data Support	yes
Short And Long Packet Decode	yes
Error Displays	SOT EOT Escape Sequence Turnaround ECC CRC
Compatible Analyzers	16800 or 16900 series with 68 or more channels
Stimulus Companion	N4861A requires N4851A
Payload Viewing	yes
User Defined Payload Class	up to 2048 bits class
Sibling Four Lane Model	N4851B
N4851B Max HS Rate	940 Mbps
Bus Status Indicators	LEDs for status and errors

N4851A Specifications

PARAMETER	VALUE
Applications	Mobile CSI-2 camera and DSI display link debug, HS/LP mode change tracking, and packet-level protocol decode with error visibility.
High speed mode to 800 Mbps	Low power mode to 10 Mbps
Hierarchical packet protocol viewer	Works with optional N4861A stimulus probe
Technical Specifications	Model: N4851A
Notes	Numeric rows from Keysight MIPI D-PHY Protocol Test Solutions datasheet N4851A/B tables.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
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Minimum Bit Rate High Speed	80 Mbps
Maximum Bit Rate Low Power	10 Mbps
LPVhigh Range	800 mV to 3.3 V
Voltage Sensitivity Typical	100 mV
Termination Style	bus snoop not an endpoint
Protocol DSI 1.01 Decode	yes
Protocol DSI 1.00 Decode	no
Protocol CSI-2 1.00 Decode: yes	
Specifications	
PARAMETER	VALUE
Mode Change Tracking	yes displayed on trace
HS And LP Data Support	yes
Short And Long Packet Decode	yes
Error Displays	SOT EOT Escape Sequence Turnaround ECC CRC
Compatible Analyzers	16800 or 16900 series with 68 or more channels
Stimulus Companion	N4861A requires N4851A
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Notes

Numeric rows from Keysight MIPI D-PHY Protocol Test Solutions datasheet N4851A/B tables.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.