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Data Sheet

August 15, 2026

AGILENT / KEYSIGHT

Agilent/Keysight N4880A

Keysight N4880A Reference Clock Multiplier for PCI Express MIPI M-PHY and UHS-II The Keysight N4880A is a reference clock multiplier with a multiplying PLL that locks J-BERT or ParBERT pattern generators to a lower-rate DUT reference clock. OEM getting-started and data-sheet literature list support for PCI Express 1 to 3, MIPI M-PHY and SD UHS-II, three 3.5 mm SMA female connectors, USB control from J-BERT, PLL loop bandwidth of 2 or 5 MHz, and 100 to 240 VAC 50 to 60 Hz at 80 VA maximum. Receiver jitter-tolerance testing of PCIe main boards, MIPI M-PHY chipsets, and UHS-II hosts that cannot accept an external reference clock. The Keysight N4880A is a reference clock multiplier with a multiplying PLL that locks J-BERT or ParBERT pattern generators to a lower-rate DUT reference clock. OEM getting-started and data-sheet literature list support for PCI Express 1 to 3, MIPI M-PHY and SD UHS-II, three 3.5 mm SMA female connectors, USB control from J-BERT, PLL loop bandwidth of 2 or 5 MHz, and 100 to 240 VAC 50 to 60 Hz at 80 VA maximum. Receiver jitter-tolerance testing of PCIe main boards, MIPI M-PHY chipsets, and UHS-II hosts that cannot accept an external reference clock.



MODEL

**Agilent / Keysight
N4880A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

N4880A

LISTING

<https://aumictechlabs.com/products/n4880a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The N4880A is a reference clock multiplier that synchronizes pattern generators in high-speed digital communication test systems to external clock signals with precise jitter tolerance control. Designed for R&D and manufacturing validation of PCI Express, MIPI M-PHY, and SD card UHS-II devices, it locks compatible BERT systems to a system reference clock, enabling accurate receiver characterization and standards compliance verification.

Technical Specifications

Clock Multiplication and Synchronization Multiplies reference clock frequencies including 19.2 MHz, 26–52 MHz, and 100 MHz Multiplying phase-locked loop with selectable bandwidth: 2 MHz to 5 MHz Transparent to 33 kHz spread spectrum clocking on reference inputs Tolerates low-frequency jitter within PLL loop bandwidth Differential input sensitivity: 100 mVpp

Supported Standards PCI Express 1.0, 2.0, 3.0 MIPI M-PHY (gear 1–3) SD card UHS-II

Power and Environmental Maximum power consumption: 80 VA Input: Single-phase AC 100–240 V, 50–60 Hz (auto-selecting power supply) Operating temperature: 5–40 °C Maximum relative humidity: 95% Altitude: Up to 2000 meters Indoor use, installation category II, pollution degree 2

Key Features Three 3.5 mm SMA female connectors (two differential inputs, one output) Standalone graphical user interface with USB remote control Software revision 7.20 or later required for remote operation Safety Class 1 with protective earth terminal

Typical Applications Jitter tolerance testing and receiver compliance validation for high-speed serial interfaces in chipset and host device development.

Compatibility & Integration Primarily designed for use with Agilent/Keysight J-BERT N4903B and ParBERT 81250A pattern generators.

N4880A Characteristics / Specifications

PARAMETER	VALUE
Model	N4880A
Instrument Type	Reference clock multiplier
PLL Type	multiplying phase locked loop
Supported Standards	PCI Express 1 to 3, MIPI M-PHY, SD UHS-II
Front Connectors	three 3.5 mm SMA female
Input Channels	two (usable single-ended)
Output Channels	one clock output to J-BERT PG Clock In or ParBERT clock module
Loop Bandwidth	2 MHz or 5 MHz
Ssc Support	keeps 33 kHz spread-spectrum clock on the reference
Input Sensitivity	100 mVpp differential class per N4880A data sheet
Ref Clock Example	19.2 MHz multiplied toward 100 MHz class BERT clock
Host Control	USB from J-BERT rear USB port
Bert Software	J-BERT SCPI remote extension version 7.20 or newer
Operating System	Windows XP SP3 English or Windows 7 32/64 bit with .NET
Mains Voltage	100 to 240 VAC auto ranging
Mains Frequency	50 to 60 Hz
Power Consumption	80 VA maximum
Manufacturer	HP Agilent Keysight
Form Factor	benchtop clock multiplier
Documentation	Keysight N4880A Reference Clock Multiplier getting started 9018-03622
Lock Indicator	PLL lock status LED green when reference is correct
Clock Source On Bert	external clock into J-BERT pattern generator
Application	stressed receiver tests with common reference-clock architecture
Protective Earth	required; interruption of earth is prohibited

PARAMETER	VALUE
Bandwidth	2 MHz or 5 MHz

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes	
From Keysight N4880A getting-started guide 9018-03622 and N4880A reference clock multiplier data sheet. Exact	

multiply ratios by standard are in the N4880A UI tables for the installed hardware revision.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.