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Data Sheet

August 15, 2026

ANALOG DEVICES

Analog Devices ADSP-BF537 EZ-KIT LITE Blackfin Processor

The Analog Devices ADSP-BF537 EZ-KIT Lite is a cost-effective evaluation platform for the Blackfin ADSP-BF537 processor. It provides developers with a suite of tools for prototyping and debugging embedded applications. The kit includes an ADSP-BF537 processor board, an ICE-100B emulator, and VisualDSP++ development software.



MODEL

**Analog Devices
ADSP-BF537 EZ-KIT
LITE**

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

**ADSP-BF537 EZ-KIT
LITE**

LISTING

<https://aumictechlabs.com/products/adsp-bf537-ez-kit-lite-3/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Analog Devices ADSP-BF537 EZ-KIT Lite is a cost-effective evaluation platform built around the ADSP-BF537 Blackfin processor for embedded signal processing and connectivity applications. This kit delivers a complete development environment with 600 MHz processing performance, 132 KB of full-speed on-chip SRAM, and 64 MB SDRAM paired with 4 MB Flash memory for standalone operation. The platform integrates dual-channel synchronous serial ports supporting 8 stereo I2S channels, 10/100 Mbps Ethernet, CAN 2.0B, UART with RS-232 interface, SPI, I2C, parallel video input, and 48 general-purpose I/Os. Audio I/O includes a 96 kHz stereo DAC and 96 kHz stereo ADC. Supported by VisualDSP++ development tools and ICE-100B emulator integration, the kit accelerates prototyping for

real-time control and DSP-intensive workloads. Technical Specifications Processor Core ADSP-BF537 Blackfin architecture with 16-/32-bit mixed ISA Clock rate: up to 600 MHz 10-stage RISC MCU/DSP pipeline Dual 16-bit MACs, dual 40-bit ALUs, four 8-bit video ALUs, 40-bit shifter with SIMD support Memory Management Unit (MMU) for protection Dynamic Power Management (DPM) with on-chip voltage regulator Memory Hierarchy L1 instruction memory: 64 KB (16 KB configurable cache) L1 data memory: two banks of 32 KB each On-board SDRAM: 64 MB (32M x 16) On-board Flash: 4 MB (2M x 16) Peripherals and Interfaces Ethernet: IEEE 802.3 10/100 MAC with SMSC LAN83C185 PHY, RJ45 connector CAN: 2.0B interface with TJA1041 transceiver, dual RJ10 connectors Serial ports: two dual-channel full-duplex SPORTs supporting 8 stereo I2S channels UART: RS-232 interface with DB9 female connector and IrDA support SPI port TWI: I2C controller PPI: ITU-R 656 video format support Timers: eight multifunction timers with PWM capability GPIO: 48 pins (8 with high current drivers) Audio: AD1871 96 kHz stereo DAC and AD1854 96 kHz stereo ADC with 1/8" connectors Three-connector expansion interface for peripheral access Key Features Flexible cache architecture for soft real-time control and hard real-time signal processing Mixed 16-/32-bit ISA optimizes code density across diverse workloads VisualDSP++ integrated development environment with ICE-100B emulator support Three expansion connectors enable extended peripheral integration Typical Applications Real-time signal processing and filtering Embedded control systems with network connectivity Audio processing and codec development CAN bus and industrial Ethernet prototyping Video data acquisition and processing Compatibility & Integration The kit operates within the VisualDSP++ toolchain and ICE-100B emulator infrastructure, providing in-circuit debugging and performance profiling capabilities for Blackfin development projects.

ADSPBF537EZKITLITE Characteristics / Specifications

PARAMETER	VALUE
Model	ADSP-BF537 EZ-KIT Lite
Instrument Type	Blackfin processor evaluation board
Processor	ADSP-BF537 Blackfin
Core Clock	up to 600 MHz
External Bus	up to 133 MHz
Package	182-ball mini-BGA
Input Crystal	25 MHz
Rtc Crystal	32.768 kHz
Sdram	64 MB (32M x 16-bit), ~SMS0
Sdram Devices	MT48LC32M8, 8M x 8-bit x 4 banks, two chips
Flash	4 MB (2M x 16-bit) ST Micro M29W320 class on ~AMS3-0
Ethernet Phy	SMSC LAN83C185 10/100 with RJ45
Can Transceiver	TJA1041 with two modular connectors
Adc	AD1871 96 kHz stereo ADC, 3.5 mm jack
Dac	AD1854 96 kHz stereo DAC, 3.5 mm jack
Uart	RS-232 line driver/receiver
Elvis	National Instruments ELVIS interface
Debug	USB-based debugger plus 14-pin JTAG ICE header
Host Cpu Class	Pentium 500 MHz or higher (original kit)
Host Ram Class	256 MB minimum (original kit)
Host Os Class	Windows 2000 or Windows XP (original kit)
Host Disk Class	750 MB disk space (original kit)
Manufacturer	Analog Devices
Literature	ADSP-BF537 EZ-KIT Lite product highlight; evaluation system manual Rev 1.1
Family	Blackfin EZ-KIT Lite

PARAMETER	VALUE
Form Factor	desktop evaluation board
System Role	ADSP-BF537 hardware/software prototype
Brand Lineage	Analog Devices
Mac Class	IEEE 802.3 10/100 Ethernet MAC on BF537
Can Class	CAN 2.0B controller on BF537
Kit Contents	board, VisualDSP++ evaluation suite, CE supply, USB cable, Ethernet cables, 3.5 mm cable, headphones

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes
From Analog Devices ADSP-BF537 EZ-KIT Lite product highlight and evaluation-system manual (600 MHz, 64 MB SDRAM, 4 MB flash, LAN83C185, TJA1041, AD1871/AD1854). Do not mix later CrossCore host-PC minima into the original kit sheet.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.