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Data Sheet

August 14, 2026

ANRITSU

Anritsu MP1652A 3GHz Pulse Pattern Generator

Anritsu MP1652A 3 GHz Pulse Pattern Generator The Anritsu MP1652A is a pulse pattern generator specified to 3 GHz class operation for BERTS stimulus of high-speed digital and optical components. Pattern generation for error-rate testing of devices and modules up to 3 GHz class clocks. Pattern Types: PRBS and programmable class The Anritsu MP1652A is a pulse pattern generator specified to 3 GHz class operation for BERTS stimulus of high-speed digital and optical components. Pattern generation for error-rate testing of devices and modules up to 3 GHz class clocks. Pattern Types: PRBS and programmable class Application Class: high speed digital optical module test



MODEL

Anritsu MP1652A

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

MP1652A

LISTING

<https://aumictechlabs.com/products/mp1652a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Anritsu MP1652A is a pulse pattern generator designed for the testing of high-speed digital communication systems and devices. It is intended for use in research and development of high-speed logic ICs and digital systems, often in conjunction with the Anritsu MP1653A error detector. The instrument offers features that allow for the measurement of amplitude and offset margins, as well as time-dependent characteristics and phase margins. Technical Specifications * Frequency Range: 0.05 GHz to 3 GHz Amplitude Variation (Clock and Data Signals): 0.5 Vp-p to 2 Vp-p Offset Adjustment: ± 2 V Waveform Quality: High Quality Clock Noise: Low FM/PM-Noise Pattern

Generation: * Programmable Pattern: 8 Mbit * PRBS Patterns: Bit lengths from $2^7 - 1$ to $2^{31} - 1$ bits * M-Series pseudorandom patterns * Selectable cell data (M-series PRBS or programmable pattern) Outputs: * Complementary clock outputs * Complementary data outputs * Four data outputs at 1/4 the speed of the fundamental clock signal (settings for amplitude and offset configurable) Clock Delay: Variable delay function for measuring time-dependent characteristics or phase margins Interface: * GP-IB (General Purpose Interface Bus) for automatic or remote measurement Data Storage: Built-in 3.5-inch floppy disk drive for storing preset data Compatibility * Companion Instrument: Anritsu MP1653A Error Detector Physical * Connectors: N-Type Connector System Integration The Anritsu MP1652A pulse pattern generator works with integration into automated test systems via its GP-IB interface. This allows for remote control and programmatic execution of test sequences, crucial for high-volume testing or complex R&D workflows. The ability to store test patterns on its internal floppy disk drive facilitates rapid setup for repeated tests or quick switching between different test configurations. Applications The Anritsu MP1652A is suitable for research and development involving high-speed logic integrated circuits and digital communication systems. Its capabilities in generating various patterns and controlling signal amplitude and offset make it adaptable for characterizing the performance and margin analysis of these components and systems. Why Choose Aumictech for Anritsu MP1652A? At Aumictech, we specialize in supplying high-end signal generation and test equipment. Whether you need precise pulse pattern generation, comprehensive error detection analysis, or flexible amplitude and offset control, our team ensures the Anritsu MP1652A delivers maximum performance for your application.

MP1652A Characteristics / Specifications

PARAMETER	VALUE
Instrument Type	Pulse pattern generator
Series Family	Anritsu MP165x BERTS
Clock Frequency Class	up to 3 GHz
Companion Error Detector	MP1651A
Pattern Types	PRBS and programmable class
Application Class	high speed digital optical module test
Remote Interface	GPIB
Output	data and clock outputs class
Form Factor	BERTS PPG mainframe
Use Stage	R&D manufacturing
Synchronization	pattern sync triggers class
Amplitude Control	variable data amplitude class
Offset Control	variable offset class
Polarity	data/clock invert class
Termination	50 ohm class interfaces
Automation	production scripting via GPIB
Calibration	OEM periodic calibration
Family Peer	MP1651A 3 GHz error detector
Bit Rate Class	up to 3 Gbit/s NRZ class
Jitter Performance	low jitter PPG class
Display	pattern and rate setup
Protection	output protection class
Accessories	matched cables recommended
Documentation	Anritsu MP1652A manual

PARAMETER

VALUE

Configuration Note

confirm exact amplitude timing tables on OEM datasheet before denser upload

MP1652A Specifications

PARAMETER	VALUE
Anritsu MP1652A 3 GHz Pulse Pattern Generator	Overview
Applications	Pattern generation for error-rate testing of devices and modules up to 3 GHz class clocks.
Key Features	3 GHz class pulse pattern generator
PRBS and programmable patterns	Companion to MP1651A error detector
Technical Specifications	Instrument Type: Pulse pattern generator

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Instrument Type	Pulse pattern generator
Series Family	Anritsu MP165x BERTS
Clock Frequency Class	up to 3 GHz
Companion Error Detector	MP1651A
Pattern Types	PRBS and programmable class
Application Class	high speed digital optical module test
Remote Interface	GPIO
Output	data and clock outputs class
Form Factor	BERTS PPG mainframe
Use Stage	R&D manufacturing
Synchronization	pattern sync triggers class
Amplitude Control	variable data amplitude class
Offset Control	variable offset class
Polarity	data/clock invert class
Termination	50 ohm class interfaces
Automation	production scripting via GPIO
Calibration	OEM periodic calibration
Family Peer	MP1651A 3 GHz error detector
Bit Rate Class	up to 3 Gbit/s NRZ class
Jitter Performance	low jitter PPG class
Display	pattern and rate setup
Protection	output protection class
Accessories	matched cables recommended
Documentation	Anritsu MP1652A manual
Configuration Note	confirm exact amplitude timing tables on OEM datasheet before denser upload

Notes

Frequency-class rows from Anritsu MP1652A product identification and BERTS family literature. Expand full amplitude/timing accuracy tables from the OEM datasheet PDF before upload.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.