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Data Sheet

August 13, 2026

ANRITSU

Anritsu MP1758A Pulse Pattern Generator

Anritsu MP1758A 4 Channel 12.5G Pulse Pattern Generator The Anritsu MP1758A is a 4-channel pulse pattern generator operating from 100 MHz to 12.5 GHz with two clock outputs. It generates programmable patterns up to 128 kbit per channel and seven PRBS patterns from 2^7-1 to $2^{31}-1$ with 1/4 phase difference across channels. Amplitude, offset, and data-to-clock delay are independently adjustable per channel for multi-lane high-speed digital and optical component testing. Discontinued OEM successor class is MP1800A. The Anritsu MP1758A is a 4-channel pulse pattern generator operating from 100 MHz to 12.5 GHz with two clock outputs. It generates programmable patterns up to 128 kbit per channel and seven PRBS patterns from 2^7-1 to $2^{31}-1$ with 1/4 phase difference across channels. Amplitude, offset, and data-to-clock delay are independently adjustable per channel for multi-lane high-speed digital and optical component testing. Discontinued OEM successor class is MP1800A.



MODEL

Anritsu MP1758A

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

MP1758A

LISTING

<https://aumictechlabs.com/products/mp1758a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Anritsu MP1758A Pulse Pattern Generator is a compact instrument for high-speed digital system testing and telecommunications applications. It generates programmable and pseudo-random binary sequence (PRBS) patterns across four independently adjustable data output channels and two clock output channels, operating from 0.1 GHz to 12.5 GHz with internal or

external clock sources. Technical Specifications Output Architecture 4 data output channels (CH 1–4) with NRZ waveform 2 clock output channels (CLOCK 1–2) Frequency range: 0.1 GHz to 12.5 GHz Clock Sources Internal clock with 1 kHz or 1 MHz resolution; 10 MHz reference (internal or external selectable) External clock input: 0.8 to 2.0 Vp-p sine wave (≥ 500 MHz) or square wave via APC-3.5 connector Pattern Generation Programmable patterns: up to 128 kbit per channel (1, 2, or 4 channel modes) PRBS patterns: 7 types with periods from 2^7-1 to $2^{31}-1$ bits; $\frac{1}{4}$ phase difference between channels supported Error insertion: rates selectable from 10^{-4} to 10^{-9} ; single error insertion capability Logic inversion supported Data & Clock Output Characteristics Amplitude: 0.5 to 2.0 Vp-p in 10 mV steps Offset voltage: -2.0 to $+2.0$ V_{OH} in 5 mV steps Clock-to-data delay: -500 ps to $+500$ ps in 1 ps steps 50 Ω load impedance; ECL termination; APC-3.5 connectors Control & Environment GPIB (IEEE488.2) and parallel interfaces Operating temperature: 15°C to 35°C Dimensions: 426 mm (W) $\times$ 221 mm (H) $\times$ 450 mm (D) Weight: ≤ 37 kg Key Features Four independent data channels with precision amplitude and offset control Dual clock outputs with picosecond-level timing adjustment Seven PRBS pattern types spanning 2^7-1 to $2^{31}-1$ bit periods Programmable error insertion at controlled rates Selectable internal/external reference; flexible clock configuration Typical Applications 6G development Virtual and cloud testing environments Internet of Things (IoT) systems Automotive high-speed digital validation Compatibility & Integration GPIB and parallel control interfaces enable integration into automated test systems and remote operation environments.

Features & description

From manufacturer datasheet

Features

- Four independently adjustable data output channels

Applications

- Multi lane SERDES and optical transceiver stimulus to 12.5 Gb/s

MP1758A Characteristics / Specifications

PARAMETER	VALUE
Model	MP1758A
Manufacturer	Anritsu
Instrument Type	4 Channel Pulse Pattern Generator
Alias	MP1758A; Anritsu MP1758A
Frequency range	100 MHz to 12.5 GHz
Data output channels	4
Clock output channels	2
Programmable pattern length	up to 128 kbit per channel
PRBS pattern count	7 patterns
PRBS periods	2 ⁷ -1 to 2 ³¹ -1
Channel phase difference PRBS	1/4 phase difference for each channel
Data amplitude range	0.5 to 2.0 Vp-p
Data offset range	-2.0 to +2.0 VOH
Data to clock delay range	-500 to +500 ps
Amplitude adjustment	independent per channel
Offset adjustment	independent per channel
Clock source	built in synthesized clock signal
Maximum PRBS length	2 ³¹ -1 bits
Pattern types	programmable and pseudo random
Primary use	multi channel pulse pattern generation to 12.5 GHz
Form	benchtop pulse pattern generator
Replacement model class	MP1800A
Discontinuance class	December 2002 OEM notice
Independent channel control	yes for amplitude offset delay
Clock outputs count	2

PARAMETER	VALUE
Data outputs count	4
Bit rate class	12.5 Gb/s class generation
I/O class	differential high speed data/clock outputs typical
Remote control class	GPIB family typical for MP17xx
Operating temperature class	standard lab environment
Power class	AC mains powered benchtop instrument Notes Specs from Anritsu MP1758A product page and datasheet summaries (100 MHz–12.5 GHz; 4 data / 2 clock; 128 kbit programmable; PRBS to $2^{31}-1$; 0.5–2.0 Vp-p; -2.0–+2.0 VOH; ± 500 ps delay). Confirm installed options and connector condition on the unit.
Operating temperature	class: standard lab environment

General Specifications & Supplementary Characteristics

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Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Data to clock delay range	-500 to +500 ps
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Notes

Specs from Anritsu MP1758A product page and datasheet summaries (100 MHz–12.5 GHz; 4 data / 2 clock; 128 kbit programmable; PRBS to 2³¹-1; 0.5–2.0 Vp-p; -2.0–+2.0 VOH; ±500 ps delay). Confirm installed options and connector condition on the unit.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.