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Data Sheet

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ANRITSU

Anritsu MP1800A – BERT Signal Quality Analyzer Mainframe

Anritsu MP1800A Signal Quality Analyzer Mainframe The Anritsu MP1800A is a modular Signal Quality Analyzer (BERT) mainframe for physical-layer testing of high-speed optical modules and digital interfaces. Plug-in PPG, ED, and jitter modules support bit rates from about 0.1 to 32.1 Gbit/s in-chassis (application-dependent), with expansion to 56/64.2 Gbit/s using external MUX/DEMUX (e.g. MP1861A/MP1862A). Up to six modules install in the chassis. Successor platform is MP1900A. 100 GbE (25G×4), OTU-4 (28G×4), 32G DP-QPSK, CEI-28G, 32G Fibre Channel, InfiniBand EDR; multi-channel BER; jitter tolerance (SJ/RJ/BUJ/SSC with JMS modules); PAM signal generation/BER with converters; R&D and production signal-integrity test. The Anritsu MP1800A is a modular Signal Quality Analyzer (BERT) mainframe for physical-layer testing of high-speed optical modules and digital interfaces. Plug-in PPG, ED, and jitter modules support bit rates from about 0.1 to 32.1 Gbit/s in-chassis (application-dependent), with expansion to 56/64.2 Gbit/s using external MUX/DEMUX (e.g. MP1861A/MP1862A). Up to six modules install in the chassis. Successor platform is MP1900A. 100 GbE (25G×4), OTU-4 (28G×4), 32G DP-QPSK, CEI-28G, 32G Fibre Channel, InfiniBand EDR; multi-channel BER; jitter tolerance (SJ/RJ/BUJ/SSC with JMS modules); PAM signal generation/BER with converters; R&D and production signal-integrity test.



MODEL

Anritsu MP1800A

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

MP1800A

LISTING

<https://aumictechlabs.com/products/mp1800a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Anritsu MP1800A is a modular, plug-in Bit Error Rate Tester (BERT) designed for signal integrity analysis across digital communication systems. It combines a Pulse Pattern Generator (PPG) for high-quality signal output with an Error Detector (ED) offering high input sensitivity. The system performs burst pattern, Bathtub Jitter, and Eye Diagram measurements, supporting bit rates from 0.1 Gbit/s to 32.1 Gbit/s, with configurations extending to 56 Gbit/s via multiplexer modules and up to 100 Gbit/s from a single chassis. Multi-channel synchronization supports up to 8 channels in the 32 Gbit/s band, with configurable slot arrangements for serial and parallel applications. The platform integrates jitter generation and analysis, PAM signal support, and pre-emphasis capabilities, making it suitable for validating high-speed backplanes, 400 GbE interfaces, and advanced interconnect technologies.

Technical Specifications

Bit Rate Range: 0.1 Gbit/s to 32.1 Gbit/s (standard); up to 56 Gbit/s with MP1821A MUX and MP1822A DEMUX modules; up to 100 Gbit/s single-chassis output with appropriate configurations

Channel Configuration: Up to 8 channels at 32 Gbit/s via multi-channel synchronization; 1, 2, or 4 channels per PPG or ED module; 2, 4, or 6 slot chassis options

Clock Jitter (Intrinsic): Less than 350 fs rms (MU181500B Jitter Modulation Source)

Jitter Generation Types: Single-tone and dual-tone SJ, RJ, BUJ, SSC; simultaneous injection of RJ, BUJ, SSC, and dual SJ (PCIe compliance)

BER Analysis: Supports low bit error rate estimation ($1E-12$, $1E-15$) from phase transitions

Emphasis: 2 and 3-tap pre-emphasis (with MP1825B 4Tap Emphasis module) up to 32.1 Gbit/s

PAM Support: PAM4 and PAM8 signal generation with dedicated converter modules

Key Features

- High-quality, low-jitter waveform output
- Built-in Bathtub Jitter analysis with clock delay function
- Q Measurement, Eye Margin, Bit Error Analysis via ISI, and Eye Diagram tools
- Jitter Tolerance testing for multiple standards
- Phase-dependent bit error rate analysis

Typical Applications

- High-speed backplane validation
- 400 GbE and advanced Ethernet interface testing
- PCIe compliance and jitter tolerance measurement
- Signal integrity analysis for digital communications

Compatibility & Integration

Modular architecture supports PPG, ED, synthesizer, and optical interface modules

Compatible with MP1821A MUX and MP1822A DEMUX for extended bit rates

Integrates MP1825B pre-emphasis module for high-speed interconnect characterization

Features & description

From manufacturer datasheet

Features

- Modular BERT mainframe; max 6 plug-in modules
- Bit-rate class with modules: 0.1 to 32.1 Gbit/s; to 56/64.2 Gbit/s with MP1861A/MP1862A MUX/DEMUX
- PPG: high-quality / high-amplitude outputs (module-dependent)
- ED: high-sensitivity input; bathtub, eye-diagram, Q analysis (module-dependent)
- Jitter Modulation Source support: SJ/RJ/BUJ/SSC for jitter-tolerance tests
- Multi-channel sync; multi-box expansion class for high aggregate throughput
- Control software: MX180000A; USB mouse included in standard composition
- Remote: GPIB [MP1800A-001], LAN 2 ports [MP1800A-002]
- Option: 4-slot PPG and/or ED [MP1800A-015]
- Power: 100–120 / 200–240 VAC auto-switching, 47.5–63 Hz; ≤600 VA
- Operating temperature: 5° to 40°C
- Dimensions / mass (without modules): 320 (W) × 177 (H) × 450 (D) mm; ≤13 kg

MP1800A Characteristics / Specifications

PARAMETER	VALUE
Model	MP1800A
Manufacturer	Anritsu
Instrument Type	Signal Quality Analyzer / BERT Mainframe
Alias	MP1800A; Anritsu MP1800A; SQA MP1800A Mainframe
Module slots	6
Remote interface	GPIO (opt. 001); LAN 2 ports (opt. 002)
Power supply	100–120 VAC / 200–240 VAC auto-switching, 47.5–63 Hz
Power consumption	≤600 VA
Operating temperature	5° to 40°C
Dimensions	320 × 177 × 450 mm (W×H×D)
Mass	≤13 kg without modules Standard composition (installation guide) Mainframe MP1800A ×1; wrist strap Z0306A; operation manual CD Z0897A; power cord; front cover B0329G; protective cover B0574A; USB mouse Z0541A; MX180000A software CD Z0918A; ESD discharger G0342A; GND cable J1627A
ED sensitivity examples	~10 mV typical eye height (single-end) / ~25 mV typical eye amplitude class
PPG intrinsic RJ examples	~200 fs rms typical (module-dependent)
PPG amplitude examples	0.5 to 3.5 Vp-p class (option-dependent) Notes This listing is the MP1800A mainframe. Bit rate, sensitivity, jitter, and channel count depend entirely on installed plug-in modules and options—verify faceplate/module labels. Do not assume 32G or 64G capability without matching PPG/ED/MUX hardware. Replaced in Anritsu lineup by MP1900A series.
Bit-rate class with modules	0.1 to 32.1 Gbit/s; to 56/64.2 Gbit/s with MP1861A/MP1862A MUX/DEMUX
PPG	high-quality / high-amplitude outputs (module-dependent)
Jitter Modulation Source support	SJ/RJ/BUJ/SSC for jitter-tolerance tests
Control software	MX180000A; USB mouse included in standard composition
Remote	GPIO [MP1800A-001], LAN 2 ports [MP1800A-002]

PARAMETER	VALUE
Option	4-slot PPG and/or ED [MP1800A-015]
Power	100–120 / 200–240 VAC auto-switching, 47.5–63 Hz; ≤600 VA
Dimensions / mass (without modules)	320 (W) × 177 (H) × 450 (D) mm; ≤13 kg

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	5° to 40°C - Dimensions / mass (without modules): 320 (W) × 177 (H) × 450 (D) mm; ≤13 kg
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Model	MP1800A
Manufacturer	Anritsu
Instrument Type	Signal Quality Analyzer / BERT Mainframe
Alias	MP1800A; Anritsu MP1800A; SQA MP1800A Mainframe
Module slots	6
Remote interface	GPIOB (opt. 001); LAN 2 ports (opt. 002)
Power supply	100–120 VAC / 200–240 VAC auto-switching, 47.5–63 Hz
Power consumption	≤600 VA
Dimensions	320 × 177 × 450 mm (W×H×D)
Mass	≤13 kg without modules Standard composition (installation guide) Mainframe MP1800A ×1; wrist strap Z0306A; operation manual CD Z0897A; power cord; front cover B0329G; protective cover B0574A; USB mouse Z0541A; MX180000A software CD Z0918A; ESD discharger G0342A; GND cable J1627A
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Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	MP1800A
Manufacturer	Anritsu
Instrument Type	Signal Quality Analyzer / BERT Mainframe
Alias	MP1800A; Anritsu MP1800A; SQA MP1800A

Mainframe
Module slots: 6
Bit-rate capability (with modules): 0.1–32.1 Gbit/s class; 56/64.2 Gbit/s with external MUX/DEMUX
Remote interface: GPIB (opt. 001); LAN 2 ports (opt. 002)
Option MP1800A-015: 4-slot PPG and/or ED

Specifications

PARAMETER	VALUE
Power supply	100–120 VAC / 200–240 VAC auto-switching, 47.5–63 Hz
Power consumption	≤600 VA
Operating temperature	5° to 40°C
Dimensions	320 × 177 × 450 mm (W×H×D)
Mass	≤13 kg without modules

Standard composition (installation guide)
Mainframe MP1800A ×1; wrist strap Z0306A; operation manual CD Z0897A; power cord; front cover B0329G; protective cover B0574A; USB mouse Z0541A; MX180000A software CD Z0918A; ESD discharger G0342A; GND cable J1627A

Example module performance (32G class literature; confirm installed modules)

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PPG amplitude examples	0.5 to 3.5 Vp-p class (option-dependent)

Notes
This listing is the MP1800A mainframe. Bit rate, sensitivity, jitter, and channel count depend entirely on

installed plug-in modules and options—verify faceplate/module labels. Do not assume 32G or 64G capability without matching PPG/ED/MUX hardware. Replaced in Anritsu lineup by MP1900A series.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.