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Data Sheet

August 13, 2026

ANRITSU

Anritsu MU183040A

Anritsu MU183040A 28G/32G bit/s Error Detector Module The Anritsu MU183040A is a plug-in 28G/32G bit/s error detector module for the MP1800A / MT1810A Signal Quality Analyzer Series. It measures PRBS, Data, Zero-Substitution, Mixed, and Sequence patterns for R&D and production of high-speed digital and optical devices. Standard rate is 2.4–28.1 Gbit/s, extendable to 32.1 Gbit/s with option x01. The Anritsu MU183040A is a plug-in 28G/32G bit/s error detector module for the MP1800A / MT1810A Signal Quality Analyzer Series. It measures PRBS, Data, Zero-Substitution, Mixed, and Sequence patterns for R&D and production of high-speed digital and optical devices. Standard rate is 2.4–28.1 Gbit/s, extendable to 32.1 Gbit/s with option x01.



MODEL

**Anritsu
MU183040A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

MU183040A

LISTING

<https://aumictechlabs.com/products/mu183040a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Anritsu MU183040A is a 28/32 Gbit/s Error Detector module for the MP1800A Signal Quality Analyzer platform. This plug-in unit performs high-speed signal integrity analysis, bit error rate testing, and jitter characterization across data rates from 2.4 Gbit/s to 28.1 Gbit/s, extended to 32.1 Gbit/s with Option-001. Available in 1- or 2-channel configurations, the module accepts differential inputs via K-connector (50 ohm) with sensitivity of 50 mVp-p and typical input amplitude range of 0.25 Vp-p to 2.0 Vp-p. Technical Specifications Data Rate Coverage: 2.4 Gbit/s to 28.1 Gbit/s; 32.1

Gbit/s with Option-001 Channel Options: 1- or 2-channel (MU183040A); 4-channel variant (MU183041A) available Input Sensitivity: 50 mVp-p minimum Rise/Fall Time: 12 ps (20–80%) at 28.1 Gbit/s or 32.1 Gbit/s with 3.5 Vp-p amplitude Intrinsic Jitter: Typically 8 ps p-p (measured with <200 fs rms sampling scope) Amplitude Control: 500 mV to 2.0 Vp-p (Options 012, 022) or 500 mV to 3.5 Vp-p (Options 013, 023); 2 mV steps Data Delay: –1000 mUI to +1000 mUI with 2 mUI resolution (Option-030) Clock Output: Single-ended, K-connector, 50 ohm; full-rate (2.4 GHz to 32.1 GHz) or half-rate (1.2 GHz to 16.05 GHz); 0.3 Vp-p to 1.0 Vp-p amplitude External Clock Input: SMA-connector, 50 ohm; 1.2 GHz to 16.05 GHz; 0.3 to 1.0 Vp-p (–6.5 to +4.0 dBm)Key Features Total Jitter, Deterministic Jitter, and Random Jitter auto-measurements Bathtub jitter analysis and eye diagram capture Per-channel variable data skew for cross-talk testing Individual pattern synchronization per channel Full-rate and half-rate operational modes Error injection via SMA auxiliary inputTypical Applications High-speed backplane and interconnect signal integrity validation Module and card-level BER testing Multi-channel cross-talk analysis Jitter decomposition and eye quality assessmentCompatibility & Integration Installs in the MP1800A Signal Quality Analyzer or MT1810A 4-Slot Chassis. Not compatible for simultaneous use with MU181020A/B and MU181040A/B modules.

Features & description

From manufacturer datasheet

Features

- Operating bit rate 2.4–28.1 Gbit/s (to 32.1 Gbit/s with x01) • Differential Data/XData inputs; K connectors • Input sensitivity typical 50 mVp-p • Large user-programmable patterns (256 Mbits class)
- Threshold –3.5 V to +3.3 V (1 mV step) • Plug-in for MP1800A / MT1810A mainframes

Applications

- High-speed serial BER measurement to 28/32 Gbit/s • Multi-channel ED when combined with optioned configurations • Optical and electrical device production test • Pattern flexibility for standards compliance debugging

MU183040A Characteristics / Specifications

PARAMETER	VALUE
Model	MU183040A
Manufacturer	Anritsu
Instrument Type	28G/32G bit/s Error Detector Plug-in Module
Host Chassis	MP1800A; MT1810A Signal Quality Analyzer Series
Operating Bit Rate Standard	2.4 to 28.1 Gbit/s
Operating Bit Rate with Opt x	2.4 to 32.1 Gbit/s
Data Inputs	Differential Data and XData
Data Connector	K (f.)
Input Sensitivity Typical	50 mVp-p
Phase Margin Typical	20 ps
Threshold Voltage	−3.5 V to +3.3 V, 1 mV step
Clock Input	Single-ended SMA (f.)
Clock Frequency Range	1.2 to 16.05 GHz
Clock Input Level	0.3 to 1.0 Vp-p (−6.5 to +4.0 dBm)
Clock Termination	AC/50 Ω
Patterns	PRBS; Data; Zero-Substitution; Mixed; Sequence
User Pattern Memory Class	256 Mbits
Dimensions	21 mm H × 234 mm W × 175 mm D (excl. protrusions)
Mass	2.5 kg max
Operating Temperature	15 to 35 °C
Storage Temperature	−20 to 60 °C
Related 4ch Model	MU183041A

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Storage temperature	−20 to 60 °C
Operating temperature	15 to 35 °C
Operating Temperature	15 to 35 °C
Related 4ch Model	MU183041A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Model	MU183040A
Manufacturer	Anritsu
Instrument Type	28G/32G bit/s Error Detector Plug-in Module
Host Chassis	MP1800A; MT1810A Signal Quality Analyzer Series
Operating Bit Rate Standard	2.4 to 28.1 Gbit/s
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Operating Temperature	15 to 35 °C
Storage Temperature	-20 to 60 °C
Related 4ch Model	MU183041A
High-Sensitivity Family: MU183040B / MU183041B	
Notes	
Specs from Anritsu MU183040A/41A/40B/41B Operation Manual. Confirm option x01 and channel options on the module label.	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.