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Data Sheet

August 2, 2026

ASTRONICS / BALLARD TECHNOLOGY

Astronics / Ballard Technology LP1553-3A PCI to MIL-STD-1553 Interface Board

The Astronics/Ballard Technology LP1553-3A is a PCI interface board that bridges host computer systems to MIL-STD-1553B data buses. It enables Bus Controller, Remote Terminal, and Bus Monitor operation modes - with advanced models supporting simultaneous BC, Monitor, and multiple RT functions. The on-board DSP core handles 1553 protocol processing independently, reducing host PC overhead and enabling real-time autonomous operation. Configurable RAM from 256 KB to 1 MB with flexible message buffering accommodates diverse simulation, test, and data acquisition workloads. A programmable 32-bit time-stamp capability supports precise event correlation in time-critical applications. Operating Modes: Bus Controller (BC), Remote Terminal (RT), Bus Monitor (BM); concurrent operation supported on select models Operating Systems: Windows 95/98/NT/Me/2000/XP driver support; Linux and VxWorks drive



MODEL

**Astronics / Ballard
Technology
LP1553-3A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

LP1553-3A

LISTING

<https://aumictechlabs.com/products/lp1553-3a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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Features & description

From manufacturer datasheet

Features

- PCI to MIL-STD-1553B interface
- Concurrent BC, RT, and monitor capability
- On-board RAM about 256 KB to 1 MB (configuration dependent)
- Programmable 32-bit time-stamp
- Plug-and-play PCI
- Single/multiple terminal configurations in family
- Compatible with MIL-STD-1553B
- Related: LP1553-3C; LP1553-5 series

LP15533A Characteristics / Specifications

PARAMETER	VALUE
Model	LP1553-3A
Manufacturer	Astronics Ballard Technology (Ballard Technology)
Instrument Type	PCI MIL-STD-1553 interface board
Protocol	MIL-STD-1553B
Functions	Bus Controller, Remote Terminal, Monitor
Memory	about 256 KB to 1 MB on-board RAM (config dependent)
Host Bus	PCI (plug-and-play)
Time Stamp	programmable 32-bit
Connector	BJ-77 / D-sub class front I/O (config dependent)
NSN Example	5998-01-573-1984 (LP-1553-3A)
Related P/N Example	18021 (Artisan configuration notes) Standard Operating Conditions Install incompatible PCI slot. Use approved 1553 transformers/cabling and bus termination. Observe host power and ESD practices for avionics interface cards.
Related	LP1553-3C; LP1553-5 series

LP15533A Specifications

PARAMETER	VALUE
Applications	MIL-STD-1553B test and simulation on PCI host computers; concurrent BC/RT/monitor avionics lab work; aerospace and defense ATE.
Key Features	- PCI to MIL-STD-1553B interface
Technical Specifications	Model: LP1553-3A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	LP1553-3A
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NSN Example	5998-01-573-1984 (LP-1553-3A)
Related P/N Example	18021 (Artisan configuration notes)

Standard Operating Conditions

Install incompatible PCI slot. Use approved 1553 transformers/cabling and bus termination. Observe host power and ESD practices for avionics interface cards.

Operational Notes

Confirm LP1553-3A vs 3C (terminals/RAM) and vs later LP1553-5 multi-channel cards. Match connector type and terminal count to the application.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.