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Data Sheet

August 2, 2026

ASTRONICS / EADS / RACAL

Astronics / EADS / Racal 1260-00C Slot 0 / Resource Manager

Astronics / EADS / Racal 1260-00C Slot 0 / Resource Manager The Astronics / EADS / Racal 1260-00C (126000C) is a single-slot C-size VXIbus Slot 0 controller and Resource Manager that transparently bridges IEEE-488 (GPIB) and VXIbus. It allows an IEEE-488 host to control VXI instruments as if they were GPIB devices, enabling integration of VXI chassis into existing GPIB test systems. As Slot 0 RM, the 1260-00C performs VXI device identification, address map configuration, static/dynamic configuration, self-test management, commander/servant hierarchy mapping, and normal operation startup. It supports VXIbus memory read/write, register-based and VME devices, trigger-line control, RS-232, and commander or servant roles. Transfer rates are about 350 kbps (PC memory to VXI memory) and up to about 1.6 MB/s maximum (signal cycle or burst mode). Standard onboard RAM is 512 kB with 1 MB optional (ordering examples 1260-00C-0113 for 512 kB, 1260-00C-0123 for 1 MB). The Astronics / EADS / Racal 1260-00C (126000C) is a single-slot C-size VXIbus Slot 0 controller and Resource Manager that transparently bridges IEEE-488 (GPIB) and VXIbus. It allows an IEEE-488 host to control VXI instruments as if they were GPIB devices, enabling integration of VXI chassis into existing GPIB test systems. As Slot 0 RM, the 1260-00C performs VXI device identification, address map configuration, static/dynamic co



MODEL

**Astronics / EADS /
Racal 1260-00C**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1260-00C

LISTING

[https://aumictechlabs.c
om/products/1260-00c
-2/](https://aumictechlabs.com/products/1260-00c-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Astronics/EADS/Racal 1260-00C is a VXIbus Slot 0 Resource Manager that bridges IEEE-STD-488 (GPIB) controllers to VXIbus instrument systems. It transparently translates GPIB signals and protocols into VXIbus instrument commands, enabling legacy GPIB controllers to manage VXIbus devices without host replacement. The module performs critical resource management functions including device identification, address mapping, configuration, self-test coordination, and commander/servant hierarchy establishment. It operates as either a VXIbus commander or servant, managing power-up resource allocation and device initialization when deployed in Slot 0.

Technical Specifications

Interface Bridge: Transparent conversion between IEEE-STD-488 (GPIB) and VXIbus protocols

VXIbus Master Mode: Accesses A16 and A24 address spaces using D16 and D08 (EO) data transfers

VXIbus Slave Mode: Supports A32, A24, A17, D17, and D08 (EO) data transfers

On-board RAM: Dual-port configuration to VXIbus via switchable A32 or A24 address space

Block Transfers: Bi-directional GPIB-to-VXIbus block transfer capability

GPIB Command Translation: Handles IEEE-STD-488 common commands including Device Clear (DCL) and Selective Device Clear (SDC)

Secondary Address Mapping: Routes GPIB communications to individual VXIbus instruments

Serial Poll Management: Manages GPIB serial poll functions per VXIbus instrument

Key Features

- Static and dynamic (switchless) VXIbus configuration
- VXIbus trigger line and protocol control
- Memory and register-based device access
- VME device control integration

Each controlled VXIbus device appears as a unique GPIB instrument address

System self-test management and initiation of normal operation

Typical Applications

- Legacy GPIB system expansion into modular VXIbus architectures
- Mixed GPIB/VXIbus test environments
- System downsizing without controller replacement

Compatibility & Integration

- Operates within VXIbus mainframe Slot 0.
- Functions as both VXIbus commander and servant depending on system configuration requirements.

Features & description

From manufacturer datasheet

Features

- GPIB (IEEE-488) to VXI Slot 0 / Resource Manager
- Transparent GPIB↔VXI protocol conversion
- Device ID, address maps, static/dynamic configuration
- Self-test and commander/servant hierarchy mapping
- VXIbus memory R/W; register and VME device access
- VXI trigger line and protocol control
- RS-232 port (full/half duplex; rates to 19.2 kbps class)
- PC memory to VXI about 350 kbps; burst up to about 1.6 MB/s
- 512 kB RAM standard; 1 MB optional
- VXIbus 1.4 compliant; VXIplug&play / VISA driver class

126000C Characteristics / Specifications

PARAMETER	VALUE
Model	1260-00C (126000C)
Manufacturer	Astronics Test Systems / EADS / Racal Instruments
Instrument Type	VXI Slot 0 controller / Resource Manager; GPIB-to-VXI bridge
Form Factor	Single-slot C-size VXI (Slot 0)
GPIB Interface	IEEE-STD-488.1 bi-directional (front panel)
VXI Interface	VXIbus 1.4; A16/A24/A32 address spaces
Transfer Rate (PC mem to VXI mem)	about 350 kbps
Maximum Transfer Rate	about 1.6 MB/s (signal cycle or burst mode)
Address Counters	24 bits wide
Transfer Counters	16 bits wide
Onboard RAM	512 kB standard; 1 MB optional
Front Panel I/O	GPIB; TTL trigger in/out (BNC); CLK10 in/out (BNC); reset pushbutton
Ordering Examples	1260-00C-0113 (512 kB); 1260-00C-0123 (1 MB)
Related	1260-00B predecessor class Standard Operating Conditions Install in Slot 0 ofacompatible VXI mainframe. Configure address maps and commander/servant roles before normal operation. Use approved GPIB and VXI cabling and termination.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	1260-00C (126000C)
Manufacturer	Astronics Test Systems / EADS / Racal Instruments
Instrument Type	VXI Slot 0 controller / Resource Manager; GPIB-to-VXI bridge
Form Factor	Single-slot C-size VXI (Slot 0)
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VXI Interface	VXIbus 1.4; A16/A24/A32 address spaces
Transfer Rate (PC mem to VXI mem)	about 350 kbps
Maximum Transfer Rate	about 1.6 MB/s (signal cycle or burst mode)
Address Counters	24 bits wide
Transfer Counters	16 bits wide
Onboard RAM	512 kB standard; 1 MB optional

RS-232: 9-pin D-sub; 75-19.2 kbps; programmable data/stop/parity

Front Panel I/O: GPIB; TTL trigger in/out (BNC); CLK10 in/out (BNC); reset pushbutton

Ordering Examples: 1260-00C-0113 (512 kB); 1260-00C-0123 (1 MB)

Related: 1260-00B predecessor class

Standard Operating Conditions

Install in Slot 0 of a compatible VXI mainframe. Configure address maps and commander/servant roles before normal operation. Use approved GPIB and VXI cabling and termination.

Operational Notes

Confirm RAM size from ordering suffix (-0113 vs -0123) or firmware label. Related 1260-00B provides similar GPIB-to-VXI bridging with earlier revision features-match faceplate before expecting identical firmware options.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.