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Data Sheet

August 2, 2026

ASTRONICS / EADS / RACAL

Astronics / EADS / Racal 1260-14 96-Line Digital I/O, TTL Compatible Module

Astronics / EADS / Racal 1260-14 96-Line Digital I/O, TTL Compatible Module The Astronics / EADS / Racal 1260-14 isaC-size single-slot VXIbus digital I/O module providing 96 TTL-compatible channels in twelve groups of eight bits. Each channel is configurable as input or output on an 8-bit byte basis. Channels support asynchronous, synchronous, and mixed modes with programmable two-wire handshaking. TTL (and CMOS) I/O lines can be placed in high-impedance tri-state. Data rates reach static to about 200 kHz asynchronous register-based and about 800 Hz synchronous. Control is via Option 01 or Option 01T. Ordering 404916 is the TTL-compatible 1260-14; related 404916-001 is CMOS and 407164 is the open-collector 1260-14C. The Astronics / EADS / Racal 1260-14 isaC-size single-slot VXIbus digital I/O module providing 96 TTL-compatible channels in twelve groups of eight bits. Each channel is configurable as input or output on an 8-bit byte basis. Channels support asynchronous, synchronous, and mixed modes with programmable two-wire handshaking. TTL (and CMOS) I/O lines can be placed in high-impedance tri-state. Data rates reach static to about 200 kHz asynchronous register-based and about 800 Hz synchronous. Control is via Option 01 or Option 01T. Ordering 404916 is the TTL-compatible 1260-14; related 404916-001 is CMOS and 407164 is the open-collector 1260-14C. TTL digital I/O in VXI



MODEL

**Astronics / EADS /
Racal 1260-14**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1260-14

LISTING

[https://aumictechlabs.c
om/products/1260-14-
2/](https://aumictechlabs.com/products/1260-14-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Astronics/EADS/Racal 1260-14 is a 96-line digital I/O module for high-density interfacing in industrial and aerospace test systems. This single-slot, C-size VXIbus module delivers twelve independently configurable 8-bit ports with TTL, CMOS, or Open Collector logic level support. Each channel configures independently as input or output, enabling flexible signal routing for data acquisition and control applications. The module operates in asynchronous, synchronous, or mixed modes, with optional two-wire programmable handshaking for managed I/O transactions. Data representation options include hex, decimal, and binary formats.

Technical Specifications
Channels: 96 total, organized as twelve 8-bit ports
Logic Levels: TTL compatible, CMOS compatible, Open Collector (OC)
Output Voltage (TTL): $V_{out} (high) \geq 2.4 \text{ V}$ @ -15 mA
Output Voltage (CMOS): $V_{out} (high) \geq 3.8 \text{ V}$ @ 6 mA
Output Voltage (Open Collector): 5 V to 32 V
Input Resistance: $> 500 \text{ k}\Omega$
Current Sink (OC): Up to 200 mA per channel
External Pull-up Supply (OC): Up to 32 VDC ; 1 A maximum per Vext pin
Interface: Message-based (Option 01) or message-based + register-based (Option 01T)
Form Factor: Single-slot, C-size, message-based VXIbus module

Key Features
Per-group internal or external pull-up selection for Open Collector outputs
Programmable handshake control - each line configurable as active high or active low
Pseudo tri-state output capability via software control (1260-14C)
Suitable for driving external circuitry, switches, relays, and digital logic devices
Inductive load applications require external voltage supply to protect output stage

Typical Applications
Industrial equipment control and monitoring
Aerospace test and diagnostic systems
Switch and relay control networks
Multi-channel data acquisition and signal routing
Mixed-signal test benches with synchronous timing requirements

Compatibility & Integration
The 1260-14 integrates into VXIbus systems via message-based or hybrid message/register-based command architectures. Configurable logic levels enable interfacing with legacy TTL equipment, modern CMOS systems, and high-voltage Open Collector circuits operating up to 32 VDC .

Features & description

From manufacturer datasheet

Features

- 96 TTL-compatible digital I/O channels (12 groups of 8)
- Per-byte input/output direction
- Tri-state (high-Z) capability on TTL/CMOS versions
- Async, sync, and mixed operating modes
- Two-wire programmable handshaking (active high/low)
- Data rate static to about 200 kHz async (register-based)
- Option 01 or 01T control
- Single-slot C-size VXI module
- Ordering: 404916 (TTL); related CMOS 404916-001; OC 1260-14C 407164

1260 Characteristics / Specifications

PARAMETER	VALUE
Model	1260-14
Manufacturer	Astronics Test Systems / EADS / Racal Instruments
Instrument Type	96-line TTL digital I/O VXI module
Form Factor	C-size, single-slot VXI
Channels	96; twelve 8-bit ports
Logic Family	TTL compatible (404916); CMOS option 404916-001
TTL Vout High	greater than or equal to 2.4 V @ -15 mA
TTL Vout Low	less than or equal to 0.5 V @ 48 mA
TTL Vin High / Low	greater than or equal to 2.0 V / less than or equal to 0.8 V; Vin max 5.25 V
Data Rate	static to about 200 kHz asynchronous register-based; about 800 Hz synchronous
Operating Modes	asynchronous; synchronous; mixed
Control	Option 01 or Option 01T
Weight	about 2.69 lb without Option 01/01T; 2.97 lb with
Ordering	404916 Standard Operating Conditions Install Option 01/01T in the leftmost 1260-series module of the switch/I/O set. Observe TTL voltage/current limits. Use mating IDC connectors supplied or ordered (50-pin / 60-pin family P/Ns).

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

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Operating Modes	asynchronous; synchronous; mixed
Control	Option 01 or Option 01T

Power (+5 V): about 2.38 A without Option 01/01T pull-up use; about 4.78 A with Option 01/01T installed (family datasheet)
Weight: about 2.69 lb without Option 01/01T; 2.97 lb with
Ordering: 404916

Standard Operating Conditions
Install Option 01/01T in the leftmost 1260-series module of the switch/I/O set. Observe TTL voltage/current limits. Use mating IDC connectors supplied or ordered (50-pin / 60-pin family P/Ns).

Operational Notes
Do not confuse with 1260-14C open-collector (407164). Faceplate must read 1260-14 TTL for this datasheet. CMOS variant uses 404916-001 electrical levels.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.