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Data Sheet

August 15, 2026

ASTRONICS / EADS / TALON INSTRUMENTS

Astronics / EADS / Talon Instruments SR101 Timing Set Module

Talon Instruments SR101 Timing Set Module for SR192 The Talon SR101 is the timing-set module that provides all timing controls for SR192 I/O modules during stimulus/response or bus-emulation sequences. OEM SR192 family literature lists timing rates to 50 MHz, data rates to 25 MHz (50 MHz multiplexed), 128 Kbits memory per channel (256K multiplexed), five-memory architecture, and 50 mA source/sink TTL levels on the digital modules the SR101 clocks. Multiple SR192 modules in a master configuration support up to 1152 channels in a 13-slot chassis. The Talon SR101 is the timing-set module that provides all timing controls for SR192 I/O modules during stimulus/response or bus-emulation sequences. OEM SR192 family literature lists timing rates to 50 MHz, data rates to 25 MHz (50 MHz multiplexed), 128 Kbits memory per channel (256K multiplexed), five-memory architecture, and 50 mA source/sink TTL levels on the digital modules the SR101 clocks. Multiple SR192 modules in a master configuration support up to 1152 channels in a 13-slot chassis. Master timing for Talon SR192 digital stimulus/response and bus-emulator ATE.

**MODEL****Astronics / EADS /
Talon Instruments
SR101****CALIBRATION****Available on
request****CONDITION****Used****STATUS****In stock****RFQ / SKU****SR101****LISTING**[https://aumictechlabs.c
om/products/sr101-2/](https://aumictechlabs.com/products/sr101-2/)

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NAICS 423690 • 811210 • 541380

This unit

The SR101 Timing Set Module is a precision timing generator engineered for the SR192 Digital Test Module system. It orchestrates I/O module stimulus/response operations and bus emulation by generating accurate timing signals and memory address/control signals. Each SR101 controls up to six I/O modules and operates at timing rates to 50 MHz with data rates to 25 MHz (50 MHz multiplexed). The module installs directly in the SR192 motherboard - a 2-slot "C" size VXI module - and supports one or two SR101 modules per motherboard.

Technical Specifications

- Timing Rates:** Up to 50 MHz
- Data Rates:** 25 MHz standard; 50 MHz multiplexed option
- Memory Architecture:** 5-memory design (Stimulus, Tristate, Record, Expect, Mask)
- Memory Depth:** 128K bits per channel; 256K bits multiplexed
- Test Sequence Memory:** 128K
- I/O Module Control Capacity:** Up to 6 modules per SR101
- Logic Standard:** TTL voltage levels
- Source/Sink Capability:** 50 mA
- Clock Resolution:** DC to 50 MHz (TS CLOCK signal determines minimum cell resolution)
- Clock Source:** Internal or external; SR210 Accessory Module provides programmable source (100 Hz to 50 MHz)

Key Features

- Dual timing module support per SR192 motherboard for enhanced sequencing capability
- Advanced sequencing via SR192 Development Environment software
- Control signal generation to Unit Under Test with UUT signal testing capability
- TTL-level I/O with 50 mA source/sink per channel
- Compatible with SR115 bi-directional 16-channel I/O modules

Typical Applications

- Digital stimulus/response testing
- Bus emulation and protocol validation
- Synchronized multi-module test sequences
- High-speed logic signal generation and capture

Compatibility & Integration

The SR101 integrates into VXI bus architecture via the SR192 motherboard, which accommodates up to 12 I/O modules, two timing modules, and one D/A module per chassis. The SR210 Accessory Module supplies programmable clock sources ranging from 100 Hz to 50 MHz for both internal timing and external device synchronization.

SR101 Characteristics / Specifications

PARAMETER	VALUE
Model	SR101
Instrument Type	Timing set module
Host System	SR192 digital test module
Timing Rate Family	timing rates to 50 MHz
Data Rate Family	data rates to 25 MHz, 50 MHz multiplexed
Memory Per Channel Family	128 Kbits per channel, 256K multiplexed
Memory Architecture Family	Stimulus, Tristate, Record, Expect, and Mask
Ttl Drive Family	50 mA source/sink TTL voltage levels on SR192 I/O
I/O Module Capacity	SR192 houses up to 12 I/O modules
Channel Capacity	96 or 192 channels per SR192; up to 1152 channels in a 13-slot chassis master configuration
Related Variable Voltage	SR214 / SR214A
Related Ttl	SR103 TTL I/O
Related Dac	SR110 DAC
Platform	VXI
Manufacturer	Talon Instruments / EADS / Astronics
Use Case	SR192 digital timing
Literature	Talon SR192 / SR101 product listings
Family	Talon SR192 digital test
Documentation Type	OEM SR101 / SR192 family table
Brand Lineage	Talon EADS Astronics
System Role	digital timing set
Form Factor	SR192 timing module
Related 50 Mhz Digital	SR192A 50 MHz digital module with 5 ns edge placement
Bus Emulation	timing for stimulus/response or bus interface emulation
Chassis Class	13-slot VXI master configurations

PARAMETER

VALUE

Data Rate

Family: data rates to 25 MHz, 50 MHz multiplexed

SR101 Specifications

PARAMETER	VALUE
Talon Instruments SR101 Timing Set Module for SR192	Overview
Applications	Master timing for Talon SR192 digital stimulus/response and bus-emulator ATE.
Key Features	SR192 timing set
Timing rates to 50 MHz	Data rates to 25 MHz family
Clocks up to 12 I/O modules	VXI digital ATE timing
Technical Specifications	Model: SR101

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Model	SR101
Instrument Type	Timing set module
Host System	SR192 digital test module
Timing Rate Family	timing rates to 50 MHz
Data Rate Family	data rates to 25 MHz, 50 MHz multiplexed
Memory Per Channel Family	128 Kbits per channel, 256K multiplexed
Memory Architecture Family	Stimulus, Tristate, Record, Expect, and Mask
Ttl Drive Family	50 mA source/sink TTL voltage levels on SR192 I/O
I/O Module Capacity	SR192 houses up to 12 I/O modules
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Related Variable Voltage	SR214 / SR214A
Related Ttl	SR103 TTL I/O
Related Dac	SR110 DAC
Platform	VXI
Manufacturer	Talon Instruments / EADS / Astronics
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Family	Talon SR192 digital test
Documentation Type	OEM SR101 / SR192 family table
Brand Lineage	Talon EADS Astronics
System Role	digital timing set
Form Factor	SR192 timing module
Related 50 Mhz Digital	SR192A 50 MHz digital module with 5 ns edge placement
Bus Emulation	timing for stimulus/response or bus interface emulation
Chassis Class	13-slot VXI master configurations

Notes

From Talon SR192/SR101 listings (50 MHz timing, 25 MHz data, 128 Kbit, 12 I/O modules, 1152-channel master).
Clock-set count and jitter are on the SR101 datasheet.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.