

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

Data Sheet

August 15, 2026

BP MICROSYSTEMS

BP Microsystems BP-1200 Universal Programmer

The BP Microsystems BP-1200 is a universal programmer engineered for high-volume production environments. It has support for a broad spectrum of programmable devices, including microcontrollers, memory chips, and programmable logic devices, distinguishing itself with an advanced architecture and high-speed programming algorithms that ensure exceptional throughput and reliability for demanding programming applications. Aumictech offers NIST-traceable calibration and repair for the BP Microsystems BP-1200. Calibration covers analog input channel accuracy and offset at all gain settings, analog output accuracy, digital I/O logic threshold verification, sample rate and timing accuracy, and trigger and synchronization bus functional verification. NI CompactRIO and PXI chassis also undergo backplane trigger line integrity check.. All calibrations ship with a NIST-traceable certificate. Turnar



MODEL	CALIBRATION
BP Microsystems BP-1200	Available on request
CONDITION	STATUS
Used	In stock
RFQ / SKU	LISTING
BP-1200	https://aumictechlabs.com/products/bp-1200-2/

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The BP Microsystems BP-1200 is a universal programmer engineered for high-volume production environments. It has support for a broad spectrum of programmable devices, including microcontrollers, memory chips, and programmable logic devices, distinguishing itself with an advanced architecture and high-speed programming algorithms that ensure exceptional throughput and reliability for demanding programming applications. Technical Specifications * Device Support: Supports a wide range of programmable devices, including complex PLDs, FPGAs, PALs, and microcontrollers. New devices are

added approximately every six weeks, and these updates are provided free for the life of the programmer. The BP-1200 can support future devices and stay current with evolving semiconductor technology. It is expandable to program and test devices with up to 240 pins. Pin Drivers: Available with 48 or 84 pin drivers, with an option for field upgrades to 84 pins. Can be configured with 32, 40, 48, 84, 144, or 240 pin drivers. Field upgrades to higher pin counts are possible with minimal tools. Up to 48 analog pin drivers and 192 digital pin drivers can be utilized for up to 240-pin devices. Programming Algorithms: Employs unique algorithms for each device to ensure correct programming. The device programming algorithms are user-selectable software modules that interface with the primary algorithm. Supports asynchronous and synchronous designs with multiple clocks by switching multiple pins simultaneously, allowing for the application of two clocks or an entire test vector simultaneously. Performance: Achieves high programming yields and proper test results due to clean signals and low ground bounce. Can program small EPROMs and PALs in one to two seconds. An 8 Mb flash EPROM can be programmed in under two minutes. Utilizes an 80826 CPU with a proprietary hardware accelerator for performance. Testing Capabilities: * Full vector test and continuity test. * Vcc voltages can be set or swept to identify device margins. * Verify and test passes are performed at high and low Vcc to guarantee operating margin. * Built-in self-test verifies correct operation of pin drivers, power supply, CPU, memory, and communications. * Ground bounce is carefully controlled to improve test results and programming yields, especially for high-speed parts. Software: * Highly acclaimed control software that is versatile, powerful, and intuitive for both novices and experts. * Automatic package selection allows for socket changes without power-down or software configuration changes. * Software package is delivered as a single file for ease of deployment. * BP JobMaster™ control software incorporates the use of ".bp" files, which ensure proper job setup and secure data, reducing errors. * Free software updates are available on the manufacturer's website. Device Data Management: ".bp" files are error-checked automatically to reduce human error by only running tested and verified jobs. These files prevent inadvertent modifications. JobMaster files facilitate easy transfer of ".bp" files, including via email. Hardware Features * Modular Design: Based on solid modular hardware and innovative design for performance. Signal Integrity: Delivers clean signals and low ground bounce for high programming yields. Sophisticated active circuitry within socket modules and cards provides clean waveform signals by minimizing noise, ground bounce, and overshoot. Pin Monitoring: Each pin driver is continuously monitored and calibrated by a supervisory circuit during operation. Interfaces * Computer Connection: Connects to a PC's standard parallel printer port (LPT port). Power Input: Features an IEC inlet. Physical * Maintenance: Requires no specific maintenance. Compatibility * Socket Modules: Interchangeable socket modules facilitate switching between various device package types. Socket modules are available for LCC, PGA, PLCC, PCMCIA, QFP, SOIC, SIMM, µBGA, and TSOP packages, among others. A univer

BP1200 Characteristics / Specifications

PARAMETER	VALUE
Model	BP-1200
Instrument Type	universal device programmer
Maximum Pin Support	240 pins
Analog Pin Drivers Maximum	48
Digital Pin Drivers Maximum	192
Configurable Pin Counts	32 40 48 84 144 or 240
Field Upgrade Tool	screwdriver only
Cpu Class	80286 with proprietary hardware accelerator
Host Interface	PC standard parallel printer port
Host Memory Use	uses PC memory without programmer RAM upgrades
Continuity Test	every pin tested when part is placed
Verify Passes	high and low Vcc after programming
Protection	power-failure protection for device in socket
Pin Driver Monitoring	continuous supervisory calibration during operation
Low Voltage Device Support	yes
Software Updates	free about every six weeks
Automated Option	available
Warranty Class Oem	three year warranty stated on brochure
Socket Modules	interchangeable package adapters
Programming Speed Example Small Devices	one or two seconds class per brochure
Programming Speed Example 8 Mb Flash	under two minutes class per brochure
Architecture	modular analog plus digital pin drivers

BP1200 Specifications

PARAMETER	VALUE
Full vector and continuity test	Programs low-voltage devices
Technical Specifications	Model: BP-1200
Notes	Architecture and pin-count rows from BP Microsystems BP-1200 Universal Device Programmer OEM brochure.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Architecture	modular analog plus digital pin drivers

Notes

Architecture and pin-count rows from BP Microsystems BP-1200 Universal Device Programmer OEM brochure.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.