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Data Sheet

August 15, 2026

CORELIS

Corelis CAS-1000

The Corelis CAS-1000 is an integrated JTAG boundary-scan controller and I2C/SMBus analysis instrument designed for debug, validation, and in-system programming across development, production, and field environments. It combines real-time bus monitoring, traffic generation, device emulation, and structural testing in a single Windows-based platform. Standard-mode, Fast-mode, and Fast-mode Plus (Fm+) operation up to 5 Mbit/s Emulation & Master/Slave Operation Configurable master, slave, or dual-mode operation Electrical & Timing Measurement Automatic measurement and verification via Parameter Scope Traffic Generation & Stress Testing



MODEL

Corelis CAS-1000

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

CAS-1000

LISTING

<https://aumictechlabs.com/products/cas-1000-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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slaves concurrently Configurable master, slave, or dual-mode operation Electrical & Timing Measurement Graphical waveform display of I2C bus electrical and timing parameters Adjustable bus voltage reference Software-configurable pull-up resistors on SDA and SCL lines Automatic measurement and verification via Parameter Scope Traffic Generation & Stress Testing Glitch injection, protocol error generation, and slave clock stretching Active bus override capability for UUT stress testing Transaction generation for peripheral device communication Monitoring & Logging Passive traffic capture with state and timing recording Time stamping, message filtering, and symbolic translation Unlimited continuous logging to file Advanced trigger events for transaction isolation Bit and protocol-level timing waveform visualization Programming & JTAG In-system programming of I2C serial EEPROMs Flash memory and CPLD programming (with optional ScanExpress software) Integrated JTAG controller for boundary-scan test operations Key Features I2C Exerciser software with consolidated GUI for host PC control Windows-based interface for intuitive operation Combined I2C analysis and JTAG boundary-scan testing Real-time visibility into bus operations Typical Applications Product development and debug System integration and validation Production testing and in-system programming Field-level troubleshooting and maintenance Bus specification compliance verification Compatibility & Integration Optional Corelis ScanExpress software extends capabilities to structural boundary-scan testing and non-volatile memory programming.

CAS1000 Characteristics / Specifications

PARAMETER	VALUE
Model	CAS-1000
Also Known As	CAS-1000-I2C/E
Instrument Type	I2C SMBus analyzer exerciser and JTAG TAP
Part Number	90002
Bus Modes	Standard-mode, Fast-mode, Fast-mode Plus, High-speed mode
Data Rate Max	5 Mbit/s
Emulated Master	up to 1 concurrent with monitoring
Emulated Slaves	up to 10 concurrent with Hs-mode monitoring
Addressing	7-bit and 10-bit
Reference Voltage	programmable 0.8 V to 5 V
Pull Ups	programmable 250 ohm to 50 kohm on SDA and SCL
Gpio	two general-purpose I/O signals
Trigger In	SMB trigger in
Trigger Out	SMB trigger out
Jtag Port	IEEE-1149.1 TAP (mutually exclusive with I2C; TAP mode required)
Usb	high-speed USB 2.0 bus-powered
Usb Cable	6 foot USB 2.0 A to B included
I2c Connector	RJ45 AMP 406549-1
I2c Cable	12 inch flying leads for 0.025 inch square posts with test clips
Dimensions	5.48 × 1.00 × 4.66 plus or minus 0.25 inches
Certifications	RoHS compliant CE marked
Host Os	Microsoft Windows 7 8.1 and 10 class per comparison chart
Software	I2C Exerciser GUI plus C/C++ API
Manufacturer	Corelis
Pec	packet error checking generation and validation

PARAMETER

VALUE

Eeprom Isp

in-system programming of I2C serial EEPROMs

Data Rate

Max: 5 Mbit/s

CAS1000 Specifications

PARAMETER	VALUE
Applications	I2C/SMBus debug, electrical/timing compliance, EEPROM ISP, and optional boundary-scan/ISP via the auxiliary TAP.
Key Features	I2C/SMBus to 5 Mbit/s including Hs-mode
One master plus ten slave emulators with monitor	0.8 to 5 V reference and 250 ohm to 50 kohm pull-ups
IEEE 1149.1 auxiliary TAP	Bus-powered USB 2.0
Technical Specifications	Model: CAS-1000

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Eeprom Isp	in-system programming of I2C serial EEPROMs

Notes

From Corelis CAS-1000-I2C/E datasheet CAS-1000-DS-final and I2C bus analyzer comparison chart. Confirm ScanExpress license if the auxiliary TAP will be used.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.