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Data Sheet

August 15, 2026

CORELIS

Corelis PC-1149.1/100F Boundary-Scan Controller Module

The Corelis PC-1149.1/100F is a PCI bus boundary-scan controller module that delivers IEEE 1149.1 (JTAG) compliance for in-system testing and programming of complex electronic assemblies. Operating from the PCI slot, this module executes high-speed JTAG operations and leverages PCI bandwidth for efficient communication between test equipment and host systems. Engineers and technicians use it for advanced diagnostics, test vector execution, and device programming across diverse circuit board applications. JTAG Signals: Test Clock (TCK), Test Mode Select (TMS), Test Data In (TDI), Test Data Out (TDO), optional Test Logic Reset (TRST) Aumictech offers NIST-traceable calibration and repair for the Corelis PC-1149.1/100F. Calibration covers analog input channel accuracy and offset at all gain settings, analog output accuracy, digital I/O logic threshold verification, sample rate and timing



MODEL

Corelis PC-1149.1/100F

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

PC-1149.1/100F

LISTING

<https://aumictechlabs.com/products/pc-1149-1-100f-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

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bandwidth for efficient communication between test equipment and host systems. Engineers and technicians use it for advanced diagnostics, test vector execution, and device programming across diverse circuit board applications. Technical Specifications Host Interface: PCI Bus Standards Compliance: IEEE 1149.1 (JTAG) JTAG Signals: Test Clock (TCK), Test Mode Select (TMS), Test Data In (TDI), Test Data Out (TDO), optional Test Logic Reset (TRST) Power Supply: PCI slot Form Factor: PCI Module Key Features PCI bus integration for high-bandwidth data transfer JTAG signal generation and capture Support for standard boundary-scan test access port architecture Designed for efficient test vector execution and device programming Compatibility & Integration The PC-1149.1/100F integrates with Corelis ScanExpress software tools, including ScanExpress Runner for test automation. The Scan Function Library (SFL) enables integration with third-party applications and custom test environments. Compatibility extends across Windows 2000, Windows XP, and later operating systems depending on driver support. Related Corelis platforms support programmable I/O voltage from 1.25 V to 3.30 V in 0.05 V steps, with 5 V tolerance on certain signal lines - features typical across the Corelis product family. Typical Applications Boundary-scan test execution on production assemblies In-system programming of flash memory and FPGAs Device diagnostics and debugging Design validation and verification workflows

PC11491100F Characteristics / Specifications

PARAMETER	VALUE
Model	PC-1149.1/100F
Instrument Type	ISA boundary scan controller
Standard	IEEE 1149.1
Host Bus	PC ISA
Family	Corelis 1149.1 controllers
Related Pci Model	PCI-1149.1
Related Vxi Model	CVXI-1149.1
Tap Signals	TCK TMS TDI TDO TRST
Tck Control	programmable under software
Mechanical Package	ISA card
Manufacturer	Corelis
Target Use	interconnect test ISP and BIST
Scan Architecture	IEEE 1149.1 TAP
Software Host	IBM PC compatible
Multi Platform	USB PCI PCIe Ethernet family peers
Parallel Io Class	digital auxiliary I/O on family controllers
Event Class	asynchronous event support on family controllers
Clock Source Class	internal or external TCK on family controllers
Single Step Class	static TCK debug on family controllers
Operating Context	desktop PC ATE
Logic Family	TTL TAP levels class
Reset Path	TRST support
Chain Topology	ring or star per IEEE 1149.1

PC11491100F Specifications

PARAMETER	VALUE
Applications	PC-hosted interconnect test, in-system programming, and boundary-scan debug of IEEE 1149.1 boards.
ISA bus host interface	IEEE 1149.1 TAP generation
Technical Specifications	Model: PC-1149.1/100F

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

performance IEEE 1149.1 boundary-scan controller in the same 1149.1 hardware family as PCI-1149.1 and CVXI-1149.1. Corelis JTAG controller literature describes ISA high-performance scan control with programmable TCK, multi-TAP support, and ScanPlus software compatibility across USB/PCI/ISA platforms.

Applications

PC-hosted interconnect test, in-system programming, and boundary-scan debug of IEEE 1149.1 boards.

Key Features

ISA bus host interface

IEEE 1149.1 TAP generation

Programmable TCK

Software transportability across Corelis controllers

Pairs with ScanPlus/ScanExpress tool chain

Technical Specifications

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Notes

Identity from Corelis PC-1149.1/100F product listing and the Corelis JTAG Boundary-Scan Controller family datasheet index. Exact TCK maximum and TAP count for the 100F SKU are on the PC-1149.1/100F datasheet matching the board revision.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.