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Data Sheet

August 15, 2026

CORELIS

Corelis PCI-1149.1 JTAG Scan Controller Module

The Corelis PCI-1149.1 is a JTAG scan controller module for PCI bus systems, enabling in-system programming, boundary-scan testing, and device diagnostics. IEEE 1149.1 compliant, it delivers user-programmable JTAG TCK speeds up to 80 MHz and supports direct programming of I2C and SPI-based devices. The module accommodates CPLDs, Flash memories, serial PROMs, and FPGAs across concurrent testing and in-system programming workflows. I/O Capabilities: External write strobe and Ready/Busy signaling for Flash programming; 16 inputs and 16 outputs of programmable parallel I/O (PCI-1149.1/Turbo variant) Output Voltage (Variable on some models): Programmable from 1.25 V to 3.30 V in 0.05 V steps Threshold Voltage (Variable on some models): Programmable from 0.00 V to 3.30 V in 0.05 V steps



MODEL

Corelis PCI-1149.1

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

PCI-1149.1

LISTING

<https://aumictechlabs.com/products/pci-1149-1-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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Interface: 32-bit PCI bus JTAG Compliance: IEEE 1149.1 Standard JTAG TCK Speed: User-programmable up to 80 MHz Supported Devices: CPLDs, Flash memories, serial PROMs, FPGAs Programming Protocols: JTAG, direct I2C and SPI programming I/O Capabilities: External write strobe and Ready/Busy signaling for Flash programming; 16 inputs and 16 outputs of programmable parallel I/O (PCI-1149.1/Turbo variant) Output Voltage (Variable on some models): Programmable from 1.25 V to 3.30 V in 0.05 V steps Threshold Voltage (Variable on some models): Programmable from 0.00 V to 3.30 V in 0.05 V steps TAP Support: Up to 32 Test Access Ports when used with compatible ScanTAP series pods Cable Length: SCSI ribbon cable up to 30 feet for pod connectivity Key Features Concurrent (gang) testing and in-system programming (ISP) Monitoring of Flash RDY/BSY signals JTAG Embedded Test for microprocessor control without boot code SCSI ribbon cable connection to intelligent pods Typical Applications Boundary-scan testing, CPLD and Flash device programming, FPGA validation, serial PROM updates, and microprocessor diagnostics in production and field environments. Compatibility & Integration Operating Systems: Windows XP, Windows Vista, Windows 7 (32-bit and 64-bit), Linux Software: Compatible with Corelis ScanExpress family, including ScanExpress Runner Pod Integration: Works with Corelis ScanTAP intelligent pods for expanded TAP capacity

PCI11491 Characteristics / Specifications

PARAMETER	VALUE
Model	PCI-1149.1
Turbo Model	PCI-1149.1/Turbo
Instrument Type	JTAG boundary scan controller
Bus Interface	PCI
Standard	IEEE 1149.1
Manufacturer	Corelis
Form Factor	PCI plug-in card
Tck	programmable clock
Tap Support	multi TAP class
Software	Corelis ScanPlus class
Operating Temperature Class	commercial lab
Power	PCI slot power
Connector	JTAG pod interface
Humidity Class	non condensing
Duty	continuous ATE
Grounding	host PC chassis
Isolation Class	pod dependent
LEDs	status indicators class
Driver	Windows device driver class
Scan Modes	interconnect and cluster test
Isp Support	in system programming class
Storage Temperature Class	commercial
Cooling	host airflow
Storage temperature	Class: commercial
Operating temperature	Class: commercial lab

PARAMETER	VALUE
Humidity	Class: non condensing

PCI11491 Specifications

PARAMETER	VALUE
Applications	Boundary-scan interconnect test, in-system programming, and cluster testing of IEEE 1149.1 compliant boards.
Key Features	IEEE 1149.1 JTAG controller
Technical Specifications	Model: PCI-1149.1

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Storage temperature	Class: commercial
Operating temperature	Class: commercial lab
Operating Temperature	Class: commercial lab
Humidity	Class: non condensing
Operating Temperature Class	commercial lab
Power	PCI slot power
Connector	JTAG pod interface
Humidity Class	non condensing
Duty	continuous ATE
Grounding	host PC chassis
Isolation Class	pod dependent
LEDs	status indicators class
Driver	Windows device driver class
Scan Modes	interconnect and cluster test
Isp Support	in system programming class
Storage Temperature Class	commercial
Cooling	host airflow Notes From Corelis PCI-1149.1 / PCI-1149.1 Turbo product listings. Exact maximum TCK and TAP count follow the Corelis datasheet for the installed card revision.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

performance

Technical Specifications

Technical Specifications

PARAMETER	VALUE
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Notes

From Corelis PCI-1149.1 / PCI-1149.1 Turbo product listings. Exact maximum TCK and TAP count follow the Corelis datasheet for the installed card revision.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.