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Data Sheet

August 15, 2026

CORELIS

Corelis PCI-1149.1/Turbo JTAG Scan Controller Module

The Corelis PCI-1149.1/Turbo is a JTAG scan controller module designed for PCI bus integration. It streamlines boundary-scan testing, debugging, and in-system programming of complex electronic assemblies through IEEE 1149.1-compliant JTAG operations. The module delivers high-speed scan clock frequencies dependent on PCI bus limits and target device characteristics, with integrated buffering that minimizes latency between the host system and JTAG interface. TTL/CMOS signal levels support standard JTAG connections (TDI, TDO, TCK, TMS, TRST) and multiple independent scan chains for simultaneous testing across board sections or different devices. High-speed in-system programming (ISP) for FPGAs, CPLDs, microcontrollers, and serial Flash memory Aumictech offers NIST-traceable calibration and repair for the Corelis PCI-1149.1/Turbo. Calibration covers analog input channel accuracy and offset



MODEL

Corelis PCI-1149.1/Turbo

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

PCI-1149.1/Turbo

LISTING

<https://aumictechlabs.com/products/pci-1149-1-turbo-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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electronic assemblies through IEEE 1149.1-compliant JTAG operations. The module delivers high-speed scan clock frequencies dependent on PCI bus limits and target device characteristics, with integrated buffering that minimizes latency between the host system and JTAG interface. TTL/CMOS signal levels support standard JTAG connections (TDI, TDO, TCK, TMS, TRST) and multiple independent scan chains for simultaneous testing across board sections or different devices. Technical Specifications JTAG Interface IEEE 1149.1 (JTAG) standard compliance Configurable scan clock frequency optimized for high-speed operation TTL/CMOS compatible signal levels Standard JTAG signals: TDI, TDO, TCK, TMS, TRST Multiple independent JTAG chain support Integrated high-speed data buffer for PCI-to-JTAG transfer PCI Bus Interface PCI (Peripheral Component Interconnect) bus type Bus mastering capability for efficient data transfer PCI interrupt support for host communication Performance Characteristics High-speed in-system programming (ISP) for FPGAs, CPLDs, microcontrollers, and serial Flash memory Fast scan clock execution rates Multi-device support within single scan chains Wide JTAG-compliant device compatibility across manufacturers Key Features Optimized throughput for test vector execution Efficient data handling via PCI bus mastering Support for complex board architectures with multiple device chains Configurable operating parameters dependent on system configuration Typical Applications Manufacturing test and diagnostics Research and development workflows Field service operations Boundary-scan testing of assembled PCBs Device programming and firmware updates Compatibility & Integration The module integrates into host systems with available PCI slots running compatible operating systems including Windows and Linux. Dedicated driver software is supplied for the target operating system, enabling direct host communication and control of JTAG operations.

PCI11491TURBO Characteristics / Specifications

PARAMETER	VALUE
Model	PCI-1149.1
Turbo Model	PCI-1149.1/Turbo
Instrument Type	JTAG boundary scan controller
Bus Interface	PCI
Standard	IEEE 1149.1
Manufacturer	Corelis
Form Factor	PCI plug-in card
Tck	programmable clock
Tap Support	multi TAP class
Software	Corelis ScanPlus class
Operating Temperature Class	commercial lab
Power	PCI slot power
Connector	JTAG pod interface
Humidity Class	non condensing
Duty	continuous ATE
Grounding	host PC chassis
Isolation Class	pod dependent
LEDs	status indicators class
Driver	Windows device driver class
Scan Modes	interconnect and cluster test
Isp Support	in system programming class
Storage Temperature Class	commercial
Cooling	host airflow
Storage temperature	Class: commercial
Operating temperature	Class: commercial lab

PARAMETER	VALUE
Humidity	Class: non condensing

PCI11491TURBO Specifications

PARAMETER	VALUE
Applications	Boundary-scan interconnect test, in-system programming, and cluster testing of IEEE 1149.1 compliant boards.
Key Features	IEEE 1149.1 JTAG controller
Technical Specifications	Model: PCI-1149.1

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Storage temperature	Class: commercial
Operating temperature	Class: commercial lab
Operating Temperature	Class: commercial lab
Humidity	Class: non condensing
Operating Temperature Class	commercial lab
Power	PCI slot power
Connector	JTAG pod interface
Humidity Class	non condensing
Duty	continuous ATE
Grounding	host PC chassis
Isolation Class	pod dependent
LEDs	status indicators class
Driver	Windows device driver class
Scan Modes	interconnect and cluster test
Isp Support	in system programming class
Storage Temperature Class	commercial
Cooling	host airflow Notes From Corelis PCI-1149.1 / PCI-1149.1 Turbo product listings. Exact maximum TCK and TAP count follow the Corelis datasheet for the installed card revision.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

performance

Technical Specifications

Technical Specifications

PARAMETER	VALUE
Model	PCI-1149.1
Turbo Model	PCI-1149.1/Turbo
Instrument Type	JTAG boundary scan controller
Bus Interface	PCI
Standard	IEEE 1149.1
Manufacturer	Corelis
Form Factor	PCI plug-in card
Tck	programmable clock
Tap Support	multi TAP class
Software	Corelis ScanPlus class
Operating Temperature Class	commercial lab
Power	PCI slot power
Connector	JTAG pod interface
Humidity Class	non condensing
Duty	continuous ATE
Grounding	host PC chassis
Isolation Class	pod dependent
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Driver	Windows device driver class
Scan Modes	interconnect and cluster test
Isp Support	in system programming class
Storage Temperature Class	commercial
Cooling	host airflow

Notes

From Corelis PCI-1149.1 / PCI-1149.1 Turbo product listings. Exact maximum TCK and TAP count follow the Corelis datasheet for the installed card revision.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.