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Data Sheet

August 15, 2026

CURTISS-WRIGHT / SYSTRAN

Curtiss-Wright / Systran SCRAMNet AHSB-DPCIG02M-T PCI Module

The Curtiss-Wright/Systran SCRAMNet AHSB-DPCIG02M-T is a PCI module designed for real-time data sharing in demanding applications. This module enables deterministic communication between multiple nodes, making it ideal for simulation, control, and data acquisition systems. Its solid design ensures reliable operation in harsh environments.



MODEL

**Curtiss-Wright /
Systran AHSB-
DPCIG02M-T**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

**AHSB-DPCIG02M-
T**

LISTING

[https://aumictechlabs.c
om/products/ahsb-dpci
g02m-t-2/](https://aumictechlabs.com/products/ahsb-dpcig02m-t-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Curtiss-Wright/Systran SCRAMNet AHSB-DPCIG02M-T is a PCI-based shared-memory module engineered for deterministic, ultra-low-latency data distribution across networked nodes. Built on replicated shared-memory architecture, this single-slot card eliminates the overhead and unpredictability of conventional networking protocols, delivering sub-microsecond system latency and 250 nSec/node propagation delay. The design allows host processors to focus entirely on application-specific computation rather than protocol management. Technical Specifications Bus Interface: PCI Form Factor: PCI Module (SC150e PCI) Shared Memory: 2 MB SRAM Data Throughput: Up to 16.7 MB/sec System Latency: Sub-microsecond; 250 nSec/node Network

Topology: Ring, expandable to 256 nodes maximum Fiber-Optic Media Options: - 820 nm interface: 300 m maximum node separation - 1300 nm interface: 3500 m maximum node separation PCI Bridge Chip: V363EPC Dimensions: 4.200" × 7.500" Key Features ASIC-based core logic handles message arbitration, network control, memory management, interrupt handling, error detection/correction, and node configuration Generates two external trigger outputs via the Address Control Register: host-access trigger and network-write trigger Host processor and operating system independence at each node "On-time, every-time" deterministic performance for precision-critical operations Typical Applications High-fidelity simulation environments Real-time data acquisition systems Distributed control architectures requiring synchronized node operation Compatibility & Integration Backward compatible with SCRAMNet Classic and SCRAMNet+ products, except for GOLD RING communication protocol. Integrates into standard PCI backplanes across computing platforms.

AHSBDPCIG02MT Characteristics / Specifications

PARAMETER	VALUE
Model	AHSB-DPCIG02M-T
Instrument Type	PCI reflective memory network module
Family	Systran SCRAMNet+ AHSB-DPCIG
Host Bus	PCI
Reflective Memory	2 MB
Related 2M	AHSB-DPCIG02M-T
Related 4M	AHSB-DPCIG04M-T
Related 8M	AHSB-DPCIG08M-T
Related Pmc	A-HS-DPMC02M-20 SCRAMNet PMC
Related Media	CTRSTDF SCRAMNet media card
Network	SCRAMNet+ fiber reflective memory ring
Form Factor	PCI card
Manufacturer	Systran / Curtiss-Wright
Use Case	real-time shared memory networking
Control	SCRAMNet API / memory-mapped window
Operating Temperature Class	commercial PCI
Literature	Curtiss-Wright Systran SCRAMNet AHSB-DPCIG02M-T listings
Protocol	SCRAMNet+ replicated memory
Media	fiber via media card
Documentation Type	OEM SCRAMNet PCI catalog
Brand Lineage	Systran Curtiss-Wright
System Role	PCI node on a SCRAMNet ring
Mapping	host PCI memory window onto the ring
Operating temperature	Class: commercial PCI

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	Class: commercial PCI
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Operating Temperature Class	commercial PCI
Literature	Curtiss-Wright Systran SCRAMNet AHSB-DPCIG02M-T listings
Protocol	SCRAMNet+ replicated memory
Media	fiber via media card
Documentation Type	OEM SCRAMNet PCI catalog
Brand Lineage	Systran Curtiss-Wright
System Role	PCI node on a SCRAMNet ring
Mapping	host PCI memory window onto the ring Notes From Curtiss-Wright / Systran SCRAMNet AHSB-DPCIG02M-T PCI module catalog identity (2 MB). Ring rate, fiber type, and interrupt mapping are on the SCRAMNet+ PCI data sheet.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Use Case	real-time shared memory networking
Control	SCRAMNet API / memory-mapped window
Operating Temperature Class	commercial PCI
Literature	Curtiss-Wright Systran SCRAMNet AHSB-DPCIG02M-T listings
Protocol	SCRAMNet+ replicated memory
Media	fiber via media card
Documentation Type	OEM SCRAMNet PCI catalog
Brand Lineage	Systran Curtiss-Wright
System Role	PCI node on a SCRAMNet ring
Mapping	host PCI memory window onto the ring
Notes	
From Curtiss-Wright / Systran SCRAMNet AHSB-DPCIG02M-T PCI module catalog identity (2 MB). Ring rate, fiber type, and interrupt mapping are on the SCRAMNet+ PCI data sheet.	

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.