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Data Sheet

August 15, 2026

CURTISS-WRIGHT / VMETRO

Curtiss-Wright / VMETRO PBT-315B PCI Bus Analyzer

The Curtiss-Wright/VMETRO PBT-315B is a PCI bus analyzer designed for capturing, displaying, and analyzing PCI bus transactions. It aids in debugging and verifying PCI-based systems by providing real-time monitoring and detailed protocol analysis. This tool is essential for engineers working on PCI hardware and software development.

**MODEL****Curtiss-Wright /
VMETRO PBT-315B****CALIBRATION****Available on
request****CONDITION****Used****STATUS****In stock****RFQ / SKU****PBT-315B****LISTING**<https://aumictechlabs.com/products/pbt-315b-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Curtiss-Wright/VMETRO PBT-315B is a single-slot PCI short card analyzer that captures, displays, and analyzes PCI bus transactions in real time. It monitors 91 PCI signals plus 8 external inputs, supporting both 32-bit and 64-bit PCI architectures at clock speeds up to 33 MHz. Engineers use it for debugging, verification, and protocol analysis of PCI-based systems, with protocol-sensitive sampling that triggers on arbitration, interrupts, and cache cycles without extensive manual probing. Technical Specifications Form Factor: Single-slot PCI short card PCI Bus Compatibility: 32-bit and 64-bit architectures PCI Clock Speed: Up to 33 MHz (min. 20 MHz) Trace Buffer (Model B): 32K × 128 bits Input Channels: 91 PCI signals + 8 external inputs on pin header Temporal Resolution: 30 ns @ 33 MHz Latency Tag: Counts wait states from FRAME# to

TRDY# assertion (max. 128 clocks) Counters: Four 20-bit occurrence counters Three 20-bit delay counters Four 20-bit event counters for statistics Key Features Dual Sampling Modes: TRANSFER-synchronous (one sample per valid Data Phase for software analysis) or CLOCK-synchronous (one sample per PCI CLK cycle for hardware analysis) Address/Data Demultiplexing: Automatic demultiplexing of Address/Data and COMMAND/BEx# with intuitive mnemonic display Flexible Triggering: Four full-width word recognizers with RANGE, NOT, and BINARY operators; supports hex, binary, or command mnemonics (MemRd, IOWri, etc.) 16-Level Sequencer: Implements IF, ELSE, ELSIF, GOTO, COUNT, DELAY, TRIGGER, STORE, SAMPLING MODE, and HALT functions Trigger Output: TTL-level output with programmable polarity, level, or pulse mode External Power Provision: Supports optional external power supply for systems with marginal power Control Interface: RS232 operation via two RS232C serial ports Typical Applications Development and debugging of PCI hardware and software; protocol verification; system integration troubleshooting; latency and arbitration analysis. Compatibility & Integration Operates with PCI systems running 20–33 MHz clock frequencies. External triggering and monitoring available through TTL output and 8 auxiliary input channels.

PBT315B Characteristics / Specifications

PARAMETER	VALUE
Model	PBT-315B
Instrument Type	PCI bus analyzer and exerciser
Bus Target	PCI
Form Factor	PCI analyzer card
Analysis Modes	state analyzer and exerciser
Triggering	multi-level bus event triggers
Capture Memory Class	deep state trace class family
Statistics	bus utilization and error counters class
Interface	host PCI slot installation
Series Family	VMETRO PBT
Related Models	PBT-415 PBTC-415BX VG-VME
Protocol Support	PCI protocol decode
Operating Temperature Class	0 C to 50 C lab class
Power Source	host PCI slot power
Use Case	PCI debug and validation
Trace Display	address data control decode
Exerciser Role	master/slave stimulus class
Timestamping	relative time tags on captured cycles
Error Detection	parity and protocol error flags class
Upgrade Path	Vanguard family analyzers
Manufacturer	Curtiss-Wright VMETRO
Brand Lineage	VMETRO Curtiss-Wright
Operating temperature	Class: 0 C to 50 C lab class

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	Class: 0 C to 50 C lab class
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Upgrade Path	Vanguard family analyzers
Manufacturer	Curtiss-Wright VMETRO
Brand Lineage	VMETRO Curtiss-Wright Notes Capability rows from Curtiss-Wright/VMETRO PBT-315B PCI bus analyzer product descriptions in the PBT/Vanguard analyzer family. Exact trace depth and PCI clock ratings are on the PBT-315B datasheet configuration.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.