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Data Sheet

August 15, 2026

CURTISS-WRIGHT / VMETRO

Curtiss-Wright / VMETRO Ultraview TIMBAT-PB Piggyback Module

The Curtiss-Wright / VMETRO Ultraview TIMBAT-PB is a piggyback module that extends VMEbus analyzer capabilities by integrating VME Bus Anomaly Trigger (VBAT) functionality with a dedicated timing analyzer. Designed for aerospace and defense applications, this module screens VMEbus traffic for specification violations using rule-based parallel trigger elements. It detects timing anomalies such as address signal instability during assertion and simultaneous multi-master access grants, routing violations directly to the trace memory and trigger circuitry of associated VBT-325 series analyzers. Rule-based parallel triggering continuously monitors VMEbus traffic for timing violations External I/O connectivity via BNC connectors supports integration with auxiliary test equipment Aumictech offers NIST-traceable calibration and repair for the Curtiss-Wright / VMETRO TIMBAT-PB. All calibration



MODEL

**Curtiss-Wright /
VMETRO TIMBAT-
PB**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

TIMBAT-PB

LISTING

[https://aumictechlabs.c
om/products/timbat-pb
-2/](https://aumictechlabs.com/products/timbat-pb-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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dedicated timing analyzer. Designed for aerospace and defense applications, this module screens VMEbus traffic for specification violations using rule-based parallel trigger elements. It detects timing anomalies such as address signal instability during assertion and simultaneous multi-master access grants, routing violations directly to the trace memory and trigger circuitry of associated VBT-325 series analyzers. Technical Specifications VMEbus signals sampled: 101 External inputs: 4 Trigger output: 1 Trigger sequencer levels: 16 Built-in voltage monitor Key Features Rule-based parallel triggering continuously monitors VMEbus traffic for timing violations VBAT integration screens address stability and bus master contention scenarios External I/O connectivity via BNC connectors supports integration with auxiliary test equipment Trigger outputs synchronize trace capture with detected anomalies 16-level sequencer enables multi-stage triggering logic Typical Applications VMEbus system validation and debugging in aerospace platforms Defense electronics diagnostics requiring real-time anomaly detection Timing violation analysis across mixed VMEbus architectures Protocol compliance verification during system integration Compatibility & Integration The TIMBAT-PB operates as a piggyback module attached to compatible Curtiss-Wright carrier boards, including the VPC-MkII. Integration with VMEbus analyzers - specifically the VBT-325 series (VBT-325B and VBT-325C) - provides trigger and memory management. The module pairs with other piggyback components such as S/TIM200-PB and VDRIVE-PB within broader VMEbus test systems.

TIMBATPB Characteristics / Specifications

PARAMETER	VALUE
Model	TIMBAT-PB
Instrument Type	Ultraview piggyback module
Manufacturer	Curtiss-Wright / VMETRO
Literature	remaining-queue listing Curtiss-Wright / VMETRO Ultraview TIMBAT-PB Piggyback Module
Family	VMETRO Ultraview TIMBAT piggyback family
Form Factor	as installed per drawing
System Role	Ultraview bus-analyzer piggyback I/O in VMETRO systems that specify TIMBAT-PB
Brand Lineage	Curtiss-Wright / VMETRO
Queue Title	Curtiss-Wright / VMETRO Ultraview TIMBAT-PB Piggyback Module
Documentation Type	OEM remaining-queue listing
Use Case	Ultraview bus-analyzer piggyback I/O in VMETRO systems that specify TIMBAT-PB
Vendor Path	Curtiss-Wright / VMETRO
Year Class	TIMBAT-PB generation
Application Class	Ultraview piggyback module
Module Class	Ultraview piggyback module
Host Class	host on the drawing
Connector Class	connectors on the drawing
Ate Class	industrial / lab ATE class
Control Class	front panel / firmware on the drawing
Environment Class	ambient on the nameplate
Voltage Class	volts on the nameplate
Current Class	amperes on the nameplate
Config	TIMBAT-PB remaining-queue suffix
Function Class	function on the drawing

PARAMETER

VALUE

Piggyback Role

TIMBAT-PB Ultraview module

Host Family

VMETRO Ultraview analyzers

TIMBATPB Specifications

PARAMETER	VALUE
Applications	Ultraview bus-analyzer piggyback I/O in VMETRO systems that specify TIMBAT-PB in systems that specify TIMBAT-PB.
Ultraview piggyback module	VMETRO Ultraview TIMBAT piggyback family
Notes	From remaining-queue title (Curtiss-Wright / VMETRO Ultraview TIMBAT-PB Piggyback Module). Pinout is on the TIMBAT-PB drawing.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Piggyback Role	TIMBAT-PB Ultraview module
Host Family	VMETRO Ultraview analyzers

Notes
From remaining-queue title (Curtiss-Wright / VMETRO Ultraview TIMBAT-PB Piggyback Module). Pinout is on the TIMBAT-PB drawing.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.