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Data Sheet

August 15, 2026

CURTISS-WRIGHT / VMETRO

Curtiss-Wright / VMETRO VBT-325C VMEbus Analyzer

The Curtiss-Wright/VMETRO VBT-325C is a VMEbus analyzer designed for capturing, monitoring, and analyzing data traffic on a VMEbus system. It assists in debugging, testing, and optimizing VMEbus-based systems by providing detailed insights into bus transactions and performance.

**MODEL****Curtiss-Wright /
VMETRO VBT-
325C****CALIBRATION****Available on
request****CONDITION****Used****STATUS****In stock****RFQ / SKU****VBT-325C****LISTING**[https://aumictechlabs.c
om/products/vbt-325c-
2/](https://aumictechlabs.com/products/vbt-325c-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Curtiss-Wright/VMETRO VBT-325C is a dual-analyzer VMEbus instrument designed for protocol-sensitive capture, monitoring, and analysis of VMEbus systems and configurable P2 bus protocols. It has simultaneous independent analysis of VMEbus and secondary bus activity - including VSB, SCSI, or user-defined protocols - diagnose complex multi-bus interactions within a single acquisition window. The instrument captures all VMEbus transactions including arbitration, interrupts, block transfers, and read-modify-write cycles with protocol-aware sampling that derives timing from bus cycles themselves. Technical Specifications Dual Analyzer Architecture VMEbus analyzer: 101 sampled signals (address, data, control, interrupt, and bus arbitration lines)

P2 analyzer: 64 configurable signals for VSB, SCSI, or user-defined protocols Protocol-sensitive sampling: derives clocks from bus cycles for accurate transaction tracing Timing and Analysis Depth State analysis: up to 25 MHz Timing analysis: up to 50 MHz (200 MHz optional with TIM200-PB piggyback) Trace buffer: 32K samples standard (expandable to 1M samples with XMEM325-PB piggyback) VME partition: 32K × 128 bits P2 partition: 32K × 64 bits Triggering and Sequencing Four full-width word recognizers per bus with address and data range capability Trigger sensitivity: 20 ns (5 ns optional) 16-level sequencer with conditional logic (If, Else, Elsif, Goto, Count, Delay, Trigger, Store, Sampling mode, Halt) Four 20-bit occurrence counters per bus for sequencer control Three 20-bit delay counters per bus: 40 ns to 0.33 s range Four 20-bit event counters per bus for statistical analysis NOT operator on signal groups Interface and Control PC-based operation via BusView™ GUI over RS232 connection ASCII terminal alternative for remote operation Key Features Independent, simultaneous analysis of two distinct bus protocols eliminates sequential capture limitations Protocol-aware sampling eliminates timing artifacts from external clock derivation Flexible triggering with multi-level sequencing enables complex conditional captures Expandable memory architecture scales trace depth for long-duration captures Typical Applications VMEbus system development and validation Hardware-software integration debugging Multi-bus protocol interaction analysis Bus arbitration and interrupt sequence diagnostics Compatibility & Integration Supports standard VMEbus signals and VSB, SCSI, or customer-defined P2 protocols. Optional piggyback modules extend timing analysis and trace memory capacity.

VBT325C Characteristics / Specifications

PARAMETER	VALUE
Model	VBT-325C
Instrument Type	VMEbus analyzer and exerciser
Bus Standard	VMEbus VME64 class
Manufacturer Line	VMETRO Vanguard bus analyzers
Primary Functions	capture decode and exercise
Form Factor Class	VME analyzer module or system probe class
Trace Memory Class	deep state trace class family
Protocol Support	VME address data and control cycles
Triggering Class	multilevel bus event triggers
Display Class	state listing and timing views class
Exerciser Role	master slave stimulus class
Operating Temperature Class	0 C to 40 C laboratory class
Storage Temperature Class	minus 40 C to 70 C class
Power Source Class	VME backplane or external class
Interface To Host Class	local UI or host link class
Series Family	VMETRO VBT-325
Related Models	VBT-325C successor variant
Related Pci Analyzers	PBT-415 PBT-315B class
Related Vanguard	VG-VME VG-SAM
Capture Modes Class	state and statistics class
Event Recognition Class	address data interrupt acknowledge
Timestamp Class	relative time tagging class
Use Environment	lab and depot VME maintenance
Storage temperature	Class: minus 40 C to 70 C class
Operating temperature	Class: 0 C to 40 C laboratory class

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
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Related Models	VBT-325C successor variant
Related Pci Analyzers	PBT-415 PBT-315B class
Related Vanguard	VG-VME VG-SAM
Capture Modes Class	state and statistics class
Event Recognition Class	address data interrupt acknowledge
Timestamp Class	relative time tagging class
Use Environment	lab and depot VME maintenance Notes Capability rows from VMETRO/Curtiss-Wright VBT-325C bus analyzer catalog descriptions. Exact trace depth and electrical loading follow the installed analyzer configuration.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.