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**Data Sheet**

August 15, 2026

DDC

# DDC BUS-65515II MIL-STD-1553 BC/RTU/MT IBM PC Interface Module

The DDC BUS-65515II is a MIL-STD-1553 BC/RTU/MT interface module designed for IBM PC systems. It facilitates communication and testing with MIL-STD-1553 avionics data buses, supporting Bus Controller (BC), Remote Terminal Unit (RTU), and Monitor Terminal (MT) modes. This module provides a versatile solution for integrating and simulating MIL-STD-1553 communication in various applications.



MODEL

**DDC BUS-65515II**

CALIBRATION

**Available on request**

CONDITION

**Used**

STATUS

**In stock**

RFQ / SKU

**BUS-65515II**

LISTING

<https://aumictechlabs.com/products/bus-65515ii-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

## This unit

The DDC BUS-65515II is a MIL-STD-1553B interface module for IBM PC systems that functions as a Bus Controller (BC), Remote Terminal Unit (RTU), or Monitor Terminal (MT). This full-size printed circuit card provides dual-redundant MIL-STD-1553B connectivity, enabling avionics system communication, simulation, and testing directly from the host PC. Technical Specifications Data Bus Standard: MIL-STD-1553B (Dual Redundant) Host Interface: IBM PC Bus (ISA or compatible) Form Factor: Full-size IBM PC printed circuit card Memory: 4K x 16 dual-access RAM with double-buffered architecture Time Tagging: Onboard Time Tag Counter for event timestamping Message Support: All dual redundant MIL-STD-1553B message formats and mode codes Configuration:

Jumper programmable interrupt level and RTU address selection

**Key Features**

- Multi-Mode Operation:** BC, RTU, and MT modes support diverse testing and simulation workflows without requiring separate hardware.
- Data Integrity:** Double-buffered memory prevents partially updated data from being read by the CPU or transmitted to the bus.
- Built-In-Test:** Dynamic wrap-around BIT capability enables rapid self-diagnostics of module functionality.
- Integrated Architecture:** Single-card design minimizes integration complexity in PC-based test systems.
- Full MIL-STD-1553B Compliance:** Supports all standard message formats, mode codes, and dual redundant bus operation.

**Typical Applications**

- Avionics system development and validation
- MIL-STD-1553B bus simulation and emulation
- Real-time data acquisition and monitoring
- Integration testing for airborne and ground-based systems

**Compatibility & Integration**

The module integrates into IBM PC systems and requires appropriate software drivers and APIs for BC, RTU, and MT operation. Implementation with DOS-based operating systems and IBM PC-compatible architectures is supported.

# BUS65515II Characteristics / Specifications

| PARAMETER           | VALUE                                                         |
|---------------------|---------------------------------------------------------------|
| Model               | BUS-65515II                                                   |
| Also Known As       | BUS-65515                                                     |
| Instrument Type     | IBM PC MIL-STD-1553 BC/RTU/MT interface                       |
| Bus                 | IBM PC / ISA full-size card                                   |
| Protocol            | MIL-STD-1553B dual redundant                                  |
| Modes               | Bus Controller, Remote Terminal Unit, or Bus Monitor          |
| Shared Ram          | 4K x 16 dual-access RAM                                       |
| Memory Architecture | double buffer to prevent partial updates                      |
| Time Tag            | onboard time-tag counter                                      |
| Built In Test       | dynamic wrap-around BIT                                       |
| Interrupt           | jumper programmable interrupt level                           |
| Rtu Address         | jumper programmable RTU address                               |
| Message Formats     | all 1553B dual-redundant message formats and mode codes       |
| Mechanical Package  | one full-size IBM PC printed circuit card                     |
| Manufacturer        | Data Device Corporation                                       |
| Family              | BUS-65515 IBM PC 1553 interface                               |
| Application Class   | dynamic 1553 simulation and test                              |
| Host                | IBM PC bus computers                                          |
| Data Integrity      | double buffer prevents CPU or 1553 from reading partial words |
| Compliance          | full MIL-STD-1553B dual-redundant operation                   |
| Product Category    | avionics databus interface card                               |
| Revision            | II suffix denotes the BUS-65515II production revision         |
| Related Pci         | BU-65549 is the later PCI Mini-ACE Plus sibling family        |

# BUS65515II Specifications

| PARAMETER                | VALUE                                                                |
|--------------------------|----------------------------------------------------------------------|
| Applications             | ISA/IBM-PC hosted 1553 simulation, test, and dynamic bus monitoring. |
| Key Features             | Full-size IBM PC 1553 BC/RTU/MT card                                 |
| Technical Specifications | Model: BUS-65515II                                                   |

# Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

| Specifications                                                                                                                                                                |                                                               |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------|
| Specifications                                                                                                                                                                |                                                               |
| PARAMETER                                                                                                                                                                     | VALUE                                                         |
| Model                                                                                                                                                                         | BUS-65515II                                                   |
| Also Known As                                                                                                                                                                 | BUS-65515                                                     |
| Instrument Type                                                                                                                                                               | IBM PC MIL-STD-1553 BC/RTU/MT interface                       |
| Bus                                                                                                                                                                           | IBM PC / ISA full-size card                                   |
| Protocol                                                                                                                                                                      | MIL-STD-1553B dual redundant                                  |
| Modes                                                                                                                                                                         | Bus Controller, Remote Terminal Unit, or Bus Monitor          |
| Shared Ram                                                                                                                                                                    | 4K x 16 dual-access RAM                                       |
| Memory Architecture                                                                                                                                                           | double buffer to prevent partial updates                      |
| Time Tag                                                                                                                                                                      | onboard time-tag counter                                      |
| Built In Test                                                                                                                                                                 | dynamic wrap-around BIT                                       |
| Interrupt                                                                                                                                                                     | jumper programmable interrupt level                           |
| Rtu Address                                                                                                                                                                   | jumper programmable RTU address                               |
| Message Formats                                                                                                                                                               | all 1553B dual-redundant message formats and mode codes       |
| Mechanical Package                                                                                                                                                            | one full-size IBM PC printed circuit card                     |
| Manufacturer                                                                                                                                                                  | Data Device Corporation                                       |
| Family                                                                                                                                                                        | BUS-65515 IBM PC 1553 interface                               |
| Application Class                                                                                                                                                             | dynamic 1553 simulation and test                              |
| Host                                                                                                                                                                          | IBM PC bus computers                                          |
| Data Integrity                                                                                                                                                                | double buffer prevents CPU or 1553 from reading partial words |
| Compliance                                                                                                                                                                    | full MIL-STD-1553B dual-redundant operation                   |
| Product Category                                                                                                                                                              | avionics databus interface card                               |
| Revision                                                                                                                                                                      | II suffix denotes the BUS-65515II production revision         |
| Related Pci                                                                                                                                                                   | BU-65549 is the later PCI Mini-ACE Plus sibling family        |
| Notes                                                                                                                                                                         |                                                               |
| From DDC BUS-65515 / BUS-65515II MIL-STD-1553 BC/RTU/MT IBM PC Interface Module product literature. Confirm interrupt jumper and RTU address strapping on the installed card. |                                                               |

### Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: [sales@aumictech.com](mailto:sales@aumictech.com) · +1 (484) 841-9341

#### Aumictech

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NIST-traceable calibration · SAM registered ·  
30-day returns  
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Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.