

**Aumictech**

Test & measurement • bought, sold, repaired, calibrated

Data Sheet

August 15, 2026

DYNAMIC ENGINEERING

Dynamic Engineering PCI3IP 3-Slot PCI IndustryPack (IP) Carrier

Dynamic Engineering PCI3IP 3-Slot PCI IndustryPack Carrier The PCI3IP isaPCI carrier for three IndustryPack (IP) mezzanine modules. OEM Dynamic Engineering PCI3IP literature describes a PCI host with three IP sites, 8/32 MHz IP clock support, plus 5 V / plus 12 V / minus 12 V IP power from the PCI slot, I/O via front-panel or rear, and register-based access to IP ID PROM and I/O space. Used to host analog, digital, serial, and motion IndustryPacks in a desktop or industrial PC. PCI hosts needing three IndustryPack sites for mixed analog/digital I/O without a VME crate. The PCI3IP isaPCI carrier for three IndustryPack (IP) mezzanine modules. OEM Dynamic Engineering PCI3IP literature describes a PCI host with three IP sites, 8/32 MHz IP clock support, plus 5 V / plus 12 V / minus 12 V IP power from the PCI slot, I/O via front-panel or rear, and register-based access to IP ID PROM and I/O space. Used to host analog, digital, serial, and motion IndustryPacks in a desktop or industrial PC. PCI hosts needing three IndustryPack sites for mixed analog/digital I/O without a VME crate.



MODEL

Dynamic Engineering PCI3IP

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

PCI3IP

LISTING

<https://aumictechlabs.com/products/pci3ip-3/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Dynamic Engineering PCI3IP is a 3-slot PCI IndustryPack (IP) carrier card that integrates IndustryPack modules into PCI-based systems. It delivers data acquisition, control, and

communication functionality through a half-length PCI interface operating at 32-bit, 33 MHz. Each of the three IP positions supports independent clock selection (8 MHz or 32 MHz) and accepts byte, word, and 32-bit long word accesses. The card supplies fused, filtered power to IP modules: 5V at 2A per fuse, $\pm 12V$ at 1.1A per fuse, with individual power planes isolating each slot. Programmable VPWR accommodates 3.3V or 5V IP interfaces with status indication. Dual interrupt lines per IP module route through an on-board FPGA to the PCI bus, with per-slot masking and status registers. A watch-dog timer protects against PCI bus hangs. On-board features include eight user-configurable LEDs, an 8-bit DIP switch readable via register, local reset switch, and ribbon cable bezel cutout compatible with IP-Debug-Bus. The card uses industry standard 50-pin box header connectors in right-angle through-bezel or vertical mount configurations. Extended industrial temperature components are standard. VITA 4 - 1995 compliant. Technical Specifications PCI interface: 32-bit, 33 MHz operation; half-length form factor; PCI-SIG specification compliant IP positions: 3 slots with independent 8 MHz or 32 MHz clock selection, glitch-free on-the-fly switching Data access: Byte, word, and 32-bit long word accesses; static or auto-incremented addressing; 32-bit to 16-bit access conversion for 16-bit IP modules Power supply: +5V (2A fuse), +12V (1.1A fuse), -12V (1.1A fuse), plus 3.3V for on-board FPGA IP interface voltage: Programmable 3.3V or 5V (VPWR) with status LEDs Interrupts: Two per IP module, routed through FPGA to PCI bus; per-slot mask bits and status registers Memory space: Full allocation per IP position; mini-map version available with 2 kB total PCI memory Key Features Watch-dog timer for PCI bus hang protection Eight user LEDs plus power and IP access indicators Individual power planes per slot for isolation 8-bit user DIP switch with register readout Local reset switch for IP boards IP-Debug-Bus compatible ribbon cable support Typical Applications Data acquisition, motion control, instrumentation, industrial I/O, real-time signal processing, and communication systems requiring modular expansion within PCI-based platforms. Compatibility & Integration Fully compliant with VITA 4 - 1995 specification. Compatible with all Dynamic Engineering IP modules and other VITA-compliant IndustryPack modules.

PCI3IP Characteristics / Specifications

PARAMETER	VALUE
Model	PCI3IP
Instrument Type	PCI IndustryPack carrier
Family	Dynamic Engineering IP carriers
Slot Count	3 IndustryPack sites
Host Bus	PCI
Ip Clock Class	8 MHz and 32 MHz IP clock (data sheet)
Ip Power Rails	plus 5 V, plus 12 V, minus 12 V from PCI
Addressing	IP ID PROM, I/O, and memory spaces per IP spec
Manufacturer	Dynamic Engineering
Literature	Dynamic Engineering PCI3IP 3-slot PCI IndustryPack carrier literature
System Role	PCI host for three IP modules
Related Cpci	cPCI2PMC CompactPCI-to-PMC sibling carrier
Form Factor	PCI card with three IP sites
Io Routing	front and/or rear I/O per PCI3IP sheet
Compliance Class	ANSI/VITA 4 IndustryPack
Interrupt Class	IP INT0/INT1 to PCI interrupt (data sheet)
Dma Class	PCI master/target per PCI3IP sheet
Operating Temperature Class	commercial/industrial on the sheet
Module Class	IP carrier
Brand Lineage	Dynamic Engineering
Documentation Type	PCI3IP listing
Ate Class	mezzanine I/O host
Host Class	desktop/industrial PCI
Year Class	PCI3IP
Connector Class	IP 50-pin plus PCI edge

PARAMETER	VALUE
Use Case	mixed analog/digital IP I/O
Operating temperature	Class: commercial/industrial on the sheet

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	Class: commercial/industrial on the sheet
Operating Temperature	Class: commercial/industrial on the sheet
Operating Temperature Class	commercial/industrial on the sheet
Module Class	IP carrier
Brand Lineage	Dynamic Engineering
Documentation Type	PCI3IP listing
Ate Class	mezzanine I/O host
Host Class	desktop/industrial PCI
Year Class	PCI3IP
Connector Class	IP 50-pin plus PCI edge
Use Case	mixed analog/digital IP I/O Notes From Dynamic Engineering PCI3IP 3-slot PCI IndustryPack carrier listings. Exact PCI watts, IP current budget, and 8 versus 32 MHz default are on the PCI3IP data sheet; do not invent amps.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	PCI3IP
Instrument Type	PCI IndustryPack carrier
Family	Dynamic Engineering IP carriers
Slot Count	3 IndustryPack sites
Host Bus	PCI
Ip Clock Class	8 MHz and 32 MHz IP clock (data sheet)
Ip Power Rails	plus 5 V, plus 12 V, minus 12 V from PCI
Addressing	IP ID PROM, I/O, and memory spaces per IP spec
Manufacturer	Dynamic Engineering
Literature	Dynamic Engineering PCI3IP 3-slot PCI IndustryPack carrier literature
System Role	PCI host for three IP modules
Related Cpci	cPCI2PMC CompactPCI-to-PMC sibling carrier
Form Factor	PCI card with three IP sites
Io Routing	front and/or rear I/O per PCI3IP sheet
Compliance Class	ANSI/VITA 4 IndustryPack
Interrupt Class	IP INT0/INT1 to PCI interrupt (data sheet)
Dma Class	PCI master/target per PCI3IP sheet
Operating Temperature Class	commercial/industrial on the sheet
Module Class	IP carrier
Brand Lineage	Dynamic Engineering
Documentation Type	PCI3IP listing
Ate Class	mezzanine I/O host
Host Class	desktop/industrial PCI
Year Class	PCI3IP
Connector Class	IP 50-pin plus PCI edge

PARAMETER	VALUE
Use Case	mixed analog/digital IP I/O

Notes

From Dynamic Engineering PCI3IP 3-slot PCI IndustryPack carrier listings. Exact PCI watts, IP current budget, and 8 versus 32 MHz default are on the PCI3IP data sheet; do not invent amps.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.