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Data Sheet

August 15, 2026

DYNAMIC SIGNALS / GAGE

DynamicSignals / GaGe CompuGen 3250 High-Speed Digital Output ISA Module

The DynamicSignals/GaGe CompuGen 3250 is a high-speed digital output ISA module designed for generating complex waveforms and patterns. It offers high resolution and speed, making it suitable for various applications such as arbitrary waveform generation, digital pattern simulation, and control systems. The module provides a flexible and precise solution for demanding digital output requirements.



MODEL

**DynamicSignals /
GaGe CompuGen
3250**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

CompuGen 3250

LISTING

<https://aumictechlabs.com/products/compu-gen-3250-3/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The DynamicSignals/GaGe CompuGen 3250 is a high-speed digital output ISA module that generates complex waveforms and digital patterns with precise timing and flexible control. It delivers 32 bits of parallel digital output clocked up to 50 MHz, supporting arbitrary waveform generation, digital pattern simulation, and hardware-retriggerable burst operations. On-board pattern memory accommodates up to 8 Meg × 32 bits, while selectable internal clocking (50 MHz, 25 MHz, 10 MHz, 5 MHz) and optional external clock input (DC to 50 MHz) enable deterministic signal generation. Multiple output modes - endless looping, multi-shot, and one-shot - combined

with internal and external triggering, adapt the module to continuous test scenarios, repetitive pattern sequences, and pulse-echo measurements. TTL/CMOS output levels drive high-impedance loads to 5 V and 50 Ω loads to 3.5 V using four parallel 74FCT162245 drivers for extended current capability. Technical Specifications Digital Output: 32 parallel bits with 1 clock output and 1 SYNC signal Output Voltage: 0–5 V into 1 M Ω ; 0–3.5 V into 50 Ω load Output Logic Levels: TTL/CMOS Clock Frequency: Internal selectable at 50 MHz, 25 MHz, 10 MHz, or 5 MHz; external input DC to 50 MHz maximum Maximum Sample Rate: 50 MHz (20 ns per sample) Memory Depth: 256K to 8M samples (32-bit words) Timebase Stability: Crystal-controlled on-board oscillator External Clock Requirements: Minimum 10 ns positive or negative pulse width Power Consumption: 20 W @ +5 V Key Features High-current output drivers for direct load interfacing Endless looping, multi-shot, and one-shot generation modes Hardware re-triggerable operation for burst pattern generation Internal software triggering and external TTL trigger input Selectable trigger edge (rising or falling) Typical Applications Arbitrary waveform generation for complex signal synthesis Digital pattern simulation and validation Pulse-echo and radar signal generation System clock and timing signal generation Control signal sequencing Compatibility & Integration ISA bus interface 68-pin MDR connector (3M p/n 10268-55H3VC)

COMPUGEN3250 Characteristics / Specifications

PARAMETER	VALUE
Model	CompuGen 3250
Instrument Type	ISA digital pattern generator
Family	GaGe / DynamicSignals CompuGen 3250
Clock Rate	50 MHz maximum (20 ns per sample)
Word Width	32 bit
Memory Depth	256K to 8M samples, each sample a 32-bit word
Timebase	on-board crystal-controlled oscillator
Output Drivers	four 74FCT162245 in parallel, TTL/CMOS
Output Drive	0 to 5 V into a 50 ohm load
Looping	one-shot, multi-shot or endless looping of patterns
Internal Trigger	software trigger via on-board register, single or periodic
External Trigger	TTL external trigger
External Clock	factory option; maximum at or below 50 MHz; 0 to 5 V CMOS/TTL
Multi Card	up to 256-bit output using an 8-card master/slave system
Software	GageBit pattern editor; SDKs for C/C++, MATLAB and LabVIEW
Bus	ISA
Related Capture	CompuScope 3200 32-bit 100 MS/s digital input sibling
Manufacturer	GaGe Applied Technologies / DynamicSignals (Vitrekt)
Literature	GaGe CompuGen 3250 hardware manual / product introduction
System Role	digital test-vector source
Form Factor	ISA digital I/O card
Cable Class	CG3250 flat pleated-foil 50 ohm class cable listings
Pattern File	GaGe .SIG binary pattern format
Use Case	ASIC stimulus with CS3200 response capture
Clock Source	internal crystal or optional external clock

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes

From GaGe CompuGen 3250 hardware manual (50 MHz, 32-bit, 256K-8M x32, 74FCT162245 into 50 ohm). Exact installed memory depth is the dash/option on the card; do not assume 8M on every unit.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.