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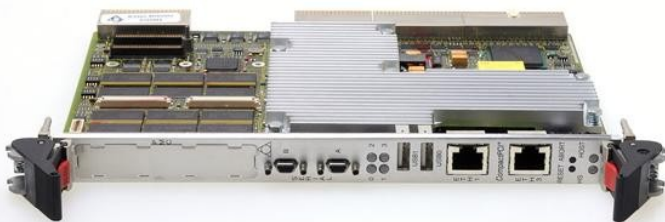
Data Sheet

August 15, 2026

EMERSON / MOTOROLA / FORCE COMPUTERS

Emerson / Motorola / Force Computers SPARC/CPCI-550/1024-650-L512-8/IPMI Single Board Computer

The Emerson/Motorola/Force Computers SPARC/CPCI-550/1024-650-L512-8/IPMI is a CompactPCI single board computer built on the UltraSPARC platform for telecommunications, industrial control, and military applications. It delivers computing in a cPCI form factor with integrated IPMI support for remote system management and monitoring across enterprise deployments. PCI-to-PCI Bridge (PPB) technology enables flexible I/O expansion and universal slot operation - boards function as either system or peripheral modules within the cPCI backplane. Dual 64-bit PCI buses support independent concurrent operations and throughput scaling. IPMI implementation provides out-of-band monitoring, alerting, and remote management for distributed systems.



MODEL

**Emerson /
Motorola / Force
Computers
SPARC/CPCI-
550/1024-650-
L512-8/IPMI**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

**SPARC/CPCI-
550/1024-650-
L512-8/IPMI**

LISTING

<https://aumictechlabs.com/products/sparc-cpc-i-550-1024-650-l512-8-ipmi-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Emerson/Motorola/Force Computers SPARC/CPCI-550/1024-650-L512-8/IPMI is a CompactPCI single board computer built on the UltraSPARC platform for telecommunications, industrial control, and military applications. It delivers computing in a cPCI form factor with integrated IPMI support for remote system management and monitoring across enterprise deployments. Technical Specifications Processor: UltraSPARC-IIi at 650 MHz System Memory: 1 GB SDRAM Cache: 512 MB L2 Flash Storage: 8 MB Bus Architecture: Dual independent 64-bit PCI buses Form Factor: CompactPCI (cPCI) Management Interface: IPMI support Key Features PCI-to-PCI Bridge (PPB) technology enables flexible I/O expansion and universal slot operation - boards function as either system or peripheral modules within the cPCI backplane. Dual 64-bit PCI buses support independent concurrent operations and throughput scaling. PMC expansion sites accept mezzanine cards operating at 33 MHz or 66 MHz. IPMI implementation provides out-of-band monitoring, alerting, and remote management for distributed systems. Typical Applications 3G wireless infrastructure and switch-fabric systems VoIP and voice communications platforms Telecommunications and data communications switching Industrial automation and robotics control Military and aerospace embedded systems Compatibility & Integration Designed for CompactPCI Packet-Switching Backplane (cPSB) systems. Follows Sun Microsystems' embedded processor roadmap, offering migration pathways for power-optimized upgrades. Supports potential legacy integration with VMEbus infrastructure where transition strategies are required.

SPARCCPCI5501024650L5128IPMI Characteristics / Specifications

PARAMETER	VALUE
Model Family	SPARC/CPCI-550
Example Configuration	SPARC/CPCI-550/1024-650-L512-8/IPMI
Instrument Type	CompactPCI single board computer
Architecture	SPARC
Form Factor	6U CompactPCI
Bus Standard	PICMG 2.0 CompactPCI
Cpu Speed Class From Config	650 MHz
Memory Class From Config	1024 MB
Local Resource Class From Config	512 MB
System Management	IPMI
Manufacturer	Force Computers Motorola Emerson
Role	system slot SBC
Hot Swap	CompactPCI hot swap class
Ethernet Class	onboard Ethernet
Serial Console Class	RS-232
Flash	onboard boot Flash
Pmc Expansion Class	PMC sites on board
Operating Systems Class	Solaris or embedded SPARC OS class
Cooling	forced air CPCI chassis
Power Rails	CPCI 3.3 V 5 V
Temperature Class	central office
Humidity Class	non condensing
Firmware	OpenBoot class
Watchdog	hardware watchdog class
Humidity	Class: non condensing

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Model Family	SPARC/CPCI-550
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Architecture	SPARC
Form Factor	6U CompactPCI
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Cpu Speed Class From Config	650 MHz
Memory Class From Config	1024 MB
Local Resource Class From Config	512 MB
System Management	IPMI
Manufacturer	Force Computers Motorola Emerson
Role	system slot SBC
Hot Swap	CompactPCI hot swap class
Ethernet Class	onboard Ethernet
Serial Console Class	RS-232
Flash	onboard boot Flash
Pmc Expansion Class	PMC sites on board
Operating Systems Class	Solaris or embedded SPARC OS class
Cooling	forced air CPCI chassis
Power Rails	CPCI 3.3 V 5 V
Temperature Class	central office
Humidity Class	non condensing
Firmware	OpenBoot class
Watchdog	hardware watchdog class

Notes

Configuration tokens parsed from inventory string SPARC/CPCI-550/1024-650-L512-8/IPMI. Confirm exact CPU stepping and memory map on the Force/Motorola configuration label.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

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30-day returns
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Generated August 15, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.