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Data Sheet

August 14, 2026

HP / AGILENT

HP / Agilent 81134A Pulse Pattern Generator, 3.35 GHz, dual-channel

Pattern memory: 8 kbit per channel (12 Mbit extended) PRBS polynomials: 2^n-1 for $n = 5$ to 15, 23, 31 Delay control modulation frequency: 0 Hz to 200 MHz Values are from Agilent/Keysight 81133A/81134A Pulse Pattern Generator data sheets. Channel 2 provides the dual-channel capability that distinguishes the 81134A from the single-channel 81133A. PRBS polynomials: 2^n-1 for $n = 5$ to 15, 23, 31 Delay control modulation frequency: 0 Hz to 200 MHz Values are from Agilent/Keysight 81133A/81134A Pulse Pattern Generator data sheets. Channel 2 provides the dual-channel capability that distinguishes the 81134A from the single-channel 81133A.



MODEL

**HP / Agilent
81134A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

81134A

LISTING

<https://aumictechlabs.com/products/81134a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The HP/Agilent 81134A is a dual-channel pulse pattern generator delivering high-speed signal generation from 15 MHz to 3.35 GHz. Engineered for characterization of high-speed logic devices and digital systems, this instrument generates complex data patterns with exceptional timing precision and signal integrity. It supports multiple pulse generation modes - pulse, square clock, data, and PRBS sequences up to $2^{31}-1$ - with 12 Mbit pattern memory per channel and burst capability. Technical Specifications Signal Generation Frequency range: 15 MHz to 3.35 GHz Data formats: NRZ, RZ, R1 Pulse modes: Pulse, Square (Clock), Data, PRBS (2^5-1 to $2^{31}-1$) Pattern

memory: 12 Mbit per channel Transition time (20% to 80%): <60 ps RMS jitter: Typically <2 ps Output amplitude: 50 mV to 2.00 V Output impedance: 50 Ω nominal Differential outputs with <10% overshoot/ringing (± 10 mV) Electrical Characteristics Level window: -2.00 V to +3.00 V Level resolution: 10 mV Level accuracy: 2% of setting ± 20 mV Settling time: 1 ns Variable crossover: 30% to 70% typical Short circuit current: -80 mA to 120 mA nominal Clocking and Synchronization Internal clock: 15 MHz to 3.35 GHz Clock accuracy: 50 ppm Frequency divider: 1, 2, 4, 8, 16, 32, 64, 128 External clock input: 10 MHz reference supported Delay range: -5 ns to 230 ns Key Features Dual-channel differential outputs for balanced signal delivery Jitter emulation up to ± 250 ps via delay control input Trigger output, clock input, and start input connectors Frequency divider for flexible clock scaling Compact footprint operating at 100–240 V AC (47–63 Hz), <200 VA maximum Typical Applications High-speed digital circuit testing, logic device characterization, signal integrity analysis, PRBS pattern generation for data transmission systems, and burst-mode signal generation for transient analysis. Compatibility & Integration Remote control via GPIB, LAN, and USB 2.0. Operates across 0–55 °C ambient temperature at altitudes up to 2000 meters.

81134A Characteristics / Specifications

PARAMETER	VALUE
Number of channels	2
Frequency range	15 MHz to 3.35 GHz
Period range	298.5 ps to 66.6 ns
Period resolution	6 digits, 0.001 ps best case
Frequency resolution	1 Hz
Frequency accuracy	50 ppm
Transition times 10% to 90%	less than 90 ps
Transition times 20% to 80%	less than 60 ps
Random jitter clock mode	less than 4 ps RMS (1.5 ps typical)
Total jitter data mode	less than 5 ps RMS (2 ps typical)
Delay variation	–5 ns to 230 ns
Delay resolution	1 ps
Delay accuracy	±20 ps
Skew between differential outputs	less than 20 ps nominal
Maximum skew range	±10 ns
Width range	100 ps to (period – 100 ps)
Width resolution	1 ps
Width accuracy	±40 ps
Duty cycle range	0.15% to 99.85%
Amplitude	50 mV to 2.00 V
Level window	–2.00 V to +3.00 V
Level resolution	10 mV
Level accuracy	2% of setting ±20 mV
Settling time	1 ns
Source impedance	50 ohm nominal

PARAMETER	VALUE
Pattern memory	8 kbit per channel (12 Mbit extended)
Data formats	RZ, NRZ, R1
PRBS polynomials	2^n-1 for $n = 5$ to $15, 23, 31$
Jitter emulation	up to ± 250 ps
Delay control modulation frequency	0 Hz to 200 MHz
Remote interfaces	GPIB, LAN, USB 2.0
Operating temperature	0 °C to 55 °C

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	0 °C to 55 °C
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Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
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PRBS polynomials	$2^n - 1$ for $n = 5$ to $15, 23, 31$
Jitter emulation	up to ± 250 ps
Delay control modulation frequency	0 Hz to 200 MHz
Remote interfaces	GPIO, LAN, USB 2.0
Operating temperature	0 °C to 55 °C

Notes

Values are from Agilent/Keysight 81133A/81134A Pulse Pattern Generator data sheets. Channel 2 provides the dual-channel capability that distinguishes the 81134A from the single-channel 81133A.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.