

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

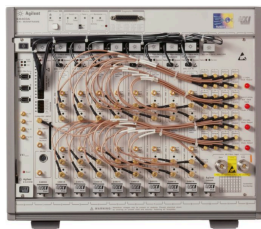
Data Sheet

August 13, 2026

HP / AGILENT

HP / Agilent 81250 13.5 Gb/s ParBERT Modules (N4872A, N4873A)

The HP/Agilent 81250 is a ParBERT (Parallel Bit Error Rate Tester) module capable of testing at speeds up to 13.5 Gb/s. It consists of the N4872A transmitter and N4873A receiver, designed for high-speed digital communication testing and characterization.



MODEL

**HP / Agilent
N4872A / N4873A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

N4872A / N4873A

LISTING

<https://aumictechlabs.com/products/n4872a-n4873a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The HP/Agilent 81250 ParBERT N4872A and N4873A modules deliver integrated transmit and receive capability for high-speed serial link characterization up to 13.5 Gb/s. The N4872A generator produces differential and single-ended NRZ signals with output amplitude 0.1 Vpp to 1.8 Vpp and transition times below 20 ps. The N4873A analyzer detects signals with 50 mVpp input sensitivity and 25 ps minimum pulse width resolution. Together, these VXI modules enable BERT testing of SerDes devices, multiplexers, and communication standards across the 620 Mb/s to 13.5 Gb/s range. Technical Specifications N4872A Generator Module Data rate: 620 Mb/s to 13.5 Gb/s Output amplitude: 0.1 Vpp to 1.8 Vpp (5 mV resolution) Transition time (20%-80%): <20 ps Jitter: <9 ps peak-to-peak (typical) Jitter modulation bandwidth: DC to 1 GHz External clock input: 620 MHz to 13.5 GHz Crosspoint adjust: 20% to 80% Delay line: voltage controllable, up to 200 ps

peak-to-peak jitter N4873A Analyzer Module Data rate: 620 Mb/s to 13.5 Gb/s Minimum detectable pulse width: 25 ps (typical) Input sensitivity: 50 mVpp typical at 10 Gb/s PRBS $2^{31}-1$, BER 10^{-12} Phase margin: 1 UI – 12 ps (typical) Key Features Generates PRBS patterns up to $2^{31}-1$ and user-defined patterns to 64 Mb Three synchronization methods: bit-by-bit shift, word detect, adjustable sampling delay to 10 ns Pattern sequencer with looping and event branching Supports parallel-to-serial, serial-to-parallel, and serial-to-serial test configurations Modular design fits single VXI slot per module Typical Applications SerDes device characterization and stress testing Multiplexer and demultiplexer validation Communication standard compliance testing High-speed signal integrity analysis Compatibility & Integration Operates as VXI modules within the 81250 ParBERT platform, which supports data rates from 675 Mb/s to 45 Gb/s system-wide.

Features & description

From manufacturer datasheet

Features

- Data rate 620 Mb/s to 13.5 Gb/s • Differential or single-ended data and clock outputs (2.4 mm female) • PRBS to $2^{31}-1$, PRWS, 64 Mbit pattern memory • One VXI slot; requires E4809A (2 VXI slots)
- Delay Control IN for delay modulation to 1 GHz / 200 ps • Drives LVDS, CML, PECL, ECL, low-voltage CMOS • Companion N4873A analyzer module

N4872AN4873A Characteristics / Specifications

PARAMETER	VALUE
Model	N4872A
Manufacturer	Agilent / Keysight
Instrument Type	ParBERT 13.5 Gb/s data generator module
Alias	N4872A; Agilent N4872A; Keysight N4872A
Data Rate Range	620 Mb/s to 13.5 Gb/s
Clock Output Frequency	620 MHz to 13.5 GHz
Connectors	2.4 mm (f), differential or single-ended
Output Impedance	50 ohm typical
Data Amplitude / Resolution	0.1 Vpp to 1.8 Vpp / 5 mV
Data Output Voltage Window	-2.00 V to +3.00 V
Clock Output Amplitude / Resolution	0.1 Vpp to 1.8 Vpp / 5 mV
Clock Output Voltage Window	-2.00 V to +2.80 V
Short Circuit Current	72 mA max
External Termination Voltage	-2 V to +3 V
Data Formats	NRZ, DNRZ
Pattern Memory	64 Mbit
PRBS	Hardware-based up to 2 ³¹ -1
VXI Slot Width	1
Required Clock Module	E4809A (2 VXI slots)
Recommended Cable Set	N4910A 24 in matched pair 2.4 mm
Delay Control Input	Single-ended 50 ohm; modulate delay to 1 GHz (200 ps)
Companion Analyzer	N4873A
System Family	81250 ParBERT
Data Rate	Range: 620 Mb/s to 13.5 Gb/s

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	N4872A
Manufacturer	Agilent / Keysight
Instrument Type	ParBERT 13.5 Gb/s data generator module
Alias	N4872A; Agilent N4872A; Keysight N4872A
Data Rate Range	620 Mb/s to 13.5 Gb/s
Clock Output Frequency	620 MHz to 13.5 GHz
Connectors	2.4 mm (f), differential or single-ended
Output Impedance	50 ohm typical
Data Amplitude / Resolution	0.1 Vpp to 1.8 Vpp / 5 mV
Data Output Voltage Window	-2.00 V to +3.00 V
Clock Output Amplitude / Resolution	0.1 Vpp to 1.8 Vpp / 5 mV
Clock Output Voltage Window	-2.00 V to +2.80 V
Short Circuit Current	72 mA max
External Termination Voltage	-2 V to +3 V
Data Formats	NRZ, DNRZ
Transition Time 20-80%: less than 20 ps	
Transition Time 10-90% Class: less than 25 ps	
Specifications	
PARAMETER	VALUE
Pattern Memory	64 Mbit
PRBS	Hardware-based up to 2^31-1
VXI Slot Width	1
Required Clock Module	E4809A (2 VXI slots)
Recommended Cable Set	N4910A 24 in matched pair 2.4 mm
Delay Control Input	Single-ended 50 ohm; modulate delay to 1 GHz (200 ps)
Companion Analyzer	N4873A

PARAMETER	VALUE
System Family	81250 ParBERT

Notes
Specs from Agilent ParBERT 13.5 Gb/s module datasheet (5988-9202 / 5968-9188E class). Confirm E4809A, N4910A cables, and termination voltage settings on the installed system.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 13, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.