

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

Data Sheet

August 13, 2026

INTEL / WIND RIVER / EST

Intel / Wind River / EST VisionICE II PowerPC MIPS JTAG Emulator

The VisionICE II is a JTAG emulator for PowerPC and MIPS embedded systems development, delivering hardware-assisted on-chip debugging integrated with Wind River's Tornado environment. The tool combines a high-speed Ethernet host interface with USB 2.0 connectivity and JTAG accelerator technology to enable real-time trace, breakpoints, memory access, and register control. Performance is optimized through hardware logic that caches common JTAG scan chains, delivering download speeds and debug throughput 4 to 10 times faster than comparable products. Compatibility & Integration Supported Architectures: PowerPC and MIPS processors including MIPS32 variants (4Kc, 4Kp, 4Km, 4KEc), Broadcom BCM series (BCM1100/1101, BCM3350/3351/3352, BCM4710, BCM7100), IDT RC series (RC32332/334/364/355), Philips PNX series (PNX852X, PNX832X), and Alchemy Semiconductor AU1000. Optional support available for A

**MODEL****Intel / Wind River /
EST VisionICE II****CALIBRATION****Available on
request****CONDITION****Used****STATUS****In stock****RFQ / SKU****VisionICE II****LISTING**<https://aumictechlabs.com/products/visionice-i-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The VisionICE II is a JTAG emulator for PowerPC and MIPS embedded systems development, delivering hardware-assisted on-chip debugging integrated with Wind River's Tornado

environment. The tool combines a high-speed Ethernet host interface with USB 2.0 connectivity and JTAG accelerator technology to enable real-time trace, breakpoints, memory access, and register control. Performance is optimized through hardware logic that caches common JTAG scan chains, delivering download speeds and debug throughput 4 to 10 times faster than comparable products.

Technical Specifications

Core Architecture: Hardware-assisted debugger utilizing on-chip debugging services embedded in target microprocessors

Host Connectivity: High-speed Ethernet connection to target; USB 2.0 with JTAG accelerator technology

Performance Gain: 4 to 10 times faster download and debug throughput versus competitive products

Software Integration: Bundled debugger provides fully integrated hardware/software debugging solution

Key Features

- Real-time trace and execution monitoring
- Internal and external hardware/software breakpoints
- Source-level debugging with memory and register observation
- On-chip debug target control: start/stop, single-step, step over/out, reset, and snapshot capture
- Target memory read/write access
- Large-scale register control with bit-level configuration utilities

Typical Applications

- Board bring-up and system-level diagnostics
- Crash diagnosis and failure analysis
- Embedded software development and validation
- Real-time system debugging and optimization

Compatibility & Integration

Supported Architectures: PowerPC and MIPS processors including MIPS32 variants (4Kc, 4Kp, 4Km, 4KEc), Broadcom BCM series (BCM1100/1101, BCM3350/3351/3352, BCM4710, BCM7100), IDT RC series (RC32332/334/364/355), Philips PNX series (PNX852X, PNX832X), and Alchemy Semiconductor AU1000. Optional support available for ARM, ColdFire, Intel, and XScale targets through specific configurations.

Development Environment: Native integration with Wind River Tornado toolchain for direct JTAG communication. Published APIs enable integration with other embedded operating systems.

Features & description

From manufacturer datasheet

Features

- Hardware-assisted JTAG emulator (VisionICE II)

VISIONICEII Characteristics / Specifications

PARAMETER	VALUE
Model	VisionICE II
Manufacturer	Wind River Systems
Instrument Type	Hardware-assisted JTAG / run-control emulator
Alias	VisionICE II; VisionICE 2
Target Architectures	PowerPC and MIPS families
Debug Interface	JTAG (target probe)
Primary Functions	Run-control; breakpoints; memory/register access; flash programming
Host Software Class	Wind River Tornado / Workbench / VisionICE tool chain
Use Cases	Board bring-up; BSP debug; flash load
Connection	Probe cable to target JTAG header (pinout per CPU family)
Operating Mode	Hardware-assisted debug (notasoftware-only stub)
Product Line	VisionICE series (II generation)
Form	Emulator box / probe system
Development Role	Embedded microprocessor debug and programming
Related Ecosystem	VxWorks / Wind River embedded platforms
Identification	VisionICE II labeling on emulator and probe
Replacement Rule	Match CPU family probe adapter and host software revision Notes Public numeric electrical tables are sparse for VisionICE II beyond architecture and function. Confirm PowerPC vs MIPS probe adapter, JTAG pinout, and host software license/version before quoting capability.
Run-control	run, stop, step, breakpoints

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	VisionICE II
Manufacturer	Wind River Systems
Instrument Type	Hardware-assisted JTAG / run-control emulator
Specifications	
PARAMETER	VALUE
Alias	VisionICE II; VisionICE 2
Target Architectures	PowerPC; MIPS
Debug Interface	JTAG (target probe)
Specifications	
PARAMETER	VALUE
Primary Functions	Run-control; breakpoints; memory/register access; flash programming
Host Software Class	Wind River Tornado / Workbench / VisionICE tool chain
Use Cases	Board bring-up; BSP debug; flash load
Specifications	
PARAMETER	VALUE
Connection	Probe cable to target JTAG header (pinout per CPU family)
Operating Mode	Hardware-assisted debug (notasoftware-only stub)
Product Line	VisionICE series (II generation)
Specifications	
PARAMETER	VALUE
Form	Emulator box / probe system
Development Role	Embedded microprocessor debug and programming
Related Ecosystem	VxWorks / Wind River embedded platforms

Identification: VisionICE II labeling on emulator and probe

Replacement Rule: Match CPU family probe adapter and host software revision

Notes

Public numeric electrical tables are sparse for VisionICE II beyond architecture and function. Confirm PowerPC vs MIPS probe adapter, JTAG pinout, and host software license/version before quoting capability.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
NAICS 423690 · 811210 · 541380
Generated August 13, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.