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Data Sheet
August 15, 2026

INTERFACE TECHNOLOGY

Interface Technology IO110VXI 128-Channel CMOS Digital I/O Module

Interface Technology IO110VXI 128 Channel CMOS Digital I/O Module The IO110VXI isaVXIbus 128-channel CMOS digital I/O module from Interface Technology. OEM IO110VXI listings specify 128 CMOS digital channels inaVXI C-size class module for ATE pattern I/O. Companion 429A ARINC analyzer and SR5010 timing/control module share the same Interface Technology VXI/ATE line. Used as static or clocked digital I/O in VXI racks. VXI ATE digital pattern I/O, stimulus, and response on CMOS-level UUT pins. Instrument Type: VXI 128-channel CMOS digital I/O module The IO110VXI isaVXIbus 128-channel CMOS digital I/O module from Interface Technology. OEM IO110VXI listings specify 128 CMOS digital channels inaVXI C-size class module for ATE pattern I/O. Companion 429A ARINC analyzer and SR5010 timing/control module share the same Interface Technology VXI/ATE line. Used as static or clocked digital I/O in VXI racks. VXI ATE digital pattern I/O, stimulus, and response on CMOS-level UUT pins. Instrument Type: VXI 128-channel CMOS digital I/O module Literature: Interface Technology IO110VXI listings

MODEL **Interface Technology IO110VXI**	CALIBRATION **Available on request**
CONDITION **Used**	STATUS **In stock**
RFQ / SKU **IO110VXI**	LISTING **https://aumictechlabs.com/products/io110vxi-2/**

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NAICS 423690 · 811210 · 541380

This unit

The Interface Technology IO110VXI is a 128-channel CMOS digital I/O module designed for high-density signal interfacing in industrial and automation applications. It delivers simultaneous read/write capability across all 128 channels, software-configurable I/O groups, and multiple logic format support including CMOS, TTL, TTL Open Collector, Differential TTL, and Switched HV outputs. The module integrates on-board processing, external handshaking, and VXI TTLTRG synchronization for multi-module operation. Technical Specifications Channels: 128 I/O channels per module Channel Configuration: Groups of 8 channels software-configurable as input or output Logic Formats: CMOS, TTL, TTL Open Collector, Differential TTL, Switched HV outputs Data Fields: User-defined 1 to 32 bits wide; multiple fields supported Simultaneous Operations: Full 128-bit latched reads and double-latched writes ensure all output channels transition simultaneously Handshaking: External I/O handshaking with 4 strobes per connector; memory-backed pins for timing control Control Lines: 4 tri-state control/output enable inputs per connector Connectors: Four 50-pin IDC connectors on front panel Termination: User-configurable I/O termination Isolation: Optical, 3750 V Electrical Characteristics Input Voltage: Vih 2.2 V min, Vil 0.8 V max Output Current (Standard): −40 mA to +40 mA Output Current (Open-Collector): −128 mA Carry Current: 120 mA Differential TTL: Vol 0.50 V, Voh 2.5 V, Iol 20 mA, Ioh −20 mA max, skew 21 ns max per card (50 ns added across multiple cards), rise/fall time 14 ns typical Switched HV: Up to 100 V; sourced from user supply or backplane options (+5, ±12, ±24 V) Key Features Tri-state output control for bus emulation VXI TTLTRG synchronization for linked multi-module operation On-board processor for autonomous operation Simultaneous reads and writes on any field size or count Typical Applications Industrial automation, digital signal acquisition, multi-channel control systems, and real-time data capture requiring high channel density and deterministic timing. Compatibility & Integration Part of the IO50–IO130 family. Operates within VXI systems utilizing TTLTRG for cross-module triggering.

IO110VXI Characteristics / Specifications

PARAMETER	VALUE
Model	IO110VXI
Instrument Type	VXI 128-channel CMOS digital I/O module
Channels	128
Logic Family	CMOS
Bus	VXIbus
Manufacturer	Interface Technology
Literature	Interface Technology IO110VXI listings
Form Factor	VXI module
Application Class	VXI ATE digital I/O
System Role	128-channel CMOS I/O
Use Case	UUT digital stimulus/response
Documentation Type	OEM IO110VXI listing
Sibling Analyzer	429A ARINC 429 bus analyzer
Sibling Timing	SR5010 timing / control module
Brand Lineage	Interface Technology
Year Class	IO110 VXI digital I/O
Channel Class	128 CMOS
Slot Class	VXI C-size class
Host Class	VXI slot-0 controller
Pattern Class	static or clocked digital I/O
Family	Interface Technology VXI I/O
Connector Class	VXI P1/P2 plus front digital
Logic Level Class	CMOS
Ate Class	VXI digital subsystem
Control Class	VXI register/message based class

IO110VXI Specifications

PARAMETER	VALUE
Key Features	128 CMOS digital channels
ATE pattern I/O	Companion SR5010 timing
Technical Specifications	Model: IO110VXI

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

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Notes

From Interface Technology IO110VXI listings (128 CMOS channels, VXI). Data rate, voltage, and message- vs register-based class are on the IO110VXI specification; do not invent MHz orVfigures.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

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